

# **CRAY C90™ Series LME System Monitor Utility**

**HDM-120-0**

Cray Research Proprietary

CRAY C90 Series LME SMON  
-120-

---

**Cray Research, Inc.**

---

# Record of Revision

---

REVISION	DESCRIPTION
----------	-------------

---

	April 1995. Original printing.
--	--------------------------------

---

Any shipment to a country outside of the United States requires a letter of assurance from Cray Research, Inc.

---

This document is the property of Cray Research, Inc. The use of this document is subject to specific license rights extended by Cray Research, Inc. to the owner or lessee of a Cray Research, Inc. computer system or other licensed party according to the terms and conditions of the license and for no other purpose.

---

Cray Research, Inc. Unpublished Proprietary Information — All Rights Reserved.

---

Autotasking, CF77, CRAY, CRAY-1, Cray Ada, CraySoft, CRAY Y-MP, HSX, MPP Apprentice, SSD, UniChem, UNICOS, and X-MP EA are federally registered trademarks and Because no workstation is an island, CCI, CCMT, CF90, CFT, CFT2, CFT77, ConCurrent Maintenance Tools, COS, CRAY-2, Cray Animation Theater, CRAY C90, CRAY C90D, Cray C++ Compiling System, CrayDoc, CRAY EL, CRAY J90, Cray NQS, Cray/REELlibrarian, CRAY T3D, CRAY T90, CrayTutor, CRAY X-MP, CRAY XMS, CRInform, CRI/TurboKiva, CSIM, CVT, Delivering the power . . ., DGauss, Docview, EMDS, HEXAR, IOS, LibSci, ND Series Network Disk Array, Network Queuing Environment, Network Queuing Tools, OLNET, RQS, SEGLDR, SMARTE, SUPERCLUSTER, SUPERLINK, System Maintenance and Remote Testing Environment, Trusted UNICOS, and UNICOS MAX are trademarks of Cray Research, Inc.

---

Requests for copies of Cray Research, Inc. publications should be directed to:

CRAY RESEARCH, INC.  
Logistics  
6251 South Prairie View Road  
Chippewa Falls, WI 54729

---

Comments about this publication should be directed to:

CRAY RESEARCH, INC.  
Hardware Publications and Training  
890 Industrial Blvd.  
Chippewa Falls, WI 54729

---

# CRAY C90 SERIES LME SYSTEM MONITOR UTILITY

Description of this Document .....	5
Notational Conventions .....	5
What Is the System Monitor Utility? .....	6
What Are Trigger Conditions? .....	7
What Is the Default Triggers Condition? .....	7
What Are the Other Predefined Trigger Conditions? .....	10
What Is a User-defined Trigger Condition? .....	11
What Is the Hung CPU Check Feature? .....	11
What Software Must I Install to Use SMON? .....	12
What Are the Potential Risks of Using SMON? .....	13
How Do I Use SMON? .....	13
How Do I Start SMON? .....	15
From the OpenWindows Workspace Menu .....	16
From a UNIX Command Prompt .....	16
How Do I Set Up the Default Triggers Condition? .....	18
How Do I Set Up a Predefined Trigger Condition? .....	20
How Do I Set Up a User-defined Trigger Condition? .....	22
How Do I Enable the Hung CPU Check Feature? .....	24
How Do I Make SMON Start Monitoring the CPUs? .....	25
What Should I Do While SMON Monitors for Triggers and Hung CPUs? .....	26
What Messages Will I See in the Log? .....	26
How Do I Determine if a Trigger Occurred? .....	28
What Happens When a Trigger Occurs? .....	28
How Do I View the Output File? .....	29
How Do I Determine if a Hung CPU Was Detected? .....	29
What Happens When SMON Detects a Hung CPU? .....	29
How Do I View the CRASH_X Output? .....	30
What Should I Do if the Mainframe Crashes? .....	30
How Do I Quit SMON? .....	30
How Can I Test SMON? .....	31

How Does SMON Work? .....	31
Start .....	33
Write DM Parameters to All CPUs .....	33
Read DM Status for One CPU .....	33
DM Triggered? .....	34
Create an Output File .....	34
Dump CPU and System Status Information into the Output File .....	35
Dump DM Parameters and DM Buffer into the Output File ..	35
Perform TP Dump .....	35
Default Triggers Selected? .....	35
Dump Instruction Buffers and CPU Registers? .....	35
Hardware Trigger? .....	36
Find Failing Exchange Package in Memory .....	36
Dump Failing Exchange Package to Output File .....	37
Perform Instruction Buffer Dump .....	37
Write Register Dump Code to Memory .....	38
Deadstart CPU to Execute Register Dump Code .....	38
Dump CPU Registers to the Output File .....	38
Read Expected Instruction Buffer Data from Memory .....	38
Dump Instruction Buffer Data and Expected Instruction Buffer Data to the Output File .....	39
Dump Current Exchange Package to the Output File .....	39
Dump Restart Exchange Package to the Output File .....	39
Dump Memory (by Using the A Registers) to the Output File	39
Hold Issue after Dump? .....	40
Write Original DM Parameters .....	40
Write DM Parameters to Force Hold Issue on Next Clock Period .....	40
Deadstart CPU Using Restart Exchange Package .....	40
Dump TP Dump Data to the Output File .....	40
Run .smon_trigger_cmd Script .....	40
Hold Issue Mode? .....	41
SMON Stops Monitoring the CPU; the CPU Is Holding Issue	41
CPU Triggered 5 Times in 1 Minute? .....	41

Reset DM .....	41
SMON Stops Monitoring the CPU; the CPU Is Executing Instructions .....	41
More Than 32 Triggers from All CPUs in 5 Minutes? .....	42
Reset DMs on All CPUs .....	42
SMON Stops Monitoring All CPUs; All CPUs Are Executing Instructions .....	42
Hung CPU Interval Expired? .....	42
Stop and Read DM Data .....	42
Valid Data in DM Buffer .....	42
Mark CPU as Hung .....	43
All CPUs Checked? .....	43
Hung CPU Detected? .....	43
Delay 0.5 Seconds .....	43
Run CRASH_X .....	43
Write Original DM Parameters to All CPUs That Were Active before CRASH_X Was Run .....	43
Run .smon_hang_cmd Script .....	44
How Do I Interpret the SMON Output File? .....	45
Examples of Interpreting the Output .....	45
ORE Interrupt in OS Kernel .....	46
Software Trap in User Code .....	51
Tips for Using SMON to Detect Uncorrectable Memory Errors ..	56

---

**APPENDIX** **57**

ORE Interrupt in the OS Kernel .....	57
Software Trap in the User Code .....	82

**Figures**

---

Figure 1.	SMON Window .....	6
Figure 2.	Using SMON Procedure (Flowchart) .....	14
Figure 3.	SMON Started Message .....	25
Figure 4.	System Monitor Activity Log .....	26
Figure 5.	CPU Triggered Message .....	28
Figure 6.	Hung CPU Detected Message .....	29
Figure 7.	C Program You Can Use to Test SMON .....	31
Figure 8.	SMON Functions Flowchart .....	32
Figure 9.	ORE Interrupt Failure Analysis Information .....	46
Figure 10.	ORE Interrupt Occurred .....	47
Figure 11.	Most Recent Memory Reference Instruction .....	48
Figure 12.	Instruction Issued from Instruction Buffer 7 .....	49
Figure 13.	Instruction Buffer 7 Contents .....	49
Figure 14.	Data Limit Address .....	50
Figure 15.	User Code Software Trap Failure Analysis Information .....	51
Figure 16.	User Source Code .....	52
Figure 17.	Failing Condition in Element 15 of Vector Register 7 .....	53
Figure 18.	046057 Instruction in the DM Buffer .....	54
Figure 19.	Failing Exchange Package .....	55

**Tables**

---

Table 1.	UNICOS Hardware Trigger Sequences .....	9
Table 2.	UNICOS Software Trigger Sequences .....	9
Table 3.	Offline Diagnostic Hardware Trigger Sequences ...	10
Table 4.	Offline Diagnostic Software Trigger Sequences ...	10
Table 5.	Kernel Mods .....	12
Table 6.	Command Line Options .....	16
Table 7.	Kernel and User Job Trigger Action Options .....	18
Table 8.	Trigger Conditions .....	20
Table 9.	Trigger Action Options .....	21
Table 10.	User-defined Trigger Action Options .....	23
Table 11.	Exchange Package Placement .....	37


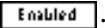
## Description of this Document

---

This document describes the System Monitor (SMON) utility. SMON is used to automatically acquire CPU information during hardware and software failures. This document describes what SMON is, how you use SMON, and how SMON works. This document also provides examples that show how to interpret SMON output files.

## Notational Conventions

This document uses the following notational conventions:

- Buttons are shown the way they appear in a window; for example, .
- Settings are shown the way they appear in a window; for example, .
- The `->` symbol indicates holding the MENU mouse button down and moving the mouse pointer to the next menu item.
- `Courier` type indicates a command you can enter and indicates references to the LME interface windows.
- **Courier bold** type indicates commands you should enter and indicates menu entries you should choose from the LME interface.
- All instructions and memory references are octal numbers.

## What Is the System Monitor Utility?

The System Monitor utility (SMON), shown in Figure 1, is an extension of the Logic Monitor Environment (LME) application. SMON automatically acquires CPU information during hardware or software failures by using the diagnostic monitor (DM) hardware of the CRAY C90 series computer system. The DM hardware is a maintenance feature, analogous to a logic analyzer, that is integrated into the hardware design of each CPU.

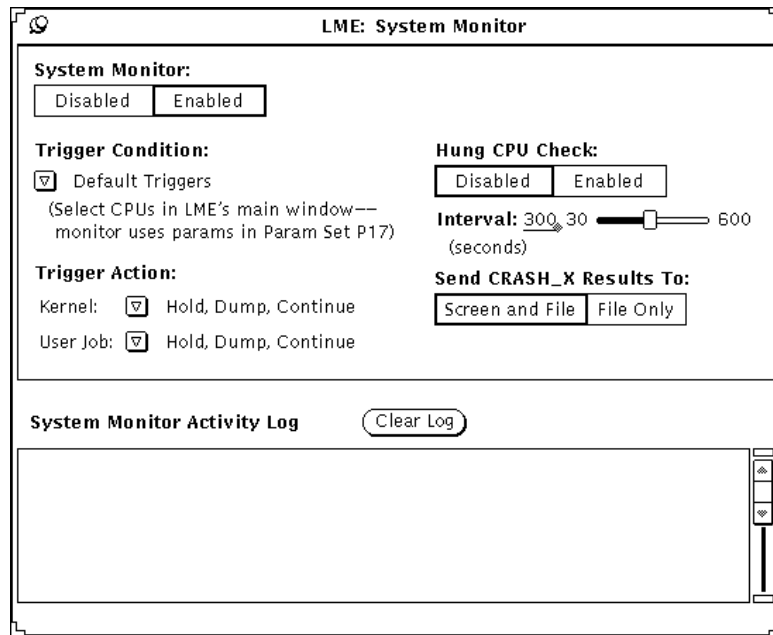


Figure 1. SMON Window

SMON programs each DM to continually record the CPU's program register (P register), current instruction parcel (CIP), and various status flags in its 64-bit by 64-element first-in-first-out (FIFO) DM buffer until a trigger occurs. SMON's Trigger Condition selection enables you to select from a list of predefined trigger conditions including Default Triggers, any current instruction parcel value (CIP value), any P register value, and various hardware flags.

After programming the DMs with the trigger conditions, SMON monitors the status of each CPU's DM. The Trigger Action selection determines SMON's reaction to sensing a DM trigger.

**NOTE:** SMON monitors only CPU modules; SMON does not monitor shared or shared I/O modules, which do not execute instructions.



SMON can also check for hung CPUs. To determine whether a CPU is hung, SMON stops the CPU's DM and examines the data recorded in the DM buffer. If there is no valid data in the DM buffer, which indicates that the CPU has not executed any instructions since the DM started recording, the CPU is hung. When SMON detects hung CPUs, SMON runs the CRASH\_X program to gather more information.

## What Are Trigger Conditions?

SMON programs the DMs to trigger, or stop recording, on the trigger conditions. Once a trigger condition occurs, the DM stops recording, and SMON creates an output file. Three types of trigger conditions are available:

- The default triggers condition
- Several trigger conditions defined by SMON, called *other predefined trigger conditions*
- User-defined trigger conditions that you create by using LME parameter sets

You can have only one trigger condition set at a time. Normally, you should use the default triggers condition, which causes SMON to collect the most data in the output file.

## What Is the Default Triggers Condition?

The default triggers condition causes a CPU's DM to trigger when the CPU issues a 002740, 002750, 002760, or 002770 instruction. These instructions were chosen because they are not defined by the Cray Assembly Language (CAL) and are normally not issued; the hardware decodes these instructions as 002700 (CMR) instructions and does not modify any CPU registers. These trigger instructions are placed in various error-trap routines so the DM triggers and captures program activity traces for a wide variety of online, offline, hardware, and software failures.

The trigger instruction is followed by two nonoperational (no-op) instructions. These no-op instructions are used to prevent problems in selecting the proper P register value. This P register value is used to restart the CPU after forcing the CPU to exchange in order to dump its instruction buffers and registers.

After SMON senses a DM has triggered, it reads the last two CIP values recorded in the DM buffer. The first of these CIP values should be the trigger instruction (002740, 002750, 002760, or 002770). The last CIP value should be either 002770 or 001000, depending on whether it is an offline failure or online failure, respectively. If SMON does not recognize these two CIP values as one of the eight defined default trigger sequences, SMON does not dump the instruction buffers or CPU registers, regardless of which trigger action was set.

For the `Default Triggers Action` setting `Hold`, `Dump`, `Continue`, SMON temporarily takes control of the CPU to dump its 64-element DM buffer, instruction buffers, failing exchange package, all CPU registers, and a 200<sub>8</sub> word block of mainframe memory for each A register [each block starts at the value contained in the corresponding A register ( $A_{xx} + DBA$ )]. After SMON finishes recording all of the available information, it rebuilds the CPU's exchange package and restarts the CPU. This enables the user job to core-dump or the kernel to panic normally.

**NOTE:** Currently, the UNICOS user hardware trigger option (002750) is not officially supported. Contact Hardware Product Support (HPS) for more information about this option.

## UNICOS Trigger Sequences

The UNICOS trigger sequences provide SMON the functionality to gather CPU information while UNICOS is running in the mainframe. The UNICOS trigger sequences are instruction sequences that are inserted into the kernel code or a user program.

Table 1 shows the UNICOS hardware trigger instruction sequences. These instruction sequences are strategically located in the kernel to execute after a hardware flag sets and an exchange occurs.

Table 1. UNICOS Hardware Trigger Sequences

Trigger Sequence	Failure Case
002750 001000 001000	<p>UNICOS user hardware (Placed in the CALLHOT path)</p> <p>Example: A user job takes an uncorrectable memory error and exchanges back into the kernel. A mask in the user mod defines which hardware interrupts will issue this trigger sequence.</p> <p><b>NOTE:</b> This trigger sequence requires the UNICOS slave kernel mod. Refer to Table 5 on page 12 for information about the slave kernel mod that must be installed to match the version of UNICOS that is installed on the mainframe.</p>
002770 001000 001000	<p>UNICOS kernel hardware (Placed at the beginning of the immtrap code)</p> <p>Example: The kernel takes an uncorrectable memory error, and an exchange to the immtrap code occurs.</p> <p><b>NOTE:</b> This trigger sequence requires the UNICOS master kernel mod. Refer to Table 5 on page 12 for information about the master kernel mod that must be installed to match the version of UNICOS that is installed on the mainframe.</p>

Table 2 shows the UNICOS software trigger instruction sequences. These instruction sequences are placed in the kernel or a user program to gather information after a failure. You must insert these instructions into the user code or a software trap.

Table 2. UNICOS Software Trigger Sequences

Trigger Sequence	Failure Case
002740 001000 001000	<p>UNICOS user software (Placed in the user code)</p> <p>Example: A user job discovers a miscompare and issues the trigger.</p> <p><b>NOTE:</b> By default, SMON will not perform dumps for any trigger instruction sequences that issue in user mode.</p>
002760 001000 001000	<p>UNICOS kernel software (Placed in software trap)</p> <p>This trigger sequence provides the flexibility to trigger on failures or events that do not exchange to the immtrap code.</p> <p>Example: A non-panic WARNING condition occurs in the kernel.</p>

Offline Diagnostic Trigger Sequences

The offline diagnostic trigger sequences provide SMON the functionality to gather CPU information while offline diagnostics are running in the mainframe.

Table 3 shows the offline diagnostic hardware trigger instruction sequences. These instruction sequences are strategically located in MME to execute after a hardware flag sets and an exchange occurs.

Table 3. Offline Diagnostic Hardware Trigger Sequences

Trigger Sequence	Failure Case
002750 002770 001000	Offline user hardware (Placed in MME interrupt router)  Example: An operand range error occurs while running <code>pave.c</code> , and an exchange to the interrupt router occurs.
002770 002770 001000	Offline kernel hardware [Placed in the MME interrupt router trap (iTRAP) routine]  Example: An uncorrectable memory error occurs while executing MME interrupt router code, and an exchange to the trap code occurs.

Table 4 shows the offline diagnostic software trigger instruction sequences. These instruction sequences must be placed in the user diagnostic code.

Table 4. Offline Diagnostic Software Trigger Sequences

Trigger Sequence	Failure Case
002740 002770 001000	Offline user software (Placed in user diagnostic code)
002760 002770 001000	Offline kernel software (Placed in user diagnostic code)

**What Are the Other Predefined Trigger Conditions?**

SMON includes other predefined trigger conditions that set the DMs to trigger on any one hardware flag (BPI, EEX, PRE, ORE, MEU, or RPE), CIP, or P register value. You can choose one of these trigger conditions from Trigger Condition: .

With this option, you cannot restart the system or dump the instruction buffers. The other predefined trigger conditions output only CPU and system status, DM parameters and data, and test point dump data.

### What Is a User-defined Trigger Condition?

A user-defined trigger condition enables you to define the triggers that cause the DMs to stop recording. You create a user-defined trigger condition by modifying an LME parameter set. This enables you to program the DMs to trigger a valid CIP, CIP and mask, P and LP mask, test point, inverted test point, or leading edge.

With this option, you cannot restart the system or dump the instruction buffers. The user-defined trigger conditions output only CPU and system status, DM parameters and data, and test point dump data.

### What Is the Hung CPU Check Feature?

The hung CPU check feature enables SMON to monitor for hung CPUs. To determine whether a CPU is hung, SMON stops the CPU's DM and examines the data recorded in the DM buffer. If there is no valid data in the DM buffer, which indicates that the CPU has not executed any instructions since the DM started recording, the CPU is hung. Because the DM is set to record only on valid CIPs, valid data is any data recorded in the DM buffer that is not the word of 1's used to mark the beginning of the buffer. (The word of 1's is written when the DM starts recording.)

After checking all CPUs, SMON runs the CRASH\_X program if any CPUs are hung. The CRASH\_X program acquires and analyzes data recorded with the DM. SMON will run CRASH\_X twice for any one hung CPU; after that, SMON runs CRASH\_X only when new hung CPUs are detected.

SMON performs the hung CPU check only on CPUs that are active. You can specify whether SMON should check for hung CPUs and the interval of time SMON waits before checking for hung CPUs.

**NOTE:** You can manually run the CRASH\_X utility by choosing the LME Utilities -> Run CRASH\_X menu button command. For more information, refer to the "Logic Monitor Environment" section of the *CRAY C90 Series Mainframe Offline Diagnostic Manual*, publication number CDM-0505-0D0.

## What Software Must I Install to Use SMON?

Before you can use SMON, you must have the proper UNICOS and maintenance software installed. Ensure that the following conditions are met:

- The appropriate kernel mod must be installed for the version of UNICOS you are using. The kernel mod contains the instructions that SMON uses to trigger the diagnostic monitors (DMs) to detect UNICOS hardware trigger conditions.

UNICOS requires a *master* kernel mod to use the 002770 trigger instruction with SMON. UNICOS requires a *slave* kernel mod to use the 002750 trigger instruction with SMON. (Refer again to Table 1 on page 9 for more information about the 002770 and 002750 trigger instruction sequences.)

Table 5 shows the kernel mods that are available.

Table 5. Kernel Mods

UNICOS Version	Master Mod	Slave Mod
7.C.3	7Cuts97669b	No slave mod is available.
8.0.0 – 8.0.2.3	80uts00000b †	No slave mod is available.
8.0.2.4 – 8.0.3	80uts97669a	Contact HPS for information about this slave kernel mod.
8.0.3.1 and higher	The standard UNICOS release includes support for the 002770 trigger instruction. No master mod is required.	Contact HPS for information about this slave kernel mod.

† Contact Dale Mays of Software Product Support (SPS) to obtain this mod. Use the `getfix` command on [hydra.cray.com](http://hydra.cray.com) to obtain the 7Cuts97669b and 80uts97669a mods.

- The ME-C2.3.1 (or later) offline diagnostic release must be installed on the maintenance workstation (MWS-E). Enter the `release` command to verify the offline diagnostic release installed on the MWS-E.

## What Are the Potential Risks of Using SMON?

---

Using SMON requires that the maintenance channel operate in its unrestricted mode. This is necessary because access to the CPUs' diagnostic monitors is disabled when the maintenance channel restrict switch is on.

There are risks involved with using the maintenance channel concurrently with OS operations in the mainframe. Anyone with access to the MWS-E could crash the system by deadstarting a diagnostic or by Master Clearing the system.

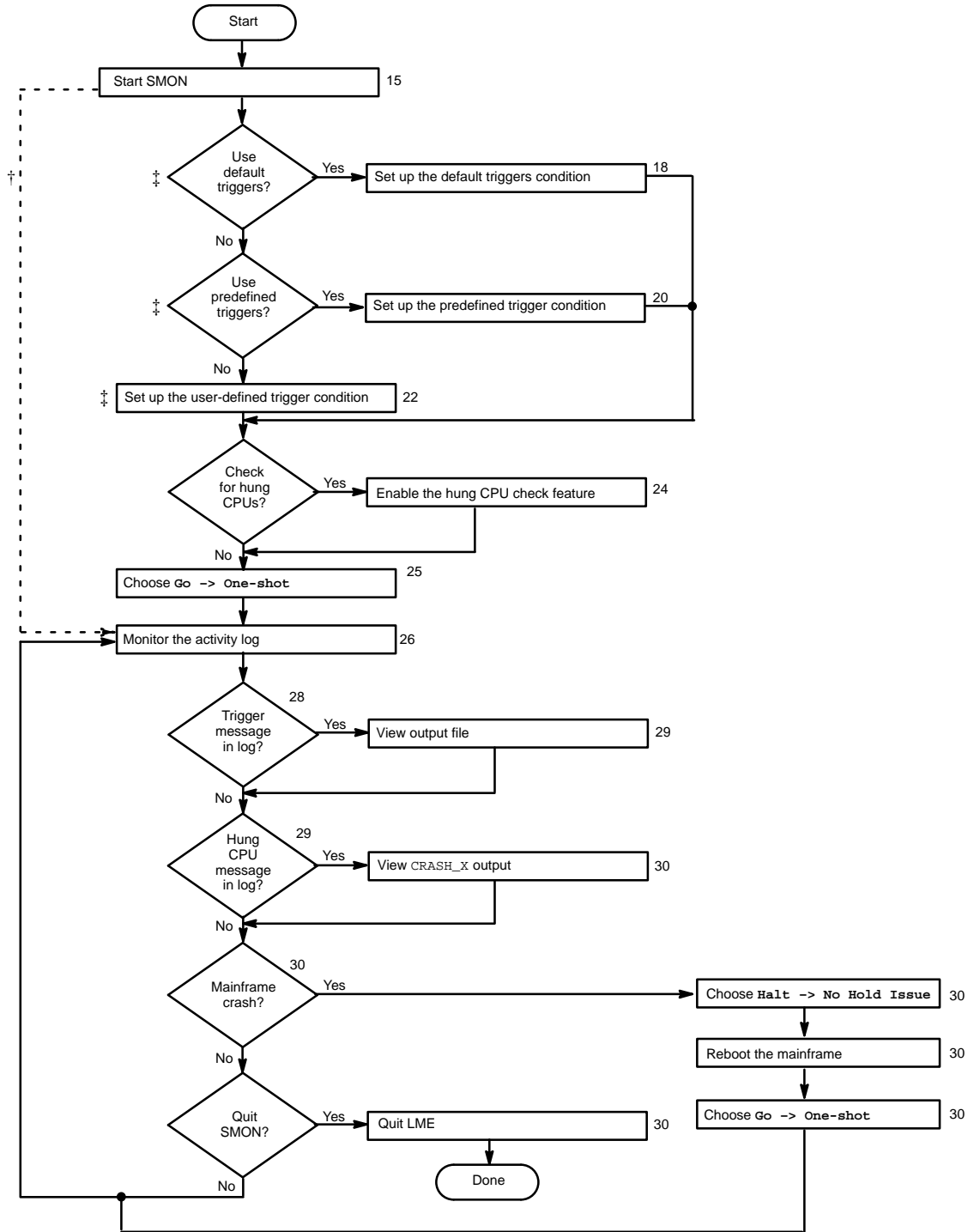
You can reduce these risks by performing the following actions:

- 1 . Ensure that only authorized users are permitted logins on the MWS-E.
- 2 . Instead of leaving the MWS-E screen unlocked to allow access to the EASE or `xelog` error report windows, export these windows to the OWS-E (use the `DISPLAY` variable). Then, lock the MWS-E screen to prevent unauthorized access to the MWS-E.

## How Do I Use SMON?

---

Figure 2 shows the process you will perform to use SMON. The number next to a step indicates the page on which detailed information for the step is located.



† If you start SMON with the -mg command line option, SMON automatically starts monitoring with the default triggers.  
 ‡ You will use only one of the trigger options at a time; normally, you should use the default triggers.

Figure 2. Using SMON Procedure (Flowchart)



## How Do I Start SMON?

If you want to start SMON when UNICOS is already running, perform only Step 6 of the following procedure. It is not necessary to bring down UNICOS to start SMON.

For the following procedure to work, you must have the current mainframe configuration saved as the default configuration for MCE.

### CAUTION

**Always start SMON after the system has been booted to single-user mode because the I/O Master Clear performed during the boot process hangs the maintenance channel.**

You can start SMON any time after UNICOS has reached single-user mode. To start SMON when UNICOS is not already running, perform the following procedure:

- 1 . Clear the mainframe.
- 2 . Quit all MCE, MME, and LME sessions on the MWS-E.
- 3 . Disable the maintenance channel.

This is necessary to re-enable the MCU LOSP with the hard switches.

- 4 . Boot UNICOS to single-user mode.
- 5 . Enable the maintenance channel.

**NOTE:** If the maintenance channel hangs, choose **Reset -> Driver** in the LME base window to clear the hang.

- 6 . Use one of the options described in “From the OpenWindows Workspace Menu” or “From a UNIX Command Prompt” to start SMON.

**From the OpenWindows Workspace Menu**

Choose **Maintenance Tools** -> **SMON** to start SMON from the OpenWindows workspace menu.

**From a UNIX Command Prompt**

Change to the `/cri/cme/c90/bin` directory.

Enter one of the following commands to start SMON from the UNIX command prompt:

- `lme -m` Starts LME; SMON appears but does not start.
- `lme -mg` Starts LME and SMON.

If you enter `lme -mg`, SMON automatically runs with the default settings. These settings include `Default Triggers` trigger condition, `Hold`, `Dump`, `Continue` kernel and user job trigger actions, and hung CPU check feature disabled.

Table 6 describes the other available command line options.

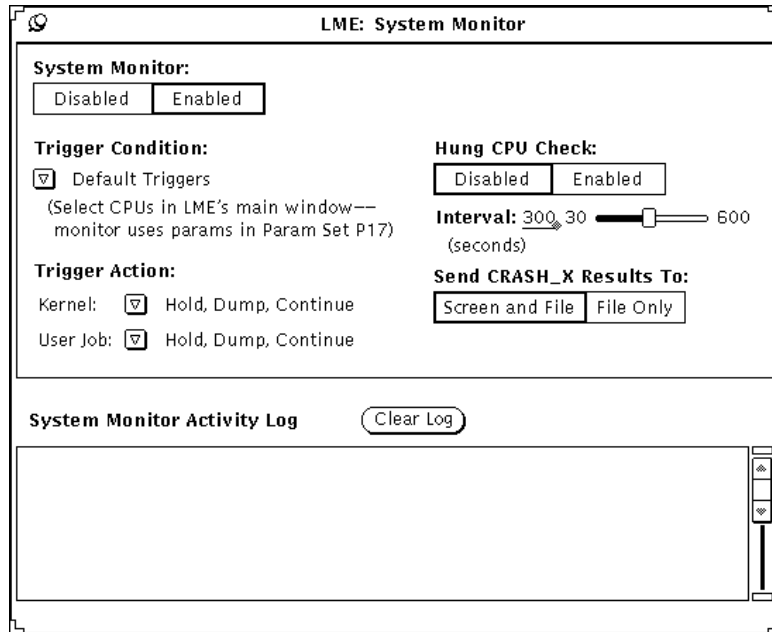
Table 6. Command Line Options

Option	Description
<code>-chn&lt;num&gt;</code>	Use the front-end interface (FEI) channel specified by <code>&lt;num&gt;</code> , which ranges from 0 to 7  The default channel number is 1.
<code>-client</code>	Start the client only
<code>-concurrent</code>	Use concurrent mode
<code>-config &lt;file&gt;</code>	Configure MCE with the configuration data stored in the file specified by <code>&lt;file&gt;</code>
<code>-copy &lt;num&gt;</code>	Connect to maintenance software assigned to the copy number specified by <code>&lt;num&gt;</code>  This option allows you to differentiate which system is being supported by this session of the software.
<code>-kill</code>	Kill all MME, MCE, and LME processes
<code>-l &lt;file&gt;</code>	Load a layout file
<code>-m</code>	Open the SMON window; do not start the utility
<code>-mg</code>	Open the SMON window; start the utility

Table 6. Command Line Options (continued)

Option	Description
-mh [num]	Enable the hung CPU check feature and set it to [num] seconds; do not start the utility
-mm	Enable SMON to perform dumps for trigger instructions issued in user mode  <b>NOTE:</b> By default, SMON does not perform dumps for trigger instruction sequences that issue in user mode.
-offline	Use offline mode  Do not use this option when UNICOS is running. This option will crash UNICOS.
-remote <host>	Start client only, connect to remote host
-server	Start server only
-sim	Use the simulator

The LME: System Monitor window appears:



**NOTE:** If the LME: System Monitor window closes while you are using SMON, choose **Utilities -> System Monitor** in the LME base window to open the window again.

## How Do I Set Up the Default Triggers Condition?

Normally, you should use SMON with the `Default Triggers` option, which enables SMON to output the maximum amount of failure information. If you use the default triggers condition, SMON provides CPU and system data, the DM parameters and data, the failing exchange package, the test point dump data, the instruction buffer dump data, the register dump data, and the memory data pointed to by the A registers.

To use SMON with the default trigger condition, perform the following procedure:

1. Click on `System Monitor`:  `Enabled` to enable SMON.
2. Choose `Trigger Condition`:  `Default Triggers` to enable the CPU to trigger on the default triggers.
3. From the `Kernel`:  or the `User Job`:  choose the action (`Hold, Dump, Continue`; `Hold, Dump, Hold Issue`; `Hold Issue`; or `Don't Hold Issue`) you want SMON to take when the kernel or user job trigger occurs. Refer to Table 7 for descriptions of the kernel and user job trigger action options.

**NOTE:** If you choose `Don't Hold Issue`, the CPU does not hold issue when a trigger occurs. This means that if a CPU triggers and a second trigger occurs before SMON has rearmed the CPU (which SMON does after processing a trigger), SMON does not sense the second trigger.

Table 7. Kernel and User Job Trigger Action Options

Trigger Action	Description
<code>Hold, Dump, Continue</code>	<p>Holds issue; dumps CPU status, system status, DM parameters, DM buffer data, failing exchange package, test point dump data, instruction buffers, all CPU registers, and a 200<sub>8</sub>-word block of mainframe memory for each A register (each block starts at address <math>A_{xx} + DBA</math>); restarts the CPU.</p> <p><b>NOTE:</b> For MEU interrupts, the CPU register dump code quickly scans memory to determine whether any additional MEUs occurred. SMON reports whether additional MEUs did occur.</p>

Table 7. Kernel and User Job Trigger Action Options (continued)

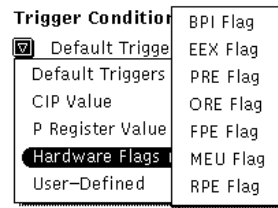
Trigger Action	Description
Hold, Dump, Hold Issue	<p>Holds issue; dumps CPU status, system status, DM parameters, DM buffer data, failing exchange package, test point dump data, instruction buffers, all CPU registers, and a 200g-word block of mainframe memory for each A register (each block starts at address Axx + DBA); restarts the CPU and immediately holds issue.</p> <p><b>NOTE:</b> For MEU interrupts, the CPU register dump code quickly scans memory to determine whether any additional MEUs occurred. SMON reports whether additional MEUs did occur.</p> <p>Having the CPU hold issue “freezes” the CPU and enables you to view its memory area as it was when the failure occurred.</p>
Hold Issue	<p>Holds issue; dumps CPU status, system status, DM parameters, DM buffer data, and test point dump data.</p> <p>Having the CPU hold issue “freezes” the CPU and enables you to view its memory area as it was when the failure occurred.</p>
Don't Hold Issue	<p>Does not hold issue; dumps CPU status, system status, DM parameters, DM buffer data, and test point dump data.</p>

## How Do I Set Up a Predefined Trigger Condition?

SMON enables you to choose from several other predefined trigger conditions. To run SMON using a predefined trigger condition, perform the following procedure:

**NOTE:** To receive the most output from SMON, you should use the Default Triggers option. The predefined trigger conditions output CPU and system status, DM parameters and data, and test point dump data only.

1. Click on System Monitor:  Enabled to enable SMON.
2. From Trigger Condition:  ▼, choose a trigger condition:



For descriptions of the available trigger conditions, refer to Table 8.

Table 8. Trigger Conditions

Trigger Condition	Description																
CIP Value	Current instruction parcel value																
P Register Value	Program register value																
Hardware Flags	<p>A hardware flag; choose one of the following options:</p> <table border="1"> <thead> <tr> <th>Option</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>BPI Flag</td> <td>Breakpoint interrupt</td> </tr> <tr> <td>EEX Flag</td> <td>Error exchange</td> </tr> <tr> <td>PRE Flag</td> <td>Program range error</td> </tr> <tr> <td>ORE Flag</td> <td>Operand range</td> </tr> <tr> <td>FPE Flag</td> <td>Floating-point error</td> </tr> <tr> <td>MEU Flag</td> <td>Uncorrectable memory error</td> </tr> <tr> <td>RPE Flag</td> <td>Register parity error</td> </tr> </tbody> </table>	Option	Description	BPI Flag	Breakpoint interrupt	EEX Flag	Error exchange	PRE Flag	Program range error	ORE Flag	Operand range	FPE Flag	Floating-point error	MEU Flag	Uncorrectable memory error	RPE Flag	Register parity error
Option	Description																
BPI Flag	Breakpoint interrupt																
EEX Flag	Error exchange																
PRE Flag	Program range error																
ORE Flag	Operand range																
FPE Flag	Floating-point error																
MEU Flag	Uncorrectable memory error																
RPE Flag	Register parity error																
User-Defined	Refer to the “How Do I Set Up a User-defined Trigger Condition?” description on page 22 for more information.																

3. If you selected a CIP Value or P Register Value trigger condition, enter the appropriate value in the field that appears when you click on the trigger.
4. From the Trigger Action: , choose **Hold Issue** or **Don't Hold Issue** to specify which action you want SMON to perform when a CPU triggers. Refer to Table 9 for descriptions of the available trigger action options.

**NOTE:** If you choose Don't Hold Issue, the CPU does not hold issue when a trigger occurs. This means that if a CPU triggers and a second trigger occurs before SMON has rearmed the CPU (which SMON does after processing a trigger), SMON does not sense the second trigger.

Table 9. Trigger Action Options

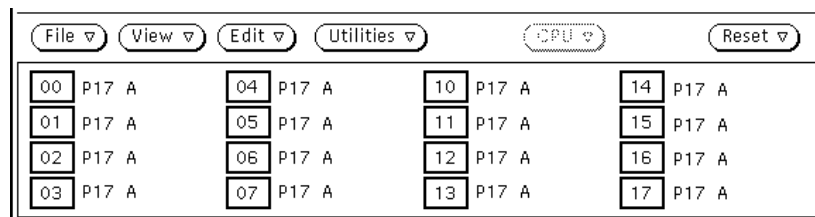
Trigger Action	Description
Hold Issue	Holds issue; dumps CPU status, system status, DM parameters, DM buffer data, and test point dump data.  Having the CPU hold issue "freezes" the CPU and enables you to view its memory area as it was when the failure occurred.
Don't Hold Issue	Does not hold issue; dumps CPU status, system status, DM parameters, DM buffer data, and test point dump data.

## How Do I Set Up a User-defined Trigger Condition?

Using the user-defined trigger conditions enables a CPU to trigger on the conditions you select in the `LME: Edit Parameter Set` window.

**NOTE:** To receive the most output from SMON, you should use the `Default Triggers` option. The user-defined trigger conditions output CPU and system status, DM parameters and data, and test point dump data only.

1. Click on `System Monitor:  Enabled` to enable SMON.
2. Choose `Trigger Condition:  User-Defined` to specify that you want the CPU to trigger on the user-defined trigger conditions selected in the `LME: Edit Parameter Set` window. SMON assigns parameter set 17 to all available CPUs:



3. Double click on a parameter set listed under `Parameter Sets:` in the `LME` base window to set the user-defined trigger parameters. The `LME: Edit Parameter Set` window appears.

For more information about setting parameters in LME, refer to the *CRAY C90 Series Mainframe Offline Diagnostic Manual*, publication number CDM-0505-0D0.

4. Choose `Trigger Action:  Hold Issue; Don't Hold Issue`; or `As specified in DM params` to specify which action you want SMON to perform when a CPU triggers. Refer to Table 10 for descriptions of the available trigger actions.

**NOTE:** If you choose `Don't Hold Issue`, the CPU does not hold issue when a trigger occurs. This means that if a CPU triggers and a second trigger occurs before SMON has rearmed the CPU (which SMON does after processing a trigger), SMON does not sense the second trigger.



Table 10. User-defined Trigger Action Options

Trigger Action †	Description
Hold Issue	<p>Holds issue; dumps CPU status, system status, DM parameters, DM buffer data, and test point dump data.</p> <p>Having the CPU hold issue “freezes” the CPU and enables you to view its memory area as it was when the failure occurred.</p>
Don't Hold Issue	Does not hold issue; dumps CPU status, system status, DM parameters, DM buffer data, and test point dump data.
As specified in DM params	Holds or does not hold issue as specified by the “mode” parameter in the DM parameters; dumps CPU status, system status, DM parameters, DM buffer data, and test point dump data.

† Choosing the `Hold Issue` or `Don't Hold Issue` trigger action options overrides the mode parameter specified in the `LME: Edit Parameter Set` window. To use the mode specified in the `LME: Edit Parameter Set` window, you must choose `As specified in DM params`.

## How Do I Enable the Hung CPU Check Feature?

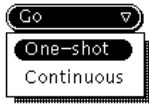
**NOTE:** When the CRASH\_X program is run, any CPUs that SMON caused to hold issue are released.

To enable SMON to check for hung CPUs, perform the following procedure:

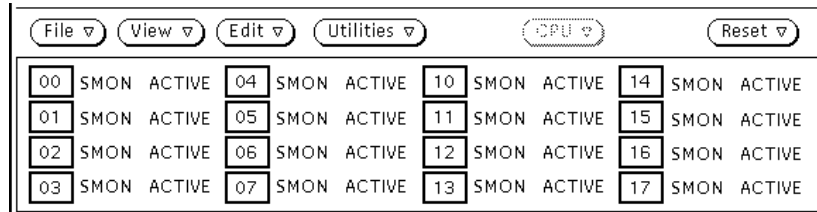
1. Click on Hung CPU Check:  Enabled.
2. In the Interval: field, enter the number or use the slider to specify how often you want SMON to check for a hung CPU. The valid values for the interval range from 30 to 600 seconds.

**NOTE:** The Send CRASH\_X Results to: options send the output from the CRASH\_X program to the screen and a file ( Screen and File) or to a file only ( File Only).

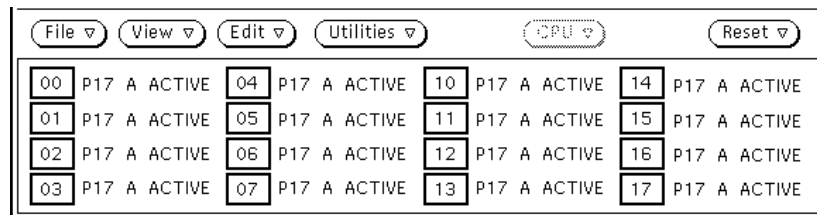
## How Do I Make SMON Start Monitoring the CPUs?



In the LME base window, choose **Go** -> **One-shot**, as shown at the left, to run the parameter set(s) once. For the default and predefined trigger conditions, **SMON ACTIVE** appears in the CPU status display to indicate that SMON is monitoring that CPU:



For user-defined trigger conditions, LME displays a parameter set, a buffer, and the word **ACTIVE** next to the CPUs in the CPU status display:



**NOTE:** The **Go** -> **Continuous** option is not valid with **SMON**.

The **System Monitor Activity Log** displays a message indicating that **SMON** has started. Figure 3 shows an example.

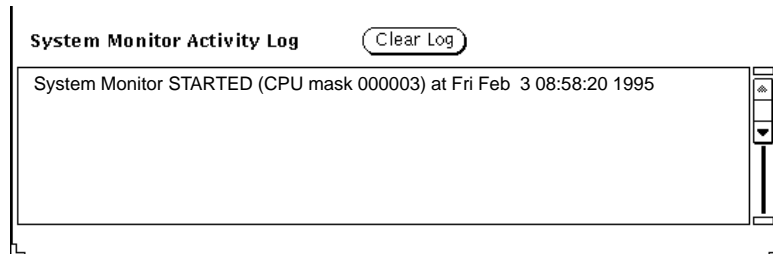


Figure 3. SMON Started Message

**SMON** remains active for a CPU until the DM triggers or you click on **Halt** in the LME base window.

## What Should I Do While SMON Monitors for Triggers and Hung CPUs?

While SMON monitors for triggers and hung CPUs, all messages about SMON activity appear in the System Monitor Activity Log scroll box, shown in Figure 4. You should monitor the activity log for status messages.

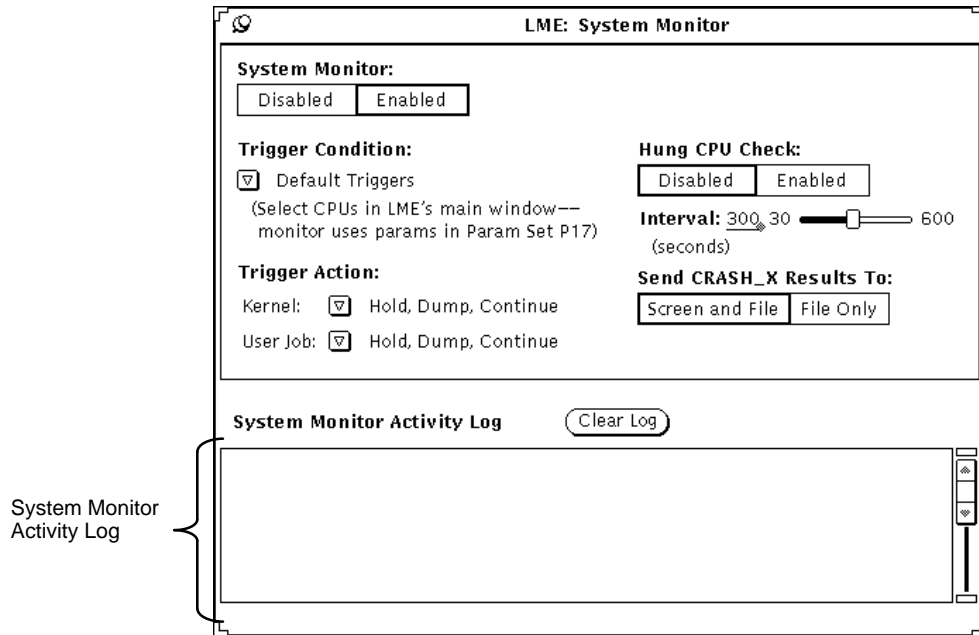


Figure 4. System Monitor Activity Log

**NOTE:** To clear the scroll box, click on .

## What Messages Will I See in the Log?

The following examples illustrate the messages that appear in the System Monitor Activity Log scroll box:

```
System Monitor STARTED (CPU mask 000003) at Fri Feb 3 08:58:20 1995
```

This message indicates that SMON has started monitoring the CPUs for triggers. This message shows a mask of the CPUs (0 and 1 in this example) that SMON is monitoring and the date and time that SMON started monitoring these CPUs.

System Monitor STOPPED (CPU mask 000003) at Fri Feb 3 08:58:34 1995

This message indicates that SMON has stopped monitoring the CPUs for triggers. This message shows a mask of CPUs that SMON was monitoring and the date and time that SMON stopped monitoring these CPUs.

CPU 01 triggered at Fri Feb 3 09:01:10 1995

Summary in '/cri/cme/c90/usr/lme/smon/cpu01.Feb03\_09:01:10'

This message indicates that SMON detected a trigger for a CPU (in this example, CPU 01). This message shows the date and time the trigger occurred and indicates the pathname for the output file that SMON generated.

Trigger messages may contain additional informational or error messages. Informational messages begin with **\*\*\***. Error messages begin with **ERROR**.

HUNG CPUS DETECTED at Fri Feb 3 09:01:10 1995

CPU 01 is **\*\*HUNG\*\***

CRASH\_X output in '/cri/cme/c90/usr/crash/crash\_x\_out.Feb03\_09:01:10'

CRASH\_X data in '/cri/cme/c90/usr/crash/crash\_x\_data.Feb03\_09:01:10'

This message indicates that SMON detected one or more hung CPUs. SMON prints a CPU *xx* is **\*\*HUNG\*\*** message for each hung CPU, where *xx* is the CPU number. This message indicates the pathnames for the CRASH\_X output and data files.

CPU 00 triggered at Fri Feb 3 09:37:38 1995

Summary in '/cri/cme/c90/usr/lme/smon/cpu00.Feb03\_09:37:38'

**\*\*\*** CPU is "pinging" (constantly triggering)--monitor not restarted.

This trigger message contains additional information that indicates a CPU has triggered 5 times in 1 minute. When this occurs, SMON stops monitoring the CPU, indicated by the monitor not restarted text.

CPU 03 triggered at Fri Feb 3 09:56:00 1995

Summary in '/cri/cme/c90/usr/lme/smon/cpu03.Feb03\_09:56:00'

**\*\*\*** Multiple CPUs constantly triggering--monitor halted for all CPUs

This trigger message contains additional information that indicates more than 32 triggers have occurred for all CPUs in the last 5 minutes. When this occurs, SMON stops monitoring all CPUs, indicated by the monitor halted for all CPUs text.

## How Do I Determine if a Trigger Occurred?

SMON displays a message in the System Monitor Activity Log when a trigger occurs. Figure 5 shows an example status message that indicates a CPU triggered.

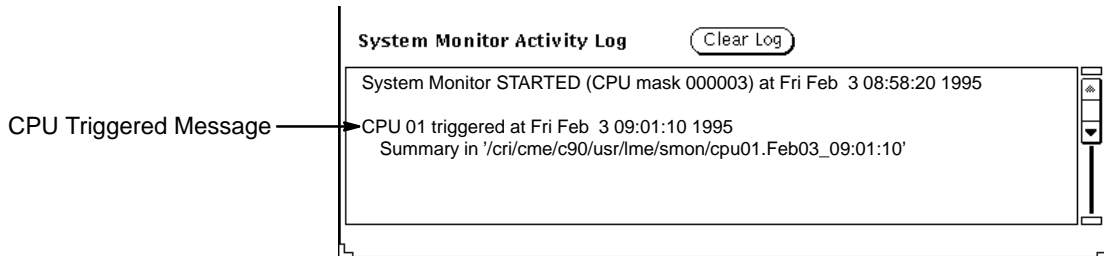


Figure 5. CPU Triggered Message

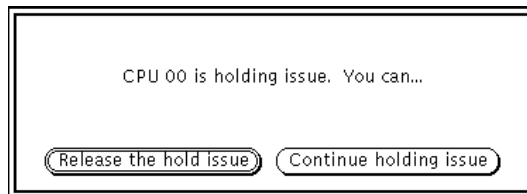
## What Happens When a Trigger Occurs?

When a CPU's DM triggers, SMON creates an output file that contains all available information from the trigger. The information that SMON gathers is determined by the Trigger Condition and Trigger Action settings.

**NOTE:** The output file can be as large as 140 Kbytes (if you use the default settings to have SMON report all available information).

SMON also executes a user-defined script each time a trigger occurs. To use this feature, create a script named `.smon_trigger_cmd` in the `/cri/cme/c90/bin` directory. Refer to the "Run `.smon_trigger_cmd` Script" description on page 40 for more information.

If you chose to hold issue after SMON detects a trigger, SMON displays the following message when a trigger occurs:



Click on  to release the hold issue option; the CPU resumes executing instructions. Click on  to continue holding issue. Having the CPU hold issue “freezes” the CPU and enables you to view its memory area as it was when the failure occurred. If you click on , the CPU holds issue until you either disable SMON, click on  to restart SMON, or quit LME.

### How Do I View the Output File?

To view the output file, view the file shown in the SMON activity log. Because the output file is an ASCII file, you can use the `more` or `vi` command to view the file.

Refer to “How Do I Interpret the SMON Output File?” on page 45 of this document for examples of how to interpret the data contained in the output file.

### How Do I Determine if a Hung CPU Was Detected?

When SMON detects a hung CPU, SMON displays a message in the activity log. Figure 6 shows an example status message that indicates SMON detected a hung CPU (CPU 1).

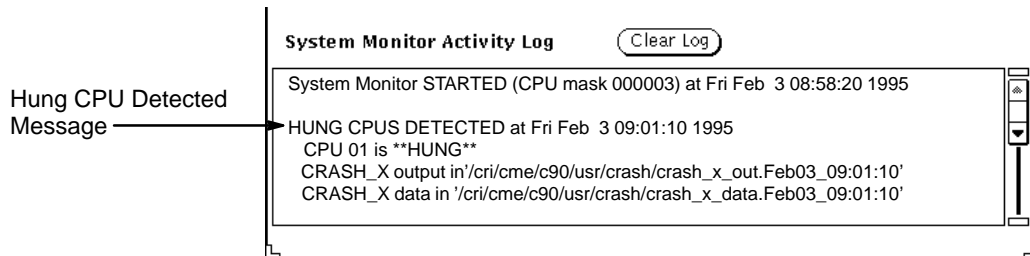


Figure 6. Hung CPU Detected Message

### What Happens When SMON Detects a Hung CPU?

When SMON detects a hung CPU, SMON runs the `CRASH_X` program. SMON also executes a user-created script each time a hung CPU is detected. To use this feature, create an executable script named `.smon_hang_cmd` in the `/cri/cme/c90/bin` directory. Refer to the “Run `.smon_hang_cmd` Script” description on page 44 for more information.

After SMON runs CRASH\_X, SMON restarts the DMs of all CPUs that were active before CRASH\_X was run.

**NOTE:** The CRASH\_X files can use up to 52 Kbytes of disk space (when SMON runs CRASH\_X for a system with 16 CPUs).

### How Do I View the CRASH\_X Output?

There are two ways to view the CRASH\_X output:

- If you clicked on Send CRASH\_X Results To:  before SMON detected the hung CPU, SMON will automatically display the CRASH\_X output file on the screen.
- Because the CRASH\_X output file is an ASCII file, you can use the `more` or `vi` command to view the file. Change to the directory shown in the SMON activity log, and view the file shown in the SMON activity log.

The analysis information appears at the end of the CRASH\_X output file.

### What Should I Do if the Mainframe Crashes?

If the mainframe crashes and you want to restart SMON, choose **Halt** -> **No Hold Issue** in the LME base window to have SMON stop monitoring the CPUs. This prevents SMON from detecting false triggers and hangs when you reboot the mainframe. Then, boot the mainframe. Once the mainframe is booted, choose **Go** -> **One-shot** in the LME base window to have SMON start monitoring the CPUs again.

### How Do I Quit SMON?

You should quit SMON before or when you bring UNICOS down to single-user mode, or SMON may detect false triggers.

To quit SMON, quit the LME application.



## How Can I Test SMON?

---

You can test SMON with the C program shown in Figure 7. Start SMON with the default triggers condition. Then, compile and run this program in the mainframe. SMON should detect an ORE in a user job.

```
#include<stdio.h>
main()
{
    int c;
    char temp[3];

    for(c=1; c>0 ;c+=20000)
        printf("%c\n", temp[c]);
}
```

Figure 7. C Program You Can Use to Test SMON

## How Does SMON Work?

---

This section describes how SMON works and is written with the assumption that you have an understanding of DM and maintenance channel activity. For more information about DM and maintenance channel activity, refer to Volume 2 of the *CRAY C90 Series Hardware Maintenance Manual*, publication number CMM-0502-0A0.

The flowchart in Figure 8 illustrates the process SMON uses to monitor CPUs and to detect and process trigger and hung CPU conditions. The number next to each step in the flowchart indicates the page on which a detailed description of the step is located.

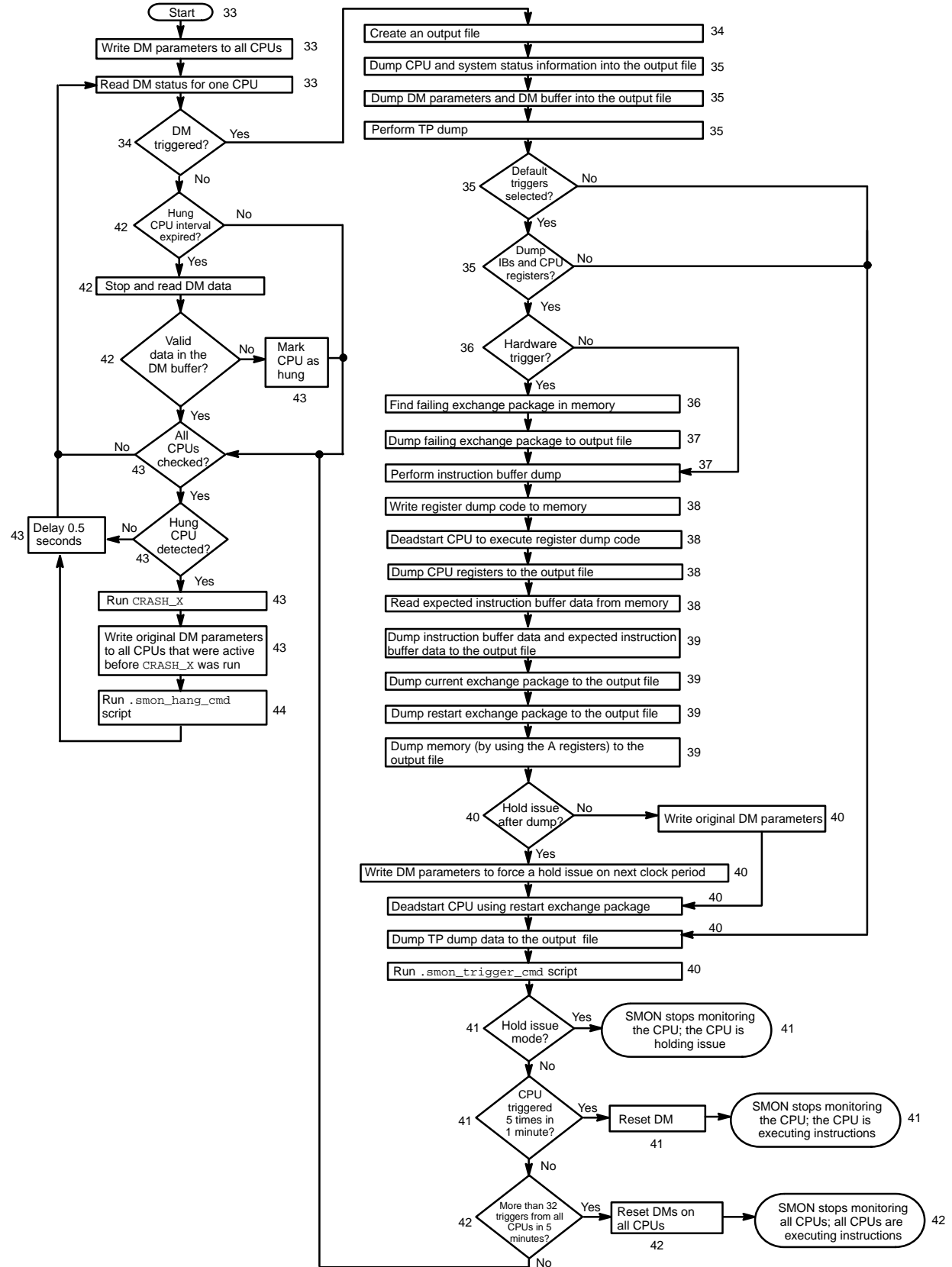


Figure 8. SMON Functions Flowchart

## Start

SMON begins running when you click on System Monitor:  Enabled in the LME: System Monitor window and choose Go -> One-shot in the LME base window.

**NOTE:** SMON starts automatically if you enter `lme -mg`.

## Write DM Parameters to All CPUs

SMON sends a `DM_WRITE` command to all available CPUs. This write command is followed by 12 parcels of DM parameters. The trigger condition and trigger action from the LME: System Monitor window determine how the parcels of DM parameters are set. All the CPUs' DMs start to record program information.

SMON arms the trigger so the DMs will stop recording when the trigger condition occurs.

## Read DM Status for One CPU

Every half second, SMON sends a `STATUS_RD` command to each CPU to test the state of its DM. Each CPU responds to the `STATUS_RD` command by returning 4 parcels of status information. SMON checks the state of the diagnostic monitor active (DA) bit in parcel 0 to determine whether the CPU's DM has triggered. When the DA bit clears, the DM has triggered and stopped recording. The `STATUS_RD` command is completely transparent to the CPU operation and uses no resources other than the maintenance channel.

One bit of the 12 parcels of DM parameters causes a CPU to hold issue when the DM triggers. Unless the trigger action is set to Don't Hold Issue, SMON programs the DM to assert this hold issue when the DM triggers.

**NOTE:** If you select Don't Hold Issue for both the Kernel and User Job trigger action settings, all DM activity is completely transparent to the CPU and has no effect on CPU performance. Otherwise, the CPU must momentarily hold issue on any trigger to determine if it is a trigger that needs to hold issue. If the selected trigger action does not include a hold issue, SMON releases the hold issue.

If the trigger action `Hold`, `Dump`, `Hold Issue` or `Hold Issue` option is selected, SMON does not automatically release the hold issue condition. This hold issue condition does not affect CPU operation; however, the OS shows the error message "CPU has not exchanged for 30 seconds". This error message appears because the CPU is not executing any instructions while the hold issue is in effect. You must manually release the hold issue condition by disabling SMON (click on `System Monitor`:  `Enabled`) in the `LME: System Monitor` window) or by restarting the CPU (click on  in the LME base window).

Having the CPU hold issue "freezes" the CPU and enables you to view its memory area as it was when the failure occurred. This is useful for user job failures. Normally, after a user job core-dumps, the OS reallocates the associated memory to a new process. By having SMON hold issue after a trigger, this memory reallocation is delayed. This enables you to view mainframe memory with the `LME Utility -> View Memory` command to search for additional clues about what happened, if necessary.

**NOTE:** If you quit LME, SMON also releases the hold issue by resetting all DMs.

## DM Triggered?

SMON checks to whether the DM has triggered. If the DM triggered, SMON captures CPU data in an output file.

**NOTE:** SMON checks the system CPU Master Clear test point (DC0 test point 045) to determine if UNICOS was halted while SMON is running. If UNICOS was halted (the system CPU Master Clear test point is set), SMON ignores the trigger.

## Create an Output File

Once SMON detects that a CPU's DM has triggered, SMON creates a text file to store the information that will be acquired. The complete path for the file is:

```
/cri/cme/c90/usr/lme/smon/cpu#.date.time
```

A new file is created for each new trigger. A header that contains the system serial number, the CPU that triggered, and the time at which the trigger occurred is stored in the file.

**NOTE:** The output file can be as large as 140 Kbytes (if you use the default settings to have SMON report all available information).

## Dump CPU and System Status Information into the Output File

The first block of information recorded in the output file contains general CPU and system status read with the `STATUS_RD` maintenance channel command.

SMON samples the status information multiple times to determine whether memory priority is locked. SMON reports the memory priority status in the output file.

## Dump DM Parameters and DM Buffer into the Output File

The second block of information recorded in the output file is the DM data. First, the DM parameters are recorded. Then, the contents of the 64-element DM buffer are recorded. SMON labels the 64 elements *events* and numbers them event 000 to event 077 (octal). By default, the trigger instruction is at event 076 of 077. The DM is programmed to record only valid CIP events; the DM buffer contains the last 62 instructions issued before the trigger event and contains the corresponding trace of P-register values.

## Perform TP Dump

SMON reprograms the DM to perform a test-point dump, which takes a snapshot of the state of each available test point. One of the main reasons for performing the test point dump is to record the current value of the exchange address present on the HCO option test points 060 to 067. The exchange address is later written in the restart exchange package.

**NOTE:** SMON saves the test-point dump data and writes it at the end of the output file.

## Default Triggers Selected?

SMON checks whether the default triggers option is selected as the trigger condition.

## Dump Instruction Buffers and CPU Registers?

SMON performs an instruction buffer dump and a CPU register dump after a trigger if the `Trigger Action` setting was `Hold`, `Dump`, `Continue Or Hold`, `Dump`, `Hold Issue`.

## Hardware Trigger?

SMON checks whether the trigger condition is a hardware trigger condition. To do this, SMON checks the triggering sequence recorded in the DM buffer to determine whether it is a hardware trigger sequence.

## Find Failing Exchange Package in Memory

If a user hardware or kernel hardware failure occurs, SMON assumes that a bad flag caused the CPU to exchange and issue the trigger instruction. In these cases, the failing exchange package is written to memory. The “current exchange package” in the CPU itself is associated with the kernel job servicing the interrupt flag.

SMON calculates the location of the failing exchange package, reads the memory area through the CPU’s maintenance port, and saves the exchange package to the output file. This data is labeled the `Failing Exchange Package`.

For a kernel failure, both the kernel and the user job exchange packages are dumped to the output file. This is done because often when an uncorrectable memory occurs in a user job, another will occur in the kernel before the user job trigger is issued, and the failure information would be lost.

Three different placement conventions, depending on the version of UNICOS being used, make it somewhat complicated to calculate where the failing exchange package is located in memory. SMON determines which convention is being used by searching for the ASCII tag field “CPU1” at locations 640 and 700. This exchange package is not used to restart the CPU. SMON uses the instruction base address (IBA) register to calculate the fetch address for reading the expected instruction buffer data. Also, the cluster number (CLN) field is used to determine which clusters of shared registers are dumped.

Table 11. Exchange Package Placement

Version of UNICOS	Exchange Package Placement
8.0.3 (8.0.2.4 is lower bound)	No "CPU" tags in lower memory CPU 0 kernel exchange package at 500 <sub>8</sub> Kernel exchange package every 100 <sub>8</sub> words USER always 040 word from KERNEL
8.0 – 8.0.2 (8.0.2.3 is upper bound)	"CPU0" tag at 500 <sub>8</sub> "CPU1" tag at 640 <sub>8</sub> CPU 0 kernel exchange package at 520 <sub>8</sub> KERNEL exchange every 140 <sub>8</sub> words USER always 100 <sub>8</sub> octal words from KERNEL
7.C	"CPU0" tag at 500 <sub>8</sub> "CPU1" tag at 700 <sub>8</sub> CPU 0 kernel exchange package at 520 <sub>8</sub> KERNEL exchange every 200 <sub>8</sub> words USER always 140 <sub>8</sub> octal words from KERNEL

### Dump Failing Exchange Package to Output File

SMON dumps the failing exchange package to the output file.

### Perform Instruction Buffer Dump

The instruction buffer dump feature of a CRAY C90 series computer system is implemented in the hardware by having the CPU exchange into a special maintenance mode in which issue is prevented, and the instruction buffer data is read out of the buffers through the normal issue path (NIP, CIP). If the CPU holds issue before exchanging into this mode, no registers or current instruction issue operations are corrupted.

However, when SMON sets the hold issue condition, the P register points to the next instruction to be gated out of the instruction buffers. Depending on issue conflicts, the other one or two instructions already in the issue pipe may or may not be issued. SMON cannot reliably determine which instructions issued and where the P register should point to upon returning the CPU to that process. When the default triggers are placed in the OS trap routines, a trigger is followed by two no-op instructions, which avoid this problem.

SMON dumps the instruction buffers and CPU registers only when the Default triggers trigger condition option is selected. This ensures that the CPU will be restarted with the correct P register value.

## **Write Register Dump Code to Memory**

To dump the CPU registers, SMON writes a deadstart exchange package and a short code sequence to memory locations 0 through 77.

## **Deadstart CPU to Execute Register Dump Code**

SMON deadstarts the CPU, which executes the code sequence to store the CPU registers to memory.

## **Dump CPU Registers to the Output File**

SMON uses the CPU's maintenance port to read the register information from memory and stores it in the output file. There is only a small area of memory, from locations 0 to 77, that is not reserved for the OS. SMON handles this limitation by using the DM to freeze the CPU between writes of 0100<sub>8</sub> word blocks of register data while SMON issues a maintenance channel function to read the data from memory. SMON performs 19 reads of memory locations 0 through 77 to dump all of the CPU registers.

For MEU interrupts, the register dump code quickly scans memory to determine whether any additional MEUs occurred. If additional MEUs did occur, SMON reports this in the output file.

## **Read Expected Instruction Buffer Data from Memory**

SMON tries to read the expected IB data from memory by calculating the fetch address by adding the P register address recorded in the DM buffer and the IBA recorded in the failing exchange package. SMON can tell which instruction buffer corresponds to the P register value in the DM buffer from the coincidence field. Therefore, SMON will be able to calculate the fetch address for an instruction buffer only if that instruction buffer number is present in the coincidence field of one of the 64 recorded events.

Note that for user hardware and kernel hardware failures, in which the CPU has done an exchange/fetch before issuing the trigger instruction, instruction buffer 0 is overwritten with the code fetched for the trap routine.



## Dump Instruction Buffer Data and Expected Instruction Buffer Data to the Output File

SMON dumps the instruction buffer data and expected instruction buffer data into the output file. The data is organized as expected, actual, and difference.

## Dump Current Exchange Package to the Output File

While SMON forces a Master Clear (MC)/exchange sequence to initiate the instruction buffer dump sequence, this is also the mechanism for dumping the CPU's current exchange package. In user software or kernel software failures, there is no exchange before issuing the trigger instruction, and the current exchange package is the "failing" exchange package. The current exchange package is written to memory location 0 as SMON forces the MC/exchange sequence to initiate the instruction buffer dump sequence. After this exchange, SMON reads the CPU's exchange package from memory location 0 and saves it in the output file.

## Dump Restart Exchange Package to the Output File

SMON writes the restart exchange package to the output file. This exchange package is the same as the "current exchange package," except for the value in the exchange address (XA) register. As previously described, the mechanism for dumping the current exchange package is the MC/exchange sequence. Setting a CPU MC causes the XA register to be set to 0. The proper XA value is restored using the HC0 test point information previously read with the test point dump operation.

Also, SMON clears the MEU flag in the exchange package if the flag was not set in the test point dump, which indicates the trigger processing that SMON performed caused the MEU. This prevents an MEU from causing a system panic on a nonfatal user job core dump.

## Dump Memory (by Using the A Registers) to the Output File

For each A register, SMON dumps a 200<sub>8</sub> word block of mainframe memory into the output file. Each block starts at the value contained in the corresponding A register plus the DBA (start address =  $A_{xx} + DBA$ ).

## Hold Issue after Dump?

The trigger action you specified determines whether the CPU should hold issue.

## Write Original DM Parameters

If the `Trigger Action` option was set to `Hold`, `Dump`, `Continue` SMON rewrites the original parameter set to the CPU's DM.

## Write DM Parameters to Force Hold Issue on Next Clock Period

If you selected a trigger action that holds issue, SMON writes a DM parameter set to force a hold issue the next clock period.

## Deadstart CPU Using Restart Exchange Package

SMON uses the restart exchange package to restart the CPU. The CPU is holding issue or executing instructions, depending on which DM parameter SMON wrote to the CPU.

## Dump TP Dump Data to the Output File

SMON dumps the test-point dump data to the output file.

## Run `.smon_trigger_cmd` Script

SMON checks the `/cri/cme/c90/bin` directory for the `.smon_trigger_cmd` script. If the script exists and the user running SMON has execute permission for the file, SMON runs the script with a `system( )` call.

SMON sends the full path of the SMON output file to this script. For example, if CPU 03 triggered on Wednesday, November 23, 1994, at 09:51:45, SMON issues the following command in the `system( )` call (from the `/cri/cme/c90` directory):

```
bin/.smon_trigger_cmd /cri/cme/c90/usr/lme/smon/cpu03.Nov23_09:51:45
```

You can create a `.smon_hang_cmd` script to use this data; for example, you could have the script use E-mail to send you a copy of the SMON output file:

```
echo "RUNNING THE SMON TRIGGER COMMAND SCRIPT (file=$1)"
mail -s 'SMON trigger file' dtg@cray.com < $1
exit 0
```

If the file does not exist or the user running SMON does not have execute permission, SMON does not make the `system( )` call.

### Hold Issue Mode?

SMON checks whether the trigger action assigned in the LME: System Monitor window includes a hold issue.

### SMON Stops Monitoring the CPU; the CPU Is Holding Issue

If you selected a hold issue trigger action, SMON stops monitoring the CPU. The CPU is holding issue.

### CPU Triggered 5 Times in 1 Minute?

SMON checks whether the CPU has triggered 5 times in the last minute. SMON monitors this because a CPU that repeatedly triggers continually generates output files.

### Reset DM

If the CPU has triggered 5 or more times in the last minute, SMON resets the DM.

### SMON Stops Monitoring the CPU; the CPU Is Executing Instructions

SMON stops monitoring the CPU. The CPU continues to execute instructions.

## **More Than 32 Triggers from All CPUs in 5 Minutes?**

SMON checks whether more than 32 triggers occurred for all CPUs in 5 minutes. SMON monitors this because CPUs that repeatedly trigger continually generate output files.

## **Reset DMs on All CPUs**

If more than 32 triggers from all CPUs occur in 5 minutes, SMON resets the DMs on all CPUs.

## **SMON Stops Monitoring All CPUs; All CPUs Are Executing Instructions**

SMON stops monitoring all CPUs. All CPUs continue to execute instructions.

## **Hung CPU Interval Expired?**

SMON determines whether the time you specified in the `Interval` field has expired.

## **Stop and Read DM Data**

If the time has expired, SMON stops the CPU's DM and reads the data recorded in the DM buffer.

## **Valid Data in DM Buffer**

SMON checks the DM buffer data to determine whether it contains valid data. If the buffer does not contain valid data, this indicates that the CPU has not executed any instructions since the DM started recording. The CPU is hung.

**Mark CPU as Hung**

If SMON detects a hung CPU, it marks that CPU as hung and continues to check the remaining CPUs. Once all CPUs are checked, SMON runs the CRASH\_X program if any CPUs are marked as hung.

**All CPUs Checked?**

SMON verifies whether all CPUs were checked. If SMON did not check all CPUs yet, SMON checks the next CPU in the system.

**Hung CPU Detected?**

SMON checks for any CPUs it marked as hung.

**Delay 0.5 Seconds**

If no CPUs are marked as hung, SMON delays 0.5 seconds before starting to monitor all CPUs for a hung CPU condition again.

**Run CRASH\_X**

If CPUs are marked as hung, SMON runs the CRASH\_X program, which gathers all available information from all CPUs.

**NOTE:** To use CRASH\_X, SMON gathers information for all CPUs that are marked as hung and passes this information to the CRASH\_X program. While gathering data for CRASH\_X, SMON checks the system CPU Master Clear test point (DC0 test point 045) to determine if UNICOS was halted while SMON is running. If the hung CPUs occurred because UNICOS was halted (the system CPU Master Clear test point is set), CRASH\_X is not run.

The CRASH\_X files can use up to 52 Kbytes of disk space (when SMON runs CRASH\_X for a system with 16 CPUs).

**Write Original DM Parameters to All CPUs That Were Active before CRASH\_X Was Run**

SMON writes the original DM parameters to all CPUs that were active before SMON ran the CRASH\_X program.

## Run `.smon_hang_cmd` Script

SMON checks the `/cri/cme/c90/bin` directory for the `.smon_hang_cmd` script. If the script exists and the user running SMON has execute permission for the file, SMON runs the script with a `system( )` call.

SMON sends the `CRASH_X` output and data files as parameters to this script. For example, if SMON detected a hung CPU on Wednesday, November 23, 1994, at 09:51:45, SMON issues the following command in the `system( )` call (from the `/cri/cme/c90` directory):

```
bin/.smon_hang_cmd /cri/cme/c90/usr/crash/crash_x_out.Nov23_09:51:45
/cri/cme/c90/usr/crash_x_data.Nov23_09:51:45
```

You can create a `.smon_hang_cmd` script to use this data; for example, you could have the script use E-mail to send you a copy of the `CRASH_X` output file:

```
echo "RUNNING THE SMON HANG COMMAND SCRIPT (file=$1)"
mail -s 'SMON hang file' dtg@cray.com < $1
exit 0
```

If the file does not exist or the user running SMON does not have execute permission, SMON does not make the `system( )` call.

## How Do I Interpret the SMON Output File?

---

To interpret the output file, start with the `Failure Analysis` information. Because the output file is different for every trigger, you should use the `Failure Analysis` information as a starting point to guide you through the output file.

### Examples of Interpreting the Output

The following two examples illustrate how you can interpret the output file that SMON generates. These examples show:

- An ORE interrupt in the OS kernel
- A software trap in the user code

The “Appendix” of this document contains the complete output files for these examples.

**ORE Interrupt in OS Kernel**

View the Failure Analysis information. The highlighted text in Figure 9 indicates that the CPU triggered on a kernel hardware trigger sequence.

```

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
                                Failure Analysis
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

** Hardware failure occurred within the UNICOS kernel
   (UNICOS revision: 8.0.2.4 or beyond)

Failing Exchange Package (read from Cray memory @ 1100):
-----
P      00007752525d  A0 007775 140113  IMODES 176000
IBA    000000000000  A1 000000 000000  IFLAGS 010000
ILA    01777776000  A2 000003 027606
DBA    000000000000  A3 000000 000200  *IRP   RPE
DLA    01777776000  A4 000000 002135  *IUM   MEU
                                           A5 000000 001750  *IFP   FPE
PN 04 XA 1100      A6 000000 000165  *IOR   *ORE
CN 01 VL 200      A7 007775 140016  *IPR   PRE
                                           *FEX   EEX
MODES 17 - *C90  *ESL  *BDM  *MM      IBP   BPI
STATS 01 - VNU   FPS   WS   *PS      ICM   MEC
                                           IMC   MCU
S0 000000 000000 000000 000000      IRT   RTI
S1 000000 000000 000000 000165      IIP   ICP
S2 000000 000000 000000 000001      IIO   IOI
S3 004211 137646 123450 164211      IPC   PCI
S4 000006 103030 000000 000000      IDL   DL
S5 004400 002010 000037 155153      IMI   MII
S6 000000 000000 000000 000040      FNX   NEX
S7 000000 000000 000000 000000
    
```

Figure 9. ORE Interrupt Failure Analysis Information

Because hardware triggers occur after a CPU exchanges with an unexpected flag, you should view the Failing Exchange Package information in the Failure Analysis block to determine which unexpected interrupt(s) occurred. SMON marks any interrupts that occur with an asterisk (\*).



The highlighted text in Figure 10 shows that an unexpected ORE interrupt occurred in the OS kernel.

```

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
                          Failure Analysis
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

** Hardware failure occurred within the UNICOS kernel
   (UNICOS revision: 8.0.2.4 or beyond)

Failing Exchange Package (read from Cray memory @ 1100):
-----
P      00007752525d  A0 007775 140113  IMODES 176000
IBA    000000000000  A1 000000 000000  IFLAGS 010000
ILA    01777776000  A2 000003 027606
DBA    000000000000  A3 000000 000200  *IRP   RPE
DLA    01777776000  A4 000000 002135  *IUM   MEU
                                     *IFP   FPE
PN 04 XA 1100      A6 000000 000165  *IOR   *ORE
CN 01 VL 200      A7 007775 140016  *IPR   PRE
                                     *FEX   EEX
MODES 17 - *C90  *ESL  *BDM  *MM      IBP   BPI
STATS 01 - VNU   FPS   WS   *PS      ICM   MEC
                                     IMC   MCU
S0 000000 000000 000000 000000      IRT   RTI
S1 000000 000000 000000 000165      IIP   ICP
S2 000000 000000 000000 000001      IIO   IOI
S3 004211 137646 123450 164211      IPC   PCI
S4 000006 103030 000000 000000      IDL   DL
S5 004400 002010 000037 155153      IMI   MII
S6 000000 000000 000000 000040      FNX   NEX
S7 000000 000000 000000 000000

```

Figure 10. ORE Interrupt Occurred

Scan backwards through the DM buffer data to determine the most recent memory references. In this example, the last memory reference that issued before the trigger was the 121700 instruction, as shown by the highlighted text in Figure 11.

Event	P Register	CIP	Coincidence	Hold Issue Conditions								IB Data Valid	Fetch Quiet	Wait Exch	CIP Valid	
				JB	JA	MEM	SR	FU	BT	V	S					A
000	0007752400c	075605	5	0	0	0	0	0	0	0	0	0	1	0	0	1
001	0007752400d	040640	5	0	0	0	0	0	0	0	0	0	1	0	0	1
002	0007752401c	051576	5	0	0	0	0	0	0	0	0	0	1	0	0	1
003	0007752401d	074705	5	0	0	0	0	0	0	0	0	0	1	0	0	1
004	0007752402a	040740	5	0	0	0	0	0	0	0	0	0	1	0	0	1
005	0007752402d	051657	5	0	0	0	0	0	0	0	0	0	1	0	0	1
006	0007752403a	040700	5	0	0	0	0	0	0	0	0	0	1	0	0	1
007	0007752403d	051567	5	0	0	0	0	0	0	0	0	0	1	1	0	1
010	0007752404a	130500	5	0	0	0	0	0	0	0	0	0	1	1	0	1
011	0007752404d	024162	5	0	0	0	0	0	0	0	0	0	1	1	0	1
012	0007752405a	101700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
013	0007752405d	042775	5	0	0	0	0	0	0	0	0	0	1	1	0	1
014	0007752406a	071617	5	0	0	0	0	0	0	0	0	0	1	1	0	1
015	0007752406b	046067	5	0	0	0	0	0	0	0	0	1	1	0	0	1
016	0007752406c	015000	5	0	0	0	0	0	0	0	0	1	1	0	0	1
017	0007752407b	024702	5	0	0	0	0	0	0	0	0	0	1	1	0	1
020	0007752407c	117100	5	0	0	0	0	0	0	0	0	0	1	1	0	1
021	0007752410b	101600	5	0	0	0	0	0	0	0	0	0	1	1	0	1
022	0007752411a	042776	5	0	0	0	0	0	0	0	0	0	1	1	0	1
023	0007752411b	071116	5	0	0	0	0	0	0	0	0	1	1	0	0	1
024	0007752411c	137100	5	0	0	0	0	0	0	0	0	1	1	0	0	1
025	0007752412b	101600	5	0	0	0	0	0	0	0	0	0	1	1	0	1
026	0007752413a	071616	5	0	0	0	0	0	0	0	0	0	1	1	0	1
027	0007752413b	046067	5	0	0	0	0	0	0	0	0	1	1	0	0	1
030	0007752413c	015000	5	0	0	0	0	0	0	0	0	1	1	0	0	1
031	0007752416c	121700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
032	0007752417b	040600	5	0	0	0	0	0	0	0	0	0	1	1	0	1
033	0007752420a	044076	5	0	0	0	0	0	0	0	0	1	1	0	0	1
034	0007752420b	015000	5	0	0	0	0	0	0	0	0	1	1	0	0	1
035	0007752421a	042277	5	0	0	0	0	0	0	0	0	0	1	1	0	1
036	0007752421b	006000	5	0	0	0	0	0	0	0	0	0	0	1	0	1
037	0007752422b	020700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
040	0007752423a	023620	5	0	0	0	0	0	0	0	0	0	1	1	0	1
041	0007752423b	032567	5	0	0	0	0	0	0	0	0	0	1	1	0	1
042	0007752423c	024702	5	0	0	0	0	0	0	0	0	0	1	1	0	1
043	0007752423d	137200	5	0	0	0	0	0	0	0	0	0	1	1	0	1
044	0007752424c	023610	5	0	0	0	0	0	0	0	0	0	1	1	0	1
045	0007752424d	030456	5	0	0	0	0	0	0	0	0	0	1	1	0	1
046	0007752425a	104200	5	0	0	0	0	0	0	0	0	1	1	0	0	1
047	0007752425d	117200	5	0	0	0	0	0	0	0	0	1	1	0	0	1
050	0007752426c	071002	5	0	0	0	0	0	0	0	0	0	1	1	0	1
051	0007752426d	014000	5	0	0	0	0	0	0	0	0	1	1	0	0	1
052	0007752427c	111200	5	0	0	0	0	0	0	0	0	0	1	1	0	1
053	0007752430b	102700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
054	0007752431a	111700	5	0	0	0	0	0	0	0	0	1	1	0	0	1
055	0007752431d	102700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
056	0007752432c	117100	5	0	0	0	0	0	0	0	0	1	1	0	0	1
057	0007752433b	112100	5	0	0	0	0	0	0	0	0	0	1	1	0	1
060	0007752434a	006000	5	0	0	0	0	0	0	0	0	0	0	1	0	1
061	0007752455a	022703	6	0	0	0	0	0	0	0	0	0	1	0	0	1
062	0007752455b	111700	6	0	0	0	0	0	0	0	0	0	1	0	0	1
063	0007752456a	074064	6	0	0	0	0	0	0	0	0	0	1	0	0	1
064	0007752456b	015000	6	0	0	0	0	0	0	0	0	1	0	0	0	1
065	0007752457a	024752	6	0	0	0	0	0	0	0	0	0	1	1	0	1
066	0007752457b	071017	6	0	0	0	0	0	0	0	0	0	1	1	0	1
067	0007752457c	014000	6	0	0	0	0	0	0	0	0	1	1	0	0	1
070	0007752522d	043700	7	0	0	0	0	0	0	0	0	0	1	0	0	1
071	0007752523a	024702	7	0	0	0	0	0	0	0	0	0	1	0	0	1
072	0007752523b	137700	7	0	0	0	0	0	0	0	0	0	1	0	0	1
073	0007752524a	024702	7	0	0	0	0	0	0	0	0	0	1	0	0	1
074	0007752524b	107100	7	0	0	0	0	0	0	0	0	0	1	0	0	1
075	0007752525a	121700	7	0	0	0	0	0	0	0	0	1	1	0	0	1
076	0005652547c	002770	0	0	0	0	0	0	0	0	0	0	1	0	0	1
077	0005652547d	001000	0	1	0	0	0	0	0	0	0	0	1	0	0	1

Figure 11. Most Recent Memory Reference Instruction

The data listed in the Coincidence column of the DM buffer data indicates the instruction issued from instruction buffer 7, as shown by the highlighted text in Figure 12.

Event	P Register	CIP	Coincidence	Hold Issue Conditions										IB Data Valid	Fetch Quiet	Wait Exch	CIP Valid	
				JB	JA	MEM	SR	FU	BT	V	S	A						
075	0007752525a	121700	7	0	0	0	0	0	0	0	0	0	0	1	1	0	0	1
076	0005652547c	002770	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1
077	0005652547d	001000	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1

Figure 12. Instruction Issued from Instruction Buffer 7

View the data from instruction buffer 7. The instruction buffer data indicates that a fetch problem caused the ORE interrupt. The CPU picked bit 3 when fetching data into instruction buffer number 7, indicated by the difference value of 000010 shown in Figure 13. This resulted in a reference to memory address 02001340075, which is greater than the data limit address (DLA) of 01777776000, shown by the highlighted text in Figure 14.

INSTRUCTION BUFFER 7 (Memory IBA 0000000000)												
ADDRESS	EXPECTED (memory)					ACTUAL (IB data)					DIFFERENCE	
0007752500	000171	006000	052513	000177	00	000171	006000	052513	000177	-----	-----	-----
0007752501	024756	071017	014000	052513	01	024756	071017	014000	052513	-----	-----	-----
0007752502	000177	003416	120000	025452	02	000177	003416	120000	025452	-----	-----	-----
0007752503	000025	015000	052426	000177	03	000025	015000	052426	000177	-----	-----	-----
0007752504	120000	025447	000025	014000	04	120000	025447	000025	014000	-----	-----	-----
0007752505	052456	000177	020700	025445	05	052456	000177	020700	025445	-----	-----	-----
0007752506	000025	025751	022710	025750	06	000025	025751	022710	025750	-----	-----	-----
0007752507	043700	040740	002174	000000	07	043700	040740	002174	000000	-----	-----	-----
0007752510	040600	155153	000037	051576	10	040600	155153	000037	051576	-----	-----	-----
0007752511	075561	020600	155272	000037	11	075561	020600	155272	000037	-----	-----	-----
0007752512	007000	110253	000133	006000	12	007000	110253	000133	006000	-----	-----	-----
0007752513	052466	000177	024765	043701	13	052466	000177	024765	043701	-----	-----	-----
0007752514	057707	130700	025447	000025	14	057707	130700	025447	000025	-----	-----	-----
0007752515	002700	003616	024765	071717	15	002700	003616	024765	071717	-----	-----	-----
0007752516	054771	043600	075605	040640	16	054771	043600	075605	040640	-----	-----	-----
0007752517	000000	000400	051576	074705	17	000000	000400	051576	074705	-----	-----	-----
0007752520	040740	002174	000000	051657	20	040740	002174	000000	051657	-----	-----	-----
0007752521	040700	155153	000037	051567	21	040700	155153	000037	051567	-----	-----	-----
0007752522	130500	025450	000025	043700	22	130500	025450	000025	043700	-----	-----	-----
0007752523	024702	137700	000057	000000	23	024702	137700	000057	000000	-----	-----	-----
0007752524	024702	107100	000057	000000	24	024702	107100	000057	000010	-----	-----	000010
0007752525	121700	025436	000025	024702	25	121700	025436	000025	024702	-----	-----	-----
0007752526	040600	000020	000000	075605	26	040600	000020	000000	075605	-----	-----	-----
0007752527	127600	000057	000000	074505	27	127600	000057	000000	074505	-----	-----	-----
0007752530	027670	137700	000063	000000	30	027670	137700	000063	000000	-----	-----	-----
0007752531	071716	061475	046375	137700	31	071716	061475	046375	137700	-----	-----	-----
0007752532	000060	000000	050453	051004	32	000060	000000	050453	051004	-----	-----	-----
0007752533	016000	056440	000177	020700	33	016000	056440	000177	020700	-----	-----	-----
0007752534	001750	000000	022620	023560	34	001750	000000	022620	023560	-----	-----	-----
0007752535	032656	032557	024702	107100	35	032656	032557	024702	107100	-----	-----	-----
0007752536	000060	000000	075603	030761	36	000060	000000	075603	030761	-----	-----	-----
0007752537	040700	000100	000000	127600	37	040700	000100	000000	127600	-----	-----	-----

Figure 13. Instruction Buffer 7 Contents

```

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
                          Failure Analysis
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

** Hardware failure occurred within the UNICOS kernel
   (UNICOS revision: 8.0.2.4 or beyond)

Failing Exchange Package (read from Cray memory @ 1100):
-----
P      00007752525d  A0 007775 140113  IMODES 176000
IBA    000000000000  A1 000000 000000  IFLAGS 010000
ILA    017777760000  A2 000003 027606
DBA    000000000000  A3 000000 000200      *IRP  RPE
DLA    017777760000  A4 000000 002135      *IUM  MEU
                                A5 000000 001750      *IFP  FPE
PN 04 XA 1100      A6 000000 000165      *IOR  *ORE
CN 01 VL 200      A7 007775 140016      *IPR  PRE
                                *FEX  EEX
MODES 17 - *C90  *ESL  *BDM  *MM      IBP  BPI
STATS 01 - VNU  FPS  WS  *PS      ICM  MEC
                                IMC  MCU
S0 000000 000000 000000 000000      IRT  RTI
S1 000000 000000 000000 000165      IIP  ICP
S2 000000 000000 000000 000001      IIO  IOI
S3 004211 137646 123450 164211      IPC  PCI
S4 000006 103030 000000 000000      IDL  DL
S5 004400 002010 000037 155153      IMI  MII
S6 000000 000000 000000 000040      FNX  NEX
S7 000000 000000 000000 000000

```

Figure 14. Data Limit Address

## Software Trap in User Code

View the Failure Analysis information. SMON indicates a Software failure caused the trigger, as shown by the highlighted text in Figure 15.

```

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
                                Failure Analysis
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

** Software failure occurred within a user job running under UNICOS
   (UNICOS revision: 8.0.2.4 or beyond)

Failing Exchange Package:
-----
(For a SOFTWARE failure, the failing exchange package is the
 'EXCHANGE PACKAGE in the CPU before IB dump', which is shown
 in the 'Instruction Buffer Dump' section of this file.)

```

Figure 15. User Code Software Trap Failure Analysis Information

User software trigger sequences are compiled within a user program to trap a software problem. The output is meaningful only to the programmer or analyst who placed the trigger sequence in the code. Obtain the consent of the system administrator and Cray Research personnel responsible for the system before using the software trigger sequences.

Figure 16 shows the source code used in this example. This code illustrates how an analyst should first identify a failure condition and then strategically place the user software trigger in the code to troubleshoot the problem.

**C Source Code:**

```

/*****
* USER_SW.C - Demonstrates use of the SMON software trap. *
*           Two integer arrays are initialized to non-zero *
*           values. The data in the arrays are randomized *
*           until the defined failure condition is met. *
*****/
extern void SmonTrap();
#define FAILING_CONDITION ((Va[015] & 0177777) == 0123456)
#define VL 0155
main()
{
    int Element, Va[VL], Vb[VL];

    for(Element=0; Element<VL; Element++)
        Va[Element] = Vb[Element] = Element+1;

    while(! FAILING_CONDITION)
        for(Element=0; Element<VL; Element++)
        {
            Vb[Element] = (Va[Element]<<1) + (Vb[Element]>>1);
            Va[Element] = (Va[Element]>>1) + (Vb[Element]<<1);
        }

    SmonTrap(); /* issue user-software trigger sequence */
}

```

**Assembly Source Code for Trap Routine:**

```

*****
* SMON_TRAP.S - SMON user-software trigger sequence. *
*****
                IDENT      SmonTrap

SmonTrap      ENTER

                VWD        D'16/O'002740    ; DM trigger instruction
                VWD        D'16/O'001000    ; indicates online trap
                VWD        D'16/O'001000    ; extra pad for ibdump

                EXIT

```

Figure 16. User Source Code

The failing condition is shown in element 15 of vector register 6. Parcel 3 equals 123456. Refer to Figure 17.

Vector Register 6					Vector Register 7				
V06-000	032213	055502	106250	031521	V07-000	024737	052416	000562	170016
V06-001	122004	060766	032110	034746	V07-001	110520	051046	100572	165442
V06-002	162373	136254	000513	164406	V07-002	123355	140176	132115	002340
V06-003	030022	127003	143736	143063	V07-003	022732	031450	055503	057660
V06-004	045232	122362	053643	052232	V07-004	153164	144457	044061	005542
V06-005	171662	164567	127653	011504	V07-005	144767	074530	001227	151014
V06-006	026770	067513	156120	130734	V07-006	016037	165026	151253	000254
V06-007	056214	150111	040063	047466	V07-007	001305	105144	171534	136744
V06-010	114553	037006	000533	007261	V07-010	057360	154146	071044	014162
V06-011	041354	153114	146010	026016	V07-011	156577	116356	016063	046056
V06-012	107160	052025	151032	002112	V07-012	103356	173565	003147	127674
V06-013	126357	054160	125427	122633	V07-013	106666	070767	165173	113530
V06-014	055054	160024	151743	047054	V07-014	165261	136377	116477	161254
V06-015	036264	032047	115133	123456	V07-015	007324	046347	060155	065242

Figure 17. Failing Condition in Element 15 of Vector Register 7

The scalar compare instruction (046057) recorded in the DM buffer is where the failing condition was flagged. Refer to Figure 18.

Event	P Register	CIP	Coincidence	Hold Issue Conditions								IB Data Valid	Fetch Quiet	Wait Exch	CIP Valid	
				JB	JA	MEM	SR	FU	BT	V	S					A
000	0000001000a	176500	4	0	0	0	0	0	0	0	0	0	1	1	0	1
001	0000001000b	025003	4	0	1	0	0	0	0	0	0	0	1	1	0	1
002	0000001000c	150450	4	0	0	0	0	0	0	0	0	0	1	1	0	1
003	0000001000d	020600	4	0	0	0	0	0	0	0	0	0	1	1	0	1
004	0000001001c	030056	4	0	0	0	0	0	0	0	0	0	1	1	0	1
005	0000001001d	176300	4	0	0	0	0	0	0	0	0	0	1	1	0	1
006	0000001002a	042760	4	0	1	0	0	0	0	0	0	0	1	1	0	1
007	0000001002b	151230	4	0	0	0	0	1	0	0	0	0	1	1	0	1
010	0000001002c	155142	4	0	0	0	0	0	0	0	0	0	1	1	0	1
011	0000001002d	177010	4	0	0	0	0	0	0	0	0	0	1	1	0	1
012	0000001003a	024003	4	0	1	0	0	0	0	0	0	0	1	1	0	1
013	0000001003b	151050	4	0	0	0	0	1	0	0	0	0	1	1	0	1
014	0000001003c	150710	4	0	0	0	0	1	0	1	0	0	1	1	0	1
015	0000001003d	155607	4	0	0	0	0	0	0	0	0	0	1	1	0	1
016	0000001004a	177060	4	0	0	0	0	0	0	0	0	0	1	1	0	1
017	0000001004b	125600	4	0	1	1	0	0	0	0	0	0	1	1	0	1
020	0000001005a	044576	4	0	0	0	0	0	0	0	0	1	1	1	0	1
021	0000001005b	040700	4	0	0	0	0	0	0	0	0	0	1	1	0	1
022	0000001006a	046057	4	0	0	0	0	0	0	0	0	0	1	1	0	1
023	0000001006b	015000	4	0	0	0	0	0	0	0	0	1	1	1	0	1
024	0000000776b	020700	3	0	0	0	0	0	0	0	0	0	1	1	0	1
025	0000000777a	024502	3	0	0	0	0	0	0	0	0	0	1	1	0	1
026	0000000777b	022605	3	0	0	0	0	0	0	0	0	0	0	1	0	1
027	0000000777c	030056	4	0	0	0	0	0	0	0	0	0	0	1	0	1
030	0000000777d	002007	4	0	0	0	0	0	0	0	0	0	0	1	0	1
031	0000001000a	176500	4	0	0	0	0	0	0	0	0	0	1	1	0	1
032	0000001000b	025003	4	0	1	0	0	0	0	0	0	0	1	1	0	1
033	0000001000c	150450	4	0	0	0	0	0	0	0	0	0	1	1	0	1
034	0000001000d	020600	4	0	0	0	0	0	0	0	0	0	1	1	0	1
035	0000001001c	030056	4	0	0	0	0	0	0	0	0	0	1	1	0	1
036	0000001001d	176300	4	0	0	0	0	0	0	0	0	0	1	1	0	1
037	0000001002a	042760	4	0	1	0	0	0	0	0	0	0	1	1	0	1
040	0000001002b	151230	4	0	0	0	0	1	0	0	0	0	1	1	0	1
041	0000001002c	155142	4	0	0	0	0	0	0	0	0	0	1	1	0	1
042	0000001002d	177010	4	0	0	0	0	0	0	0	0	0	1	1	0	1
043	0000001003a	024003	4	0	1	0	0	0	0	0	0	0	1	1	0	1
044	0000001003b	151050	4	0	0	0	0	1	0	0	0	0	1	1	0	1
045	0000001003c	150710	4	0	0	0	0	1	0	1	0	0	1	1	0	1
046	0000001003d	155607	4	0	0	0	0	0	0	0	0	0	1	1	0	1
047	0000001004a	177060	4	0	0	0	0	0	0	0	0	0	1	1	0	1
050	0000001004b	125600	4	0	1	1	0	0	0	0	0	0	1	1	0	1
051	0000001005a	044576	4	0	0	0	0	0	0	0	0	1	1	1	0	1
052	0000001005b	040700	4	0	0	0	0	0	0	0	0	0	1	1	0	1
053	0000001006a	046057	4	0	0	0	0	0	0	0	0	0	1	1	0	1
054	0000001006b	015000	4	0	0	0	0	0	0	0	0	1	1	1	0	1
055	0000001007a	020600	4	0	0	0	0	0	0	0	0	0	1	1	0	1
056	0000001007d	007000	4	0	0	0	0	0	0	0	0	0	0	1	0	1
057	0000000744a	020000	3	0	0	0	0	0	0	0	0	0	1	1	0	1
060	0000000744d	025077	3	0	0	0	0	0	0	0	0	0	1	1	0	1
061	0000000745a	022504	3	0	0	0	0	0	0	0	0	0	1	1	0	1
062	0000000745b	024066	3	0	0	0	0	0	0	0	0	0	1	1	0	1
063	0000000745c	035577	3	0	0	0	0	0	0	0	0	0	1	1	0	1
064	0000000745d	024766	3	0	0	0	0	0	1	0	0	0	1	1	0	1
065	0000000746a	022504	3	0	0	0	0	0	0	0	0	0	1	1	0	1
066	0000000746b	030557	3	0	0	0	0	0	0	0	0	0	1	1	0	1
067	0000000746c	025601	3	0	0	0	0	0	0	0	0	0	1	1	0	1
070	0000000746d	025702	3	0	0	0	0	0	0	0	0	0	1	1	0	1
071	0000000747a	024767	3	0	0	0	0	0	1	0	0	0	1	1	0	1
072	0000000747b	031075	3	0	0	0	0	0	0	0	0	0	1	1	0	1
073	0000000747c	025566	3	0	0	0	0	0	0	0	0	0	1	1	0	1
074	0000000747d	024702	3	0	0	0	0	0	1	0	0	0	1	1	0	1
075	0000000750a	013000	3	0	0	0	0	0	0	0	0	1	1	1	0	1
076	0000000750d	002740	3	0	0	0	0	0	0	0	0	0	1	1	0	1
077	0000000751a	001000	3	1	0	0	0	0	0	0	0	0	1	1	0	1

Figure 18. 046057 Instruction in the DM Buffer



The CPU does not exchange before a software trap. The failing exchange package is actually the current exchange package, which is labeled EXCHANGE PACKAGE in the CPU before IB dump.

In the failing exchange package for this example, S5 equals the test pattern in S7. Refer to Figure 19.

```

EXCHANGE PACKAGE in the CPU before IB dump
-----
P      00000000751c  A0 000000 003421  IMODES 176665
IBA    00050740000  A1 000000 000005  IFLAGS 000000
ILA    00051130000  A2 000000 107333
DBA    00050740000  A3 000000 107672      *IRP   RPE
DLA    00051130000  A4 000000 113317      *IUM   MEU
                                           A5 000000 107676      *IFP   FPE
PN 11  XA 0000      A6 000000 040273      *IOR   ORE
CN 15  VL 155      A7 000000 107672      *IPR   PRE
                                           *FEX   EEX
MODES 16 - *C90    *ESL  *BDM  MM      IBP   BPI
STATS 00 - VNU    FPS   WS   PS      *ICM   MEC
                                           *IMC   MCU
S0 000000 000000 000000 000000      IRT   RTI
S1 000000 000000 000000 163160      *IIP   ICP
S2 000000 000000 000000 034654      *IIO   IOI
S3 000000 000000 000000 163060      IPC   PCI
S4 000000 000000 000000 163514      *IDL   DL
S5 000000 000000 000000 123456      IMI   MII
S6 036264 032047 115133 123456      *FNX   NEX
S7 000000 000000 000000 123456
    
```

Figure 19. Failing Exchange Package

## Tips for Using SMON to Detect Uncorrectable Memory Errors

---

The following tips apply to using SMON with uncorrectable memory errors:

- In cases where the MEU occurred during a scalar register memory read, you can find the failing data pattern in the `Failing Exchange Package` data. First, identify which reference caused the MEU by finding the reference that was addressing the failing bank.

For user jobs, the expected data pattern in memory is overwritten unless you selected a trigger action that causes the CPU to permanently hold issue after a trigger sequence. Otherwise, you can manually read the expected data pattern from memory with the `View -> Memory` menu button command in the LME base window.

- In cases where the MEU occurred while reading into the vector registers, the B registers, or the T registers, the failing data pattern is recorded in the register dump. Isolation in these cases will be more difficult.
- Do not use the LME `View -> Memory` utility to view mainframe memory after an uncorrectable memory error failure occurs unless the system has already crashed. The memory viewer utility uses a CPU's maintenance channel DMA port to read memory. If a maintenance channel read results in an MEU interrupt while the CPU is in a kernel process, the system will panic.

If the system has not crashed, use the UNIX octal dump (`od`) utility or `crash` utility to view mainframe memory.

- In cases where the MEU occurred during a channel transfer, it is very difficult to locate the failing data pattern when the MEU occurs during a channel transfer.

# Appendix

This appendix contains the complete SMON output for the two examples included in this document:

- An ORE interrupt in the OS kernel
- A software trap in the user code

## ORE Interrupt in the OS Kernel

---

```
*****
                LME-C90 4.1.8      System Monitor Dump Summary
S/N 4027  CPU 04 (logical 04) triggered at:  Mon Feb 13 13:55:39 1995
*****

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
                                CPU/System Status
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

CPU Status:      100040 000103 177777 000000

CPU-related Status                                System-related Status
-----
Memory:      256K mode           = 0                System I/O Master Clear      = 0
              Half memory mode  = 0                System CPU Master Clear     = 0
              Half upper select = 0
CPU:         Idle                = 0                Function Error                = 0
                                                System Error                  = 0
                                                Control Cable Enables 0     = 1
                                                Control Cable Enables 1     = 0
                                                Memory Priority            = 03
                                                CPUs Present Mask         = 177777
                                                CPU Parity Errors Mask    = 000000

              I/O ECC Enabled   = 0
              Test Mode         = 0
              Maint Restricted   = 0

Diag Monitor: Active            = 0
               Parity Error     = 0

Shared Register Select          = 0

Master CPU Select               = 00

Memory Priority Check
-----
Memory priority is NOT LOCKED

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
                DM Params set by the System Monitor (SMON)
                and the resulting DM data buffer
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

DM Params:
-----
MODES      02      DISA *HICU  HICD  TPD  IBD  ECHO      00: 005000
DATA       20      [LWR P] [CIP] [UPPR P] [COIN/HOLD]    01: 101001
TP SEL     00      OPT=YE0                                02: 000200
                                                03: 002740
EVENT      01      *VCIP  CIPM  PLPM  TP  ~TP  LEAD     04: 177747
TRIGGER    02      VCIP  *CIPM  PLPM  TP  ~TP  LEAD     05: 000000
TR MODE    1      *CPCT  TRCT                                06: 000000
TR RESET   0      CIPM  PLPM                                07: 000000
                                                10: 000000
DTC        000200                                        11: 000000
                                                12: 000000
CIP VAL    002740      P VAL    0000000000a      13: 000000
CIP MASK   177747      P MASK    00000a


```

```

TP0 (YE /xx /HA /DA ) 00      TP4 (YG /YK0/JC /xx ) 00
TP1 (YF0/xx /HC /DB ) 00      TP5 (YH /YK1/JQ /JQ ) 00
TP2 (YF1/YM0/JA /DC ) 00      TP6 (YJ0/xx /VQ0/xx ) 00
TP3 (YF2/YM1/JB /DD ) 00      TP7 (YJ1/xx /VQ1/SIE) 00
    
```

DM Data:  
-----

Buffer Contains 64 valid words of DM data

Event	P Register	CIP	Coincidence	Hold Issue Conditions								IB Data Valid	Fetch Quiet	Wait Exch	CIP Valid	
				JB	JA	MEM	SR	FU	BT	V	S					A
000	0007752400c	075605	5	0	0	0	0	0	0	0	0	0	1	0	0	1
001	0007752400d	040640	5	0	0	0	0	0	0	0	0	0	1	0	0	1
002	0007752401c	051576	5	0	0	0	0	0	0	0	0	0	1	0	0	1
003	0007752401d	074705	5	0	0	0	0	0	0	0	0	0	1	0	0	1
004	0007752402a	040740	5	0	0	0	0	0	0	0	0	0	1	0	0	1
005	0007752402d	051657	5	0	0	0	0	0	0	0	0	0	1	0	0	1
006	0007752403a	040700	5	0	0	0	0	0	0	0	0	0	1	0	0	1
007	0007752403d	051567	5	0	0	0	0	0	0	0	0	0	1	1	0	1
010	0007752404a	130500	5	0	0	0	0	0	0	0	0	0	1	1	0	1
011	0007752404d	024162	5	0	0	0	0	0	0	0	0	0	1	1	0	1
012	0007752405a	101700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
013	0007752405d	042775	5	0	0	0	0	0	0	0	0	0	1	1	0	1
014	0007752406a	071617	5	0	0	0	0	0	0	0	0	0	1	1	0	1
015	0007752406b	046067	5	0	0	0	0	0	0	0	0	1	1	0	0	1
016	0007752406c	015000	5	0	0	0	0	0	0	0	0	1	1	0	0	1
017	0007752407b	024702	5	0	0	0	0	0	0	0	0	0	1	1	0	1
020	0007752407c	117100	5	0	0	0	0	0	0	0	0	0	1	1	0	1
021	0007752410b	101600	5	0	0	0	0	0	0	0	0	0	1	1	0	1
022	0007752411a	042776	5	0	0	0	0	0	0	0	0	0	1	1	0	1
023	0007752411b	071116	5	0	0	0	0	0	0	0	0	1	1	0	0	1
024	0007752411c	137100	5	0	0	0	0	0	0	0	0	1	1	0	0	1
025	0007752412b	101600	5	0	0	0	0	0	0	0	0	0	1	1	0	1
026	0007752413a	071616	5	0	0	0	0	0	0	0	0	1	1	0	0	1
027	0007752413b	046067	5	0	0	0	0	0	0	0	0	1	1	0	0	1
030	0007752413c	015000	5	0	0	0	0	0	0	0	0	1	1	0	0	1
031	0007752416c	121700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
032	0007752417b	040600	5	0	0	0	0	0	0	0	0	0	1	1	0	1
033	0007752420a	044076	5	0	0	0	0	0	0	0	0	1	1	0	0	1
034	0007752420b	015000	5	0	0	0	0	0	0	0	0	1	1	0	0	1
035	0007752421a	042277	5	0	0	0	0	0	0	0	0	0	1	1	0	1
036	0007752421b	006000	5	0	0	0	0	0	0	0	0	0	0	1	0	1
037	0007752422b	020700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
040	0007752423a	023620	5	0	0	0	0	0	0	0	0	0	1	1	0	1
041	0007752423b	032567	5	0	0	0	0	0	0	0	0	1	1	0	0	1
042	0007752423c	024702	5	0	0	0	0	0	0	0	0	0	1	1	0	1
043	0007752423d	137200	5	0	0	0	0	0	0	0	0	0	1	1	0	1
044	0007752424c	023610	5	0	0	0	0	0	0	0	0	0	1	1	0	1
045	0007752424d	030456	5	0	0	0	0	0	0	0	0	1	1	0	0	1
046	0007752425a	104200	5	0	0	0	0	0	0	0	0	1	1	0	0	1
047	0007752425d	117200	5	0	0	0	0	0	0	0	0	1	1	0	0	1
050	0007752426c	071002	5	0	0	0	0	0	0	0	0	0	1	1	0	1
051	0007752426d	014000	5	0	0	0	0	0	0	0	0	1	1	0	0	1
052	0007752427c	111200	5	0	0	0	0	0	0	0	0	0	1	1	0	1
053	0007752430b	102700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
054	0007752431a	111700	5	0	0	0	0	0	0	0	0	1	1	0	0	1
055	0007752431d	102700	5	0	0	0	0	0	0	0	0	0	1	1	0	1
056	0007752432c	117100	5	0	0	0	0	0	0	0	0	1	1	0	0	1
057	0007752433b	112100	5	0	0	0	0	0	0	0	0	0	1	1	0	1
060	0007752434a	006000	5	0	0	0	0	0	0	0	0	0	0	1	0	1
061	0007752455a	022703	6	0	0	0	0	0	0	0	0	0	1	0	0	1
062	0007752455b	111700	6	0	0	0	0	0	0	0	0	0	1	0	0	1
063	0007752456a	074064	6	0	0	0	0	0	0	0	0	0	1	0	0	1
064	0007752456b	015000	6	0	0	0	0	0	0	0	0	1	1	0	0	1
065	0007752457a	024752	6	0	0	0	0	0	0	0	0	0	1	1	0	1
066	0007752457b	071017	6	0	0	0	0	0	0	0	0	0	1	1	0	1
067	0007752457c	014000	6	0	0	0	0	0	0	0	0	1	1	0	0	1
070	0007752522d	043700	7	0	0	0	0	0	0	0	0	0	1	0	0	1
071	0007752523a	024702	7	0	0	0	0	0	0	0	0	0	1	0	0	1
072	0007752523b	137700	7	0	0	0	0	0	0	0	0	0	1	0	0	1
073	0007752524a	024702	7	0	0	0	0	0	0	0	0	0	1	0	0	1
074	0007752524b	107100	7	0	0	0	0	0	0	0	0	0	1	0	0	1
075	0007752525a	121700	7	0	0	0	0	0	0	0	0	1	1	0	0	1
076	0005652547c	002770	0	0	0	0	0	0	0	0	0	0	1	0	0	1
077	0005652547d	001000	0	1	0	0	0	0	0	0	0	0	1	0	0	1

```

*****
Failure Analysis
*****
    
```

\*\* Hardware failure occurred within the UNICOS kernel  
(UNICOS revision: 8.0.2.4 or beyond)

Failing Exchange Package (read from Cray memory @ 1100):

```

-----
P 00007752525d A0 007775 140113 IMODES 176000
IBA 000000000000 A1 000000 000000 IFLAGS 010000
    
```

```

ILA 01777776000 A2 000003 027606
DBA 00000000000 A3 000000 000200 *IRP RPE
DLA 01777776000 A4 000000 002135 *IUM MEU
A5 000000 001750 *IFP FPE
PN 04 XA 1100 A6 000000 000165 *IOR *ORE
CN 01 VL 200 A7 007775 140016 *IPR PRE
*FEX EEX
MODES 17 - *C90 *ESL *BDM *MM IBP BPI
STATS 01 - VNU *FPS WS *PS ICM MEC
IMC MCU
S0 000000 000000 000000 000000 IRT RTI
S1 000000 000000 000000 000165 IIP ICP
S2 000000 000000 000000 000001 IIO IOI
S3 004211 137646 123450 164211 IPC PCI
S4 000006 103030 000000 000000 IDL DL
S5 004400 002010 000037 155153 IMI MII
S6 000000 000000 000000 000040 FNX NEX
S7 000000 000000 000000 000000
    
```

User Job Exchange Package (read from Cray memory @ 1140):

```

-----
P 00000003526b A0 000000 000004 IMODES 156665
IBA 00044734000 A1 000000 000004 IFLAGS 000000
ILA 00045210000 A2 000000 000006
DBA 00044734000 A3 000000 000055 *IRP RPE
DLA 00045210000 A4 177777 177777 *IUM MEU
A5 000000 023573 IFP FPE
PN 04 XA 1140 A6 000000 023776 *IOR ORE
CN 15 VL 200 A7 000000 002003 *IPR PRE
*FEX EEX
MODES 14 - *C90 *ESL BDM MM IBP BPI
STATS 04 - VNU *FPS WS PS ICM MEC
*IMC MCU
S0 000000 000000 000000 000055 IRT RTI
S1 000000 034250 102040 151622 *IIP ICP
S2 000000 000000 000000 000055 *IIO IOI
S3 177777 177777 177777 177777 IPC PCI
S4 177777 177777 177777 177777 *IDL DL
S5 000000 000700 000000 000000 IMI MII
S6 000000 000000 000003 177777 *FNX NEX
S7 000000 000000 000000 000077
    
```

```

*****
Instruction Buffer Dump
(with buffer-to-memory data compare)
*****
    
```

INSTRUCTION BUFFER 0 (Memory IBA 000000000)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
0005652540	073701 054724 055704 023670	00 073701 054724 055704 023670	-----
0005652541	020200 000133 000000 032662	01 020200 000133 000000 032662	-----
0005652542	020200 010565 000001 030662	02 020200 010565 000001 030662	-----
0005652543	106100 000104 000000 121500	03 106100 000104 000000 121500	-----
0005652544	000006 000000 043720 045557	04 000006 000000 043720 045557	-----
0005652545	131500 000006 000000 120300	05 131500 000006 000000 120300	-----
0005652546	050247 000027 120400 050250	06 050247 000027 120400 050250	-----
0005652547	000027 004000 002770 001000	07 000027 004000 002770 001000	-----
0005652550	001000 002770 001000 001000	10 001000 002770 001000 001000	-----
0005652551	100000 152005 000023 011000	11 100000 152005 000023 011000	-----
0005652552	037463 000135 100000 023243	12 037463 000135 100000 023243	-----
0005652553	000022 010000 052732 000135	13 000022 010000 052732 000135	-----
0005652554	100100 016255 000022 001413	14 100100 016255 000022 001413	-----
0005652555	020100 000500 000000 022720	15 020100 000500 000000 022720	-----
0005652556	022404 032474 073701 054724	16 022404 032474 073701 054724	-----
0005652557	055704 023770 032474 030114	17 055704 023770 032474 030114	-----
0005652560	030501 040100 000021 000000	20 030501 040100 000021 000000	-----
0005652561	071201 020600 050274 000027	21 071201 020600 050274 000027	-----
0005652562	007000 175573 000133 024601	22 007000 175573 000133 024601	-----
0005652563	024702 030105 073701 054724	23 024702 030105 073701 054724	-----
0005652564	055704 023670 020700 000133	24 055704 023670 020700 000133	-----
0005652565	000000 032667 020700 010565	25 000000 032667 020700 010565	-----
0005652566	000001 030667 001302 073201	26 000001 030667 001302 073201	-----
0005652567	054224 055204 022303 023420	27 054224 055204 022303 023420	-----
0005652570	032334 121400 000000 000000	30 032334 121400 000000 000000	-----
0005652571	133400 063522 000001 073671	31 133400 063522 000001 073671	-----
0005652572	133600 063523 000001 002700	32 133600 063523 000001 002700	-----
0005652573	073771 133700 063524 000001	33 073771 133700 063524 000001	-----
0005652574	043300 073375 121100 000006	34 043300 073375 121100 000006	-----
0005652575	000000 051201 055220 051301	35 000000 051201 055220 051301	-----
0005652576	054260 051002 015000 053006	36 054260 051002 015000 053006	-----
0005652577	000135 120500 050210 000027	37 000135 120500 050210 000027	-----

INSTRUCTION BUFFER 1 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
00	000177 121700	000005 000000	
01	043601 044076	015000 051057	
02	000177 121000	000156 000000	
03	015000 051057	000177 121000	
04	000155 000000	121100 000022	
05	000000 015000	051057 000177	
06	054160 053120	015000 051057	
07	000177 121700	000022 000000	
10	054740 053720	121100 000005	
11	000000 015000	051057 000177	
12	040700 000200	000000 051617	
13	131600 000005	000000 121000	
14	000162 000000	015000 051142	
15	000177 020700	002225 000000	
16	042777 024664	040740 000664	
17	020000 030567	022716 024602	
20	136700 000016	000000 116500	
21	000017 000000	030676 007000	
22	035460 000133	024702 137100	
23	000067 000000	024702 127000	
24	000067 000000	014000 051142	
25	000177 024702	127100 000056	
26	000000 024002	024202 022110	
27	025066 034177	030021 022106	
30	036100 005000	024761 127200	
31	000207 000000	024702 137200	
32	000024 000000	051002 014000	
33	051476 000177	061002 075200	
34	017000 051222	000177 042776	
35	024602 040740	000677 020000	
36	040600 155161	000037 040500	
37	155163 000037	040400 000677	

INSTRUCTION BUFFER 2 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
00	000000 044076	015000 051431	
01	000177 101700	000004 000000	
02	042777 071617	046067 101200	
03	000021 000000	015000 051476	
04	000177 040700	000031 000000	
05	071612 061076	016000 051476	
06	000177 040700	000000 000002	
07	044017 014000	051463 000177	
10	043700 040740	002000 000000	
11	044017 014000	051463 000177	
12	041700 000000	000002 024702	
13	044117 137100	000025 000000	
14	006000 051473	000177 040700	
15	000000 000002	024702 051117	
16	137100 000025	000000 131100	
17	000005 000000	024764 127000	
20	003031 000000	017000 051561	
21	000177 024762	107600 000012	
22	000000 071006	015000 051677	
23	000177 020700	000223 000000	
24	042777 024664	040740 000752	
25	020000 030567	022716 024602	
26	136700 000016	000000 116500	
27	000017 000000	030676 007000	
30	036674 000133	120000 010533	
31	000001 014000	051677 000177	
32	020600 155267	000037 007000	
33	172713 000132	006000 051677	
34	000177 024761	127700 000000	
35	000000 040600	100000 000000	
36	044076 015000	051677 000177	
37	024764 040700	000010 000000	

INSTRUCTION BUFFER 3 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
00	037043 000133	000000 000000	
01	073163 060566	062440 020040	
02	000000 000000	000005 000005	
03	000000 000000	000133 036674	
04	000400 000000	000000 000000	
05	000000 000000	000000 000000	
06	000000 000000	000000 000004	
07	000000 000000	000000 000000	
10	073162 062563	072040 020040	
11	000000 000000	000005 000005	
12	000000 000000	000133 037020	
13	000400 000000	000000 000000	
14	000000 000000	000000 000000	
15	000000 000000	000000 000004	
16	000000 000000	000000 000000	
17	020000 147542	000026 025077	

20	022504	024066	035577	024766					
21	022504	030557	025601	025702					
22	024767	031075	025566	024702					
23	013000	036554	000133	106100					
24	000001	000000	024664	126000					
25	003031	000000	126100	002224					
26	000000	017000	037012	000133					
27	024563	105300	000016	000000					
30	030113	002003	040200	000002					
31	000000	061012	014000	036756					
32	000133	016000	037012	000133					
33	030001	177010	030113	030001					
34	177020	143777	030113	030001					
35	177030	143666	030113	030001					
36	177040	030113	030001	177050					
37	030113	030001	177060	001302					

INSTRUCTION BUFFER 4 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)				ACTUAL (IB data)				DIFFERENCE
00	030113	030001	040100	000010	030113	030001	040100	000010	
01	000000	001302	136100	002224	000000	001302	136100	002224	
02	000000	177070	024702	030070	000000	177070	024702	030070	
03	025766	022703	034700	005000	025766	022703	034700	005000	
04	020000	147551	000026	025077	020000	147551	000026	025077	
05	022504	024066	035577	024766	022504	024066	035577	024766	
06	022504	030557	025601	025702	022504	030557	025601	025702	
07	024767	031075	025566	024702	024767	031075	025566	024702	
10	013000	036567	000133	024664	013000	036567	000133	024664	
11	126000	003031	000000	126100	126000	003031	000000	126100	
12	002224	000000	040200	000002	002224	000000	040200	000002	
13	000000	017000	037131	000133	000000	017000	037131	000133	
14	061012	017000	037131	000133	061012	017000	037131	000133	
15	024563	105300	000016	000000	024563	105300	000016	000000	
16	020500	000223	000000	030165	020500	000223	000000	030165	
17	030113	002003	030001	176100	030113	002003	030001	176100	
20	014000	037125	000133	030113	014000	037125	000133	030113	
21	030001	176200	030113	030001	030001	176200	030113	030001	
22	176300	030113	030001	176400	176300	030113	030001	176400	
23	030113	030001	176500	030113	030113	030001	176500	030113	
24	030001	176600	030113	030001	030001	176600	030113	030001	
25	176700	042177	136100	002224	176700	042177	136100	002224	
26	000000	024702	030070	025766	000000	024702	030070	025766	
27	022703	034700	005000	000000	022703	034700	005000	000000	
30	020100	024043	024565	072163	020100	024043	024565	072163	
31	027543	030457	066544	027541	027543	030457	066544	027541	
32	072155	074143	064147	027163	072155	074143	064147	027163	
33	020040	020040	020040	020070	020040	020040	020040	020070	
34	030056	032440	020040	020060	030056	032440	020040	020060	
35	034457	030062	027471	031040	034457	030062	027471	031040	
36	030461	035061	033072	030066	030461	035061	033072	030066	
37	000000	000000	000000	000000	000000	000000	000000	000000	

INSTRUCTION BUFFER 5 (Memory IBA 000000000)

ADDRESS	EXPECTED (memory)				ACTUAL (IB data)				DIFFERENCE
0007752400	054771	043600	075605	040640	054771	043600	075605	040640	
0007752401	000000	000400	051576	074705	000000	000400	051576	074705	
0007752402	040740	002010	000000	051657	040740	002010	000000	051657	
0007752403	040700	155153	000037	051567	040700	155153	000037	051567	
0007752404	130500	025450	000025	024162	130500	025450	000025	024162	
0007752405	101700	000004	000000	042775	101700	000004	000000	042775	
0007752406	071617	046067	015000	052270	071617	046067	015000	052270	
0007752407	000177	024702	117100	000070	000177	024702	117100	000070	
0007752410	000000	101600	000021	000000	000000	101600	000021	000000	
0007752411	042776	071116	137100	000072	042776	071116	137100	000072	
0007752412	000000	101600	000004	000000	000000	101600	000004	000000	
0007752413	071616	046067	015000	052072	071616	046067	015000	052072	
0007752414	000177	002700	004000	040700	000177	002700	004000	040700	
0007752415	155167	000037	130700	005031	155167	000037	130700	005031	
0007752416	000000	002700	121700	000005	000000	002700	121700	000005	
0007752417	000000	040600	000040	000000	000000	040600	000040	000000	
0007752420	044076	015000	052110	000177	044076	015000	052110	000177	
0007752421	042277	006000	052111	000177	042277	006000	052111	000177	
0007752422	043200	020700	001750	000000	043200	020700	001750	000000	
0007752423	023620	032567	024702	137200	023620	032567	024702	137200	
0007752424	000071	000000	023610	030456	000071	000000	023610	030456	
0007752425	104200	021456	000025	117200	104200	021456	000025	117200	
0007752426	000067	000000	071002	014000	000067	000000	071002	014000	
0007752427	052163	000177	111200	000025	052163	000177	111200	000025	
0007752430	000000	102700	000026	000000	000000	102700	000026	000000	
0007752431	111700	000026	000000	102700	111700	000026	000000	102700	
0007752432	000026	000000	117100	000025	000026	000000	117100	000025	
0007752433	000000	112100	000026	000000	000000	112100	000026	000000	
0007752434	006000	052264	000177	071730	006000	052264	000177	071730	
0007752435	024702	040600	000100	000000	024702	040600	000100	000000	
0007752436	061576	111100	000025	000000	061576	111100	000025	000000	
0007752437	063675	061571	111100	000026	063675	061571	111100	000026	

INSTRUCTION BUFFER 6 (Memory IBA 0000000000)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
0007752440	000000 107600 000071 000000	00 000000 107600 000071 000000	-----
0007752441	070460 063375 071540 050510	01 070460 063375 071540 050510	-----
0007752442	062253 022520 067564 042672	02 062253 022520 067564 042672	-----
0007752443	044316 064545 061463 020400	03 044316 064545 061463 020400	-----
0007752444	001750 000000 032365 023540	04 001750 000000 032365 023540	-----
0007752445	064425 042577 126300 025436	05 064425 042577 126300 025436	-----
0007752446	000025 075505 056505 063274	06 000025 075505 056505 063274	-----
0007752447	061472 032564 023440 030234	07 061472 032564 023440 030234	-----
0007752450	061764 122600 025376 000025	10 061764 122600 025376 000025	-----
0007752451	023470 023310 074705 030553	11 023470 023310 074705 030553	-----
0007752452	056704 115100 021456 000025	12 056704 115100 021456 000025	-----
0007752453	051465 051637 132400 025376	13 051465 051637 132400 025376	-----
0007752454	000025 136600 025436 000025	14 000025 136600 025436 000025	-----
0007752455	022703 111700 000004 000000	15 022703 111700 000004 000000	-----
0007752456	074064 015000 052301 000177	16 074064 015000 052301 000177	-----
0007752457	024752 071017 014000 052513	17 024752 071017 014000 052513	-----
0007752460	000177 120700 023334 000022	20 000177 120700 023334 000022	-----
0007752461	024702 127200 000026 000000	21 024702 127200 000026 000000	-----
0007752462	044072 120100 010541 000001	22 044072 120100 010541 000001	-----
0007752463	015000 052323 000177 044012	23 015000 052323 000177 044012	-----
0007752464	015000 052513 000177 024765	24 015000 052513 000177 024765	-----
0007752465	127700 021436 000025 042677	25 127700 021436 000025 042677	-----
0007752466	024661 060576 137500 021436	26 024661 060576 137500 021436	-----
0007752467	000025 126700 000000 000000	27 000025 126700 000000 000000	-----
0007752470	040600 100000 000000 044076	30 040600 100000 000000 044076	-----
0007752471	015000 052513 000177 024162	31 015000 052513 000177 024162	-----
0007752472	101700 000004 000000 042776	32 101700 000004 000000 042776	-----
0007752473	071617 046067 015000 052513	33 071617 046067 015000 052513	-----
0007752474	000177 042777 024602 040740	34 000177 042777 024602 040740	-----
0007752475	001066 020000 022716 136700	35 001066 020000 022716 136700	-----
0007752476	000016 000000 116100 000017	36 000016 000000 116100 000017	-----
0007752477	000000 030676 007000 103623	37 000000 030676 007000 103623	-----

INSTRUCTION BUFFER 7 (Memory IBA 0000000000)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
0007752500	000171 006000 052513 000177	00 000171 006000 052513 000177	-----
0007752501	024756 071017 014000 052513	01 024756 071017 014000 052513	-----
0007752502	000177 003416 120000 025452	02 000177 003416 120000 025452	-----
0007752503	000025 015000 052426 000177	03 000025 015000 052426 000177	-----
0007752504	120000 025447 000025 014000	04 120000 025447 000025 014000	-----
0007752505	052456 000177 020700 025445	05 052456 000177 020700 025445	-----
0007752506	000025 025751 022710 025750	06 000025 025751 022710 025750	-----
0007752507	043700 040740 002174 000000	07 043700 040740 002174 000000	-----
0007752510	040600 155153 000037 051576	10 040600 155153 000037 051576	-----
0007752511	075561 020600 155272 000037	11 075561 020600 155272 000037	-----
0007752512	007000 110253 000133 006000	12 007000 110253 000133 006000	-----
0007752513	052466 000177 024765 043701	13 052466 000177 024765 043701	-----
0007752514	057707 130700 025447 000025	14 057707 130700 025447 000025	-----
0007752515	002700 003616 024765 071717	15 002700 003616 024765 071717	-----
0007752516	054771 043600 075605 040640	16 054771 043600 075605 040640	-----
0007752517	000000 000400 051576 074705	17 000000 000400 051576 074705	-----
0007752520	040740 002174 000000 051657	20 040740 002174 000000 051657	-----
0007752521	040700 155153 000037 051567	21 040700 155153 000037 051567	-----
0007752522	130500 025450 000025 043700	22 130500 025450 000025 043700	-----
0007752523	024702 137700 000057 000000	23 024702 137700 000057 000000	-----
0007752524	024702 107100 000057 000000	24 024702 107100 000057 000010	----- 000010
0007752525	121700 025436 000025 024702	25 121700 025436 000025 024702	-----
0007752526	040600 000020 000000 075605	26 040600 000020 000000 075605	-----
0007752527	127600 000057 000000 074505	27 127600 000057 000000 074505	-----
0007752530	027670 137700 000063 000000	30 027670 137700 000063 000000	-----
0007752531	071716 061475 046375 137700	31 071716 061475 046375 137700	-----
0007752532	000060 000000 050453 051004	32 000060 000000 050453 051004	-----
0007752533	016000 056440 000177 020700	33 016000 056440 000177 020700	-----
0007752534	001750 000000 022620 023560	34 001750 000000 022620 023560	-----
0007752535	032656 032557 024702 107100	35 032656 032557 024702 107100	-----
0007752536	000060 000000 075603 030761	36 000060 000000 075603 030761	-----
0007752537	040700 000100 000000 127600	37 040700 000100 000000 127600	-----

EXCHANGE PACKAGE in the CPU before IB dump

```

P 00005652550b A0 000000 000000 IMODES 000000
IBA 00000000000 A1 000000 001100 IFLAGS 000000
ILA 01777776000 A2 000000 000000
DBA 00000000000 A3 000000 000000 IRP RPE
DLA 01777776000 A4 000000 000000 IUM MEU
A5 000000 000000 IFP FPE
PN 04 XA 0000 A6 000001 011341 IOR ORE
CN 01 VL 200 A7 000000 000000 IPR PRE
FEX EXX
MODES 11 - *C90 ESL BDM *MM IBP BPI
STATS 00 - VNU FPS WS PS ICM MEC
IMC MCU
S0 000000 000000 000000 000000 IRT RTI
S1 000000 000000 000000 000000 IIP ICP
S2 000000 000000 000000 000000 IIO IOI
S3 000000 000000 000000 000000 IPC PCI
S4 000000 000000 000000 000000 IDL DL

```



```

S5 000000 000000 000000 000000      IMI  MII
S6 000000 000000 000000 000000      FNX  NEX
S7 000000 000000 000000 000000

```

RESTART EXCHANGE PACKAGE after IB dump

```

-----
P      00005652550b  A0 000000 000000  IMODES 000000
IBA 000000000000    A1 000000 001100  IFLAGS 000000
ILA 01777776000    A2 000000 000000
DBA 000000000000    A3 000000 000000      IRP  RPE
DLA 01777776000    A4 000000 000000      IUM  MEU
                        A5 000000 000000      IFP  FPE
PN 04 XA 1100      A6 000001 011341      IOR  ORE
CN 01 VL 200      A7 000000 000000      IPR  PRE
                        FEK  EEX
MODES 11 - *C90    ESL  BDM  *MM      IBP  BPI
STATS 00 - VNU    FPS  WS   PS        ICM  MEC
                        IMC  MCU
S0 000000 000000 000000 000000      IRT  RTI
S1 000000 000000 000000 000000      IIP  ICP
S2 000000 000000 000000 000000      IIO  IOI
S3 000000 000000 000000 000000      IPC  PCI
S4 000000 000000 000000 000000      IDL  DL
S5 000000 000000 000000 000000      IMI  MII
S6 000000 000000 000000 000000      FNX  NEX
S7 000000 000000 000000 000000

```

Register Dump  
(V, B, T, SB, ST, SM, VM, and VM1 registers)

```

-----
Vector Register 0
V00-000 000000 000000 000000 000000
V00-001 000000 000000 000000 000000
V00-002 000000 000000 000000 000000
V00-003 000000 000000 000000 000000
V00-004 000000 000000 000000 000000
V00-005 000000 000000 000000 000000
V00-006 000000 000000 000000 000000
V00-007 000000 000000 000000 000000
V00-010 000000 000000 000000 000000
V00-011 000000 000000 000000 000000
V00-012 000000 000000 000000 000000
V00-013 000000 000000 000000 000000
V00-014 000000 000000 000000 000000
V00-015 000000 000000 000000 000000
V00-016 000000 000000 000000 000000
V00-017 000000 000000 000000 000000
V00-020 000000 000000 000000 000000
V00-021 000000 000000 000000 000000
V00-022 000000 000000 000000 000000
V00-023 000000 000000 000000 000000
V00-024 000000 000000 000000 000000
V00-025 000000 000000 000000 000000
V00-026 000000 000000 000000 000000
V00-027 000000 000000 000000 000000
V00-030 000000 000000 000000 000000
V00-031 000000 000000 000000 000000
V00-032 000000 000000 000000 000000
V00-033 000000 000000 000000 000000
V00-034 000000 000000 000000 000000
V00-035 000000 000000 000000 000000
V00-036 000000 000000 000000 000000
V00-037 000000 000000 000000 000000
V00-040 000000 000000 000000 000000
V00-041 000000 000000 000000 000000
V00-042 000000 000000 000000 000000
V00-043 000000 000000 000000 000000
V00-044 000000 000000 000000 000000
V00-045 000000 000000 000000 000000
V00-046 000000 000000 000000 000000
V00-047 000000 000000 000000 000000
V00-050 000000 000000 000000 000000
V00-051 000000 000000 000000 000000
V00-052 000000 000000 000000 000000
V00-053 000000 000000 000000 000000
V00-054 000000 000000 000000 000000
V00-055 000000 000000 000000 000000
V00-056 000000 000000 000000 000000
V00-057 000000 000000 000000 000000
V00-060 000000 000000 000000 000000
V00-061 000000 000000 000000 000000
V00-062 000000 000000 000000 000000
V00-063 000000 000000 000000 000000
V00-064 000000 000000 000000 000000
V00-065 000000 000000 000000 000000
V00-066 000000 000000 000000 000000
V00-067 000000 000000 000000 000000
V00-070 000000 000000 000000 000000

Vector Register 1
V01-000 000000 000000 000000 000000
V01-001 000000 000000 000000 000000
V01-002 000000 000000 000000 000000
V01-003 000000 000000 000000 000000
V01-004 000000 000000 000000 000000
V01-005 000000 000000 000000 000000
V01-006 000000 000000 000000 000000
V01-007 000000 000000 000000 000000
V01-010 000000 000000 000000 000000
V01-011 000000 000000 000000 000000
V01-012 000000 000000 000000 000000
V01-013 000000 000000 000000 000000
V01-014 000000 000000 000000 000000
V01-015 000000 000000 000000 000000
V01-016 000000 000000 000000 000000
V01-017 000000 000000 000000 000000
V01-020 000000 000000 000000 000000
V01-021 000000 000000 000000 000000
V01-022 000000 000000 000000 000000
V01-023 000000 000000 000000 000000
V01-024 000000 000000 000000 000000
V01-025 000000 000000 000000 000000
V01-026 000000 000000 000000 000000
V01-027 000000 000000 000000 000000
V01-030 000000 000000 000000 000000
V01-031 000000 000000 000000 000000
V01-032 000000 000000 000000 000000
V01-033 000000 000000 000000 000000
V01-034 000000 000000 000000 000000
V01-035 000000 000000 000000 000000
V01-036 000000 000000 000000 000000
V01-037 000000 000000 000000 000000
V01-040 000000 000000 000000 000000
V01-041 000000 000000 000000 000000
V01-042 000000 000000 000000 000000
V01-043 000000 000000 000000 000000
V01-044 000000 000000 000000 000000
V01-045 000000 000000 000000 000000
V01-046 000000 000000 000000 000000
V01-047 000000 000000 000000 000000
V01-050 000000 000000 000000 000000
V01-051 000000 000000 000000 000000
V01-052 000000 000000 000000 000000
V01-053 000000 000000 000000 000000
V01-054 000000 000000 000000 000000
V01-055 000000 000000 000000 000000
V01-056 000000 000000 000000 000000
V01-057 000000 000000 000000 000000
V01-060 000000 000000 000000 000000
V01-061 000000 000000 000000 000000
V01-062 000000 000000 000000 000000
V01-063 000000 000000 000000 000000
V01-064 000000 000000 000000 000000
V01-065 000000 000000 000000 000000
V01-066 120614 040350 105514 052000
V01-067 173062 144365 071563 016011
V01-070 144355 110743 034474 002310

```





V02-157 067523 103744 033527 011174  
V02-160 037120 144516 113060 101246  
V02-161 052573 102516 064335 134275  
V02-162 001750 170507 071120 103777  
V02-163 022761 106605 020575 144012  
V02-164 017206 021364 077664 114357  
V02-165 037731 057770 103060 160273  
V02-166 041464 160105 154330 027643  
V02-167 167664 023275 021053 173705  
V02-170 133077 032774 052634 155070  
V02-171 004113 127043 005616 016010  
V02-172 157073 060515 174003 002322  
V02-173 070040 136546 044055 063302  
V02-174 011557 175374 015712 170567  
V02-175 114060 073445 047247 107535  
V02-176 165324 051021 046254 017327  
V02-177 142233 043655 075125 034272

V03-157 165105 075757 175621 136677  
V03-160 144263 166660 064444 170302  
V03-161 113565 100125 060005 115572  
V03-162 072552 103402 120173 140772  
V03-163 061103 140470 162356 051173  
V03-164 110346 054400 042245 041351  
V03-165 034175 131406 121221 177341  
V03-166 006260 076524 155042 015073  
V03-167 135524 134026 016527 056052  
V03-170 121023 104753 014066 143600  
V03-171 153620 045724 065201 006212  
V03-172 136466 037631 041633 134541  
V03-173 176610 042765 160712 113262  
V03-174 031136 137357 030242 062544  
V03-175 160775 155273 157323 151304  
V03-176 031065 010657 074327 146456  
V03-177 162246 122575 141272 166352

Vector Register 4  
V04-000 160000 112767 151170 000000  
V04-001 060000 070243 016300 000000  
V04-002 060000 061053 076730 000000  
V04-003 040642 135407 160650 000000  
V04-004 121637 100005 114210 000000  
V04-005 160000 012765 021130 000000  
V04-006 060000 150447 106530 000000  
V04-007 160000 111404 037150 000000  
V04-010 060000 014305 056560 000000  
V04-011 160000 116535 163050 000000  
V04-012 160000 135373 156440 000000  
V04-013 060000 105655 060460 000000  
V04-014 160000 112330 142450 000000  
V04-015 027633 064422 155460 000000  
V04-016 160000 153070 045330 000000  
V04-017 023014 162446 054220 000000  
V04-020 060000 143340 032230 000000  
V04-021 160000 100015 033170 000000  
V04-022 060000 065076 031400 000000  
V04-023 160000 122301 152270 000000  
V04-024 026316 006332 074310 000000  
V04-025 160000 134002 111600 000000  
V04-026 060000 104626 151360 000000  
V04-027 060000 046252 170440 000000  
V04-030 060000 104632 051340 000000  
V04-031 060000 104470 113240 000000  
V04-032 160000 152735 013560 000000  
V04-033 160000 151641 002300 000000  
V04-034 160000 120513 066610 000000  
V04-035 160000 025536 167030 000000  
V04-036 060000 001136 052370 000000  
V04-037 060000 002352 052130 000000  
V04-040 160000 121752 151040 000000  
V04-041 060000 142661 141640 000000  
V04-042 060000 022101 053510 000000  
V04-043 160000 035034 016130 000000  
V04-044 160000 107651 141000 000000  
V04-045 060000 033621 104260 000000  
V04-046 160000 113575 100320 000000  
V04-047 060000 153104 046040 000000  
V04-050 060000 064340 146170 000000  
V04-051 060000 114753 037550 000000  
V04-052 050726 026022 063320 000000  
V04-053 160000 005035 135500 000000  
V04-054 000000 000000 000000 000000  
V04-055 022204 116706 031260 000000  
V04-056 060000 012706 160440 000000  
V04-057 060000 103655 116010 000000  
V04-060 160000 025505 122230 000000  
V04-061 160000 005402 021630 000000  
V04-062 000000 000000 000000 000000  
V04-063 160000 120766 076260 000000  
V04-064 000000 000000 000000 000000  
V04-065 160000 152002 157010 000000  
V04-066 156502 130417 167157 171772  
V04-067 054771 045621 123460 132171  
V04-070 135125 034445 121737 034334  
V04-071 134405 010031 143553 001113  
V04-072 111566 130570 002125 030071  
V04-073 014505 047710 065405 151022  
V04-074 072661 051235 052575 143602  
V04-075 064114 045230 001664 006263  
V04-076 077162 021350 021074 135571  
V04-077 113045 057373 052153 120737  
V04-100 064420 175542 014533 170554  
V04-101 061271 060227 072214 107021  
V04-102 114626 126165 070471 001647  
V04-103 177544 050143 004423 021650  
V04-104 006313 072615 141236 041470  
V04-105 137661 065044 075264 167407  
V04-106 143116 046410 170464 134512  
V04-107 016104 067721 104663 112457  
V04-110 001251 031746 046161 042371

Vector Register 5  
V05-000 060000 000000 064663 075720  
V05-001 060000 000000 035640 114344  
V05-002 060000 000000 026513 032610  
V05-003 021501 000000 122147 002553  
V05-004 000000 000000 000000 000000  
V05-005 060000 000000 001104 012457  
V05-006 060000 000000 146630 145351  
V05-007 060000 000000 062624 156545  
V05-010 060000 000000 001342 040710  
V05-011 060000 000000 072143 052014  
V05-012 060000 000000 122121 162572  
V05-013 060000 000000 055661 041312  
V05-014 060000 000000 064040 055240  
V05-015 000000 000000 000000 000000  
V05-016 060000 000000 153655 003622  
V05-017 000000 000000 000000 000000  
V05-020 060000 000000 134742 135502  
V05-021 060000 000000 046420 064377  
V05-022 060000 000000 032414 132315  
V05-023 060000 000000 077623 111541  
V05-024 000000 000000 000000 000000  
V05-025 060000 000000 117442 101151  
V05-026 060000 000000 054370 126717  
V05-027 060000 000000 015640 012451  
V05-030 060000 000000 054375 032222  
V05-031 060000 000000 054176 173711  
V05-032 060000 000000 153365 105173  
V05-033 060000 000000 151175 105324  
V05-034 060000 000000 075105 041477  
V05-035 060000 000000 004327 025641  
V05-036 060000 000000 000006 137726  
V05-037 060000 000000 000035 010655  
V05-040 060000 000000 077107 071130  
V05-041 060000 000000 133657 004655  
V05-042 060000 000000 003055 075262  
V05-043 060000 000000 007736 140721  
V05-044 060000 000000 060400 006173  
V05-045 060000 000000 007203 026246  
V05-046 060000 000000 065733 112654  
V05-047 060000 000000 153705 032227  
V05-050 060000 000000 031662 002403  
V05-051 060000 000000 067530 002300  
V05-052 041651 000000 004440 166526  
V05-053 060000 000000 000173 020141  
V05-054 000000 000000 000000 000000  
V05-055 000000 000000 000000 000000  
V05-056 060000 000000 001072 111512  
V05-057 060000 000000 053204 060110  
V05-060 060000 000000 004314 157624  
V05-061 060000 000000 000221 146632  
V05-062 000000 000000 000000 000000  
V05-063 060000 000000 075511 022453  
V05-064 000000 000000 000000 000000  
V05-065 060000 000000 151500 056335  
V05-066 066324 174137 035361 133735  
V05-067 017225 047753 033604 015536  
V05-070 037101 050727 116355 041360  
V05-071 052334 060675 106211 177627  
V05-072 013101 132362 034512 002560  
V05-073 166324 032644 012456 071430  
V05-074 117236 124615 142346 036144  
V05-075 137274 027044 022170 060703  
V05-076 156747 160417 004315 130147  
V05-077 053551 035631 151740 052746  
V05-100 032141 024771 152447 007570  
V05-101 134670 125131 102041 116707  
V05-102 116206 004676 167567 112015  
V05-103 104732 146332 136535 056242  
V05-104 044425 177021 174330 145320  
V05-105 001316 011642 041043 123260  
V05-106 036703 111720 172503 102622  
V05-107 072141 013771 160740 175402  
V05-110 074672 174133 031451 062462





V06-176	075244	052555	035177	036170	V07-176	040604	122352	060657	123125
V06-177	170213	001015	035213	060301	V07-177	153342	102313	132567	136415

B Registers				T Registers			
B00	000177	051542		T00	000000	000000	000000 000020
B01	000037	155152		T01	000000	000000	000000 000001
B02	007775	140016		T02	000000	000000	000000 000000
B03	000000	000000		T03	000000	000000	000000 000000
B04	000135	070000		T04	000000	000000	000000 006063
B05	000001	010306		T05	000000	000000	000000 000000
B06	000000	006063		T06	000000	000000	000000 000000
B07	000000	000000		T07	000000	000000	000000 000000
B10	000000	000000		T10	000000	000000	000000 000000
B11	000000	000000		T11	000000	000000	000000 000000
B12	000000	000000		T12	000000	000000	000000 000000
B13	000000	000000		T13	000000	000000	000000 000000
B14	000000	000000		T14	000000	000000	000000 000000
B15	000000	000000		T15	000000	000000	000000 000000
B16	000000	000000		T16	000000	000000	000000 000000
B17	000000	000000		T17	000000	000000	000000 000000
B20	000000	000000		T20	000000	000000	000000 000000
B21	000000	000000		T21	000000	000000	000000 000000
B22	000000	000000		T22	000000	000000	000000 000000
B23	000000	000000		T23	000000	000000	000000 000000
B24	000000	000000		T24	000000	000000	000000 000000
B25	000000	000000		T25	000000	000000	000000 000000
B26	000000	000000		T26	000000	000000	000000 000000
B27	000000	000000		T27	000000	000000	000000 000000
B30	000000	000000		T30	000000	000000	000000 000000
B31	000000	000000		T31	000000	000000	000000 000000
B32	000000	000000		T32	000000	000000	000000 000000
B33	000000	000000		T33	000000	000000	000000 000000
B34	000000	000000		T34	000000	000000	000000 000000
B35	000000	000000		T35	000000	000000	000000 000000
B36	000000	000000		T36	000000	000000	000000 000000
B37	000000	000000		T37	000000	000000	000000 000000
B40	000000	000000		T40	000000	000000	000000 000000
B41	000000	000000		T41	000000	000000	000000 000000
B42	000000	000000		T42	000000	000000	000000 000000
B43	000000	000000		T43	000000	000000	000000 000000
B44	000000	000000		T44	000000	000000	000000 000000
B45	000000	000000		T45	000000	000000	000000 000000
B46	000000	000000		T46	000000	000000	000000 000000
B47	000000	000000		T47	000000	000000	000000 000000
B50	000000	000000		T50	000000	000000	000000 000000
B51	000000	000000		T51	000000	000000	000000 000000
B52	000000	000000		T52	000000	000000	000000 000000
B53	000000	000000		T53	000000	000000	000000 000000
B54	000000	001140		T54	000000	000000	000000 000000
B55	000001	011341		T55	000000	000000	000000 000000
B56	000000	000017		T56	000000	000000	000000 000000
B57	000000	000001		T57	000000	000000	000000 000000
B60	000000	000000		T60	000000	000000	000000 000000
B61	000003	041610		T61	000000	000000	000000 000000
B62	000003	041414		T62	000000	000000	000000 000000
B63	000223	124000		T63	000000	000000	000000 000000
B64	000223	130000		T64	000000	000000	000000 000000
B65	000000	000004		T65	177777	177777	177777 177777
B66	007775	140112		T66	000000	000000	000000 000000
B67	007775	142733		T67	000000	000000	000000 000000
B70	000000	123505		T70	101721	172223	010101 152623
B71	116033	156076		T71	123742	170034	133321 105423
B72	102517	043121		T72	124437	110413	000346 072223
B73	170537	116547		T73	021045	025765	004176 070036
B74	106345	115350		T74	052424	116347	156226 010465
B75	036133	153362		T75	003320	106153	044153 124647
B76	061617	121654		T76	050375	030523	016533 026646
B77	000026	147542		T77	074475	146134	014206 140676

SHARED REGISTERS for Cluster 1

Shared B Registers		Shared T Registers	
SB00	000000 000000	ST00	000000 000000 000000 000000
SB01	000000 000000	ST01	000000 000000 000000 000000
SB02	000000 000000	ST02	001113 000000 000000 000000
SB03	000000 000000	ST03	000000 000000 000000 000000
SB04	000000 000000	ST04	000000 000000 000000 000000
SB05	000000 000000	ST05	000000 000000 000000 000000
SB06	000000 000000	ST06	000001 000000 000000 000000
SB07	000000 000000	ST07	000000 000000 000000 000000
SM	000000 000000		
VM	177777 177777 177777 176000		
VM1	060000 173065 124136 054146		

```

#####
A-register Memory Dump
#####

```

```
Memory from A0 (A0 + DBA = 1777340113)
```

```

-----
1777340113 000000 000000 000177 051542
1777340114 000000 000000 000037 155152
1777340115 000000 000000 007775 140016
1777340116 000000 000000 000000 001750
1777340117 000000 000000 000000 001750
1777340120 000000 000000 000000 000020
1777340121 000000 000000 000003 001542
1777340122 000000 000000 000003 001346
1777340123 000000 000000 000000 000000
1777340124 040060 140000 000000 000000
1777340125 000000 000000 000003 001346
1777340126 000000 000000 000000 000047
1777340127 000000 000000 000000 000000
1777340130 020000 002010 000000 000001
1777340131 000000 000000 003732 066225
1777340132 000000 000000 000000 000070
1777340133 000000 000000 000047 016550
1777340134 000000 000000 000000 000000
1777340135 000000 000000 000000 000000
1777340136 000000 000000 000005 140000
1777340137 000000 000000 000000 000000
1777340140 020000 003526 000000 000001
1777340141 000000 004000 000000 020005
1777340142 000000 000000 000000 127620
1777340143 000000 000000 000000 045145
1777340144 000000 000000 000000 037262
1777340145 000000 000000 000000 000004
1777340146 000000 000000 000000 002665
1777340147 000000 000000 000000 000044
1777340150 000000 000000 000000 000000
1777340151 000000 000000 000000 000000
1777340152 000000 000000 000000 017004
1777340153 000000 000000 000000 012671
1777340154 000000 000000 000000 000000
1777340155 000000 000000 000000 000314
1777340156 000000 000000 000000 000000
1777340157 000000 000000 000000 000000
1777340160 000000 000000 000000 000000
1777340161 000000 000000 000000 006614
1777340162 000000 000000 000000 000000
1777340163 000000 000000 000000 004711
1777340164 000000 000000 000000 012003
1777340165 000000 000000 000000 000000
1777340166 000000 000000 000000 005072
1777340167 000000 000000 000000 016252
1777340170 000000 000000 000000 000000
1777340171 000000 000000 000000 002665
1777340172 000000 000000 000000 000000
1777340173 000000 000000 000000 000000
1777340174 000000 000000 000000 000000
1777340175 000000 000000 000000 000000
1777340176 000000 000000 000000 000000
1777340177 000000 000000 000000 000000
1777340200 000000 000000 000000 000000
1777340201 000000 000000 000000 000000
1777340202 000000 000000 000150 011663
1777340203 000000 000000 007775 140116
1777340204 000000 000000 007775 140123
1777340205 000000 000000 000046 034507
1777340206 000000 000000 000026 125134
1777340207 000000 000000 000132 142173
1777340210 000000 000000 007775 140034
1777340211 000000 000000 007775 140112
1777340212 020000 002444 000000 000001
1777340213 000000 000000 000000 174306
1777340214 000000 000000 000000 000001
1777340215 000000 000000 000000 000000
1777340216 000000 000000 000026 146246
1777340217 000000 000000 000177 002356
1777340220 000000 000000 000026 146326
1777340221 000000 000000 007775 140206
1777340222 000000 000000 000000 000000
1777340223 000000 000000 000000 000000
1777340224 000000 000000 000002 033514
1777340225 000000 000000 000000 000000
1777340226 000000 000000 000036 061175
1777340227 000000 000000 000177 031463
1777340230 000000 000000 007775 140140
1777340231 000000 000000 007775 140201
1777340232 000000 000000 000000 000000
1777340233 000000 000000 000036 061175
1777340234 000000 000000 000177 031463
1777340235 000000 000000 007775 140171
1777340236 000000 000000 007775 140206
1777340237 000000 000000 000000 000000
1777340240 000000 000000 000000 000001

```



```

1777340241 020000 000223 000000 000001
1777340242 000010 000000 000000 000000
1777340243 000000 000000 000000 000014
1777340244 000000 000000 000000 000000
1777340245 000000 000000 000036 015705
1777340246 000000 000000 000026 162651
1777340247 000000 000000 000177 061033
1777340250 000000 000000 007775 140213
1777340251 000000 000000 007775 140226
1777340252 000000 000000 000003 054534
1777340253 000000 000000 000026 162651
1777340254 000000 000000 000177 061033
1777340255 000000 000000 007775 140220
1777340256 000000 000000 007775 140233
1777340257 000000 000000 000002 033514
1777340260 000000 000000 000000 000047
1777340261 000000 000000 000000 000001
1777340262 000000 000000 000002 034116
1777340263 000000 000000 000002 034116
1777340264 000000 000000 000000 000000
1777340265 000011 171377 000000 000000
1777340266 000000 000000 000000 000377
1777340267 000000 000000 000000 000000
1777340270 000000 000000 000000 000030
1777340271 000000 000000 000000 000000
1777340272 000000 000000 000036 061175
1777340273 000000 000000 000177 031463
1777340274 000000 000000 007775 140230
1777340275 000000 000000 007775 140245
1777340276 000000 000000 000000 000000
1777340277 000000 000000 000000 000001
1777340300 020000 000223 000000 000001
1777340301 010000 000000 000000 000000
1777340302 000000 000000 000000 000003
1777340303 000000 000000 000000 000000
1777340304 000000 000000 000000 000000
1777340305 000000 000000 000017 075706
1777340306 000000 000000 000000 000000
1777340307 000000 000000 000036 015743
1777340310 000000 000000 000155 107513
1777340311 000000 000000 007775 140236
1777340312 000000 000000 007775 140254

```

```

Memory from A1 (A1 + DBA = 0000000000)
-----
0000000000 000000 000000 000000 000000
0000000001 000000 000000 000000 000000
0000000002 000000 000000 000000 000000
0000000003 000000 000000 000000 000000
0000000004 000000 000000 000000 000000
0000000005 000000 000000 000000 000000
0000000006 000000 000000 000000 000000
0000000007 000000 000000 000000 000000
0000000010 000000 000000 000000 000000
0000000011 000000 000000 000000 000000
0000000012 001113 000000 000000 000000
0000000013 000000 000000 000000 000000
0000000014 000000 000000 000000 000000
0000000015 000000 000000 000000 000000
0000000016 000001 000000 000000 000000
0000000017 000000 000000 000000 000000
0000000020 000000 000000 000000 000000
0000000021 177777 177777 177777 176000
0000000022 060000 173065 124136 054146
0000000023 000000 000000 000000 000000
0000000024 000000 000000 000000 000000
0000000025 000000 000000 000000 000000
0000000026 000000 000000 000000 000000
0000000027 000000 000000 000000 000000
0000000030 000000 000000 000000 000000
0000000031 000000 000000 000000 000000
0000000032 000000 000000 000000 000000
0000000033 000000 000000 000000 000000
0000000034 000000 000000 000000 000000
0000000035 000000 000000 000000 000000
0000000036 000000 000000 000000 000000
0000000037 000000 000000 000000 000000
0000000040 000000 000000 000000 000000
0000000041 000000 000000 000000 000000
0000000042 000000 000000 000000 000000
0000000043 000000 000000 000000 000000
0000000044 000000 000000 000000 000000
0000000045 000000 000000 000000 000000
0000000046 000000 000000 000000 000000
0000000047 000000 000000 000000 000000
0000000050 000000 000000 000000 000000
0000000051 000000 000000 000000 000000
0000000052 000000 000000 000000 000000
0000000053 000000 000000 000000 000000
0000000054 000000 000000 000000 000000
0000000055 000000 000000 000000 000000
0000000056 000000 000000 000000 000000

```

```

000000057 000000 000000 000000 000000
000000060 000000 000000 000000 000000
000000061 000000 000000 000000 000000
000000062 000000 000000 000000 000000
000000063 000000 000000 000000 000000
000000064 000000 000000 000000 000000
000000065 177777 177777 177777 177777
000000066 000000 000000 000000 000000
000000067 000000 000000 000000 000000
000000070 101721 172223 010101 152623
000000071 123742 170034 133321 105423
000000072 124437 110413 000346 072223
000000073 021045 025765 004176 070036
000000074 052424 116347 156226 010465
000000075 003320 106153 044153 124647
000000076 050375 030523 016533 026646
000000077 074475 146134 014206 140676
000000100 006000 000334 000000 120100
000000101 000047 000000 060441 060771
000000102 100100 000046 000000 025100
000000103 100000 000112 000000 100100
000000104 000113 000000 100200 000114
000000105 000000 100300 000115 000000
000000106 120000 000116 000000 120100
000000107 000117 000000 120200 000120
000000110 000000 005000 006000 157164
000000111 000000 000000 000000 000000
000000112 000000 000000 000000 000000
000000113 000000 000000 000000 000000
000000114 000000 000000 000000 000000
000000115 000000 000000 000000 000000
000000116 000000 000000 000000 000000
000000117 000000 000000 000000 000000
000000120 000000 000000 000000 000000
000000121 000000 000000 000000 000000
000000122 000000 000000 000000 000000
000000123 000000 000000 000000 000000
000000124 000000 000000 000000 000000
000000125 000000 000000 000000 000000
000000126 000000 000000 000000 000000
000000127 000000 000000 000000 000000
000000130 000000 000000 000000 000000
000000131 000000 000000 000000 000000
000000132 000000 000000 000000 000000
000000133 000000 000000 000000 000000
000000134 000000 000000 000000 000000
000000135 000000 000000 000000 000000
000000136 000000 000000 000000 000000
000000137 000000 000000 000000 000000
000000140 000000 000000 000000 000000
000000141 000000 000000 000000 000000
000000142 000000 000000 000000 000000
000000143 000000 000000 000000 000000
000000144 000000 000000 000000 000000
000000145 000000 000000 000000 000000
000000146 000000 000000 000000 000000
000000147 000000 000000 000000 000000
000000150 000000 000000 000000 000000
000000151 000000 000000 000000 000000
000000152 000000 000000 000000 000000
000000153 000000 000000 000000 000000
000000154 000000 000000 000000 000000
000000155 000000 000002 000000 000000
000000156 000000 000000 000000 000000
000000157 000000 000000 000000 000000
000000160 000000 000000 000000 000000
000000161 000000 000000 000000 000000
000000162 000000 000000 000000 000000
000000163 000000 000000 000000 000000
000000164 000000 000000 000000 000000
000000165 000000 000000 000000 000000
000000166 000000 000000 000000 000000
000000167 000000 000000 000000 000000
000000170 000000 000000 000000 000000
000000171 000000 000000 000000 000000
000000172 000000 000000 000000 000000
000000173 000000 000000 000000 000000
000000174 000000 000000 000000 000000
000000175 000000 000000 000000 000000
000000176 000000 000000 000000 000000
000000177 000000 000000 000000 000000

```

Memory from A2 (A2 + DBA = 0000627606)

```

-----
0000627606 000000 000000 000000 000000
0000627607 000000 000000 000000 000000
0000627610 067554 061546 070164 000001
0000627611 000000 000000 000000 000000
0000627612 000000 000000 000000 000003
0000627613 000000 000000 000000 000000
0000627614 000000 000000 000000 042264
0000627615 000000 000000 000003 027606

```

0000627616	000000	000000	000003	030002
0000627617	000000	000000	000000	000000
0000627620	000000	000000	000000	000000
0000627621	000000	000000	000000	010011
0000627622	000000	000000	000001	072020
0000627623	000000	000000	000000	000000
0000627624	000000	000000	000003	040647
0000627625	000000	000000	000002	142045
0000627626	000000	000000	000000	000000
0000627627	000000	000000	000000	000165
0000627630	000000	000000	000000	000000
0000627631	000000	000000	000000	000025
0000627632	000000	000000	000000	000000
0000627633	000000	000000	000003	041414
0000627634	000000	000000	000003	041414
0000627635	000000	000000	000000	000010
0000627636	000000	000000	000000	000003
0000627637	000000	000000	000000	000000
0000627640	000000	000001	035565	124531
0000627641	000000	000000	000410	041447
0000627642	000000	000000	000165	171421
0000627643	000000	000000	000000	000000
0000627644	000000	000001	035565	124531
0000627645	000000	000000	000000	000000
0000627646	000000	000000	000000	000000
0000627647	000000	000000	000000	161562
0000627650	000000	000000	000000	000000
0000627651	000000	000000	000000	000000
0000627652	000000	000000	000000	161561
0000627653	000000	100001	002107	100007
0000627654	177777	077772	001070	070400
0000627655	000000	000014	000600	007370
0000627656	000001	000000	002100	040000
0000627657	000000	000000	154000	000000
0000627660	000000	000000	020000	000000
0000627661	000000	000000	000000	000000
0000627662	000000	000000	000001	000634
0000627663	000000	000000	000001	004627
0000627664	000000	000000	000000	000016
0000627665	000000	000000	000000	000000
0000627666	000000	000000	000000	000000
0000627667	000000	000000	000000	000000
0000627670	000000	000000	000000	000000
0000627671	000000	000000	000000	000000
0000627672	000000	000000	000000	000000
0000627673	000000	000000	000000	000000
0000627674	000000	000000	000000	000000
0000627675	000000	000000	000000	000000
0000627676	000000	000000	000000	000000
0000627677	000211	137646	015440	177446
0000627700	000000	000000	000000	000001
0000627701	000000	000000	000000	000000
0000627702	000000	000000	000000	000000
0000627703	000000	000000	000000	000000
0000627704	000000	000000	000000	000000
0000627705	000000	000000	000000	000000
0000627706	000000	000000	000022	030263
0000627707	040010	130430	121161	004247
0000627710	000000	000000	000000	000165
0000627711	000000	000000	000000	000000
0000627712	000211	137617	047627	002042
0000627713	000211	137617	047623	133745
0000627714	000211	137646	015440	175616
0000627715	000000	000000	000000	000000
0000627716	000000	000000	000000	000000
0000627717	000000	000000	000000	000000
0000627720	000000	000000	000000	000000
0000627721	000000	000000	000000	000000
0000627722	000000	000000	000000	000000
0000627723	000000	000000	000000	000000
0000627724	000000	000000	000000	000000
0000627725	000000	000000	000000	000000
0000627726	000000	000000	000000	000000
0000627727	000000	000000	000000	000000
0000627730	000000	000000	000000	000000
0000627731	000000	000000	000000	000000
0000627732	000000	000000	000000	000000
0000627733	000000	000000	000000	000000
0000627734	000000	000000	000000	000000
0000627735	000000	000000	000000	000000
0000627736	000000	000000	000000	000000
0000627737	000000	000000	000000	000000
0000627740	000000	000000	000000	000000
0000627741	000000	000000	000000	000000
0000627742	000000	000000	000000	000000
0000627743	000000	000000	000000	000000
0000627744	000000	000000	000000	000000
0000627745	000000	000000	000000	000000
0000627746	000000	000000	000000	000000
0000627747	000000	000000	000000	000000
0000627750	000000	000000	000000	000000
0000627751	000000	000001	035565	124531

```

0000627752 000000 000001 034501 110757
0000627753 000000 000000 000407 020117
0000627754 000000 000000 000000 000000
0000627755 000000 000000 000000 000000
0000627756 000000 000000 000000 000000
0000627757 000000 000000 000000 000000
0000627760 000000 000000 000000 000000
0000627761 000000 000000 000000 000000
0000627762 000000 000000 000000 000000
0000627763 000000 000000 000000 000000
0000627764 000000 000000 000000 000000
0000627765 000000 000000 177777 177777
0000627766 177777 177777 177777 177777
0000627767 000000 000000 000000 000000
0000627770 000000 000000 000133 002157
0000627771 000000 000000 000000 000000
0000627772 000000 000000 000000 000000
0000627773 000000 000000 000000 000000
0000627774 000000 000000 000000 000000
0000627775 000000 000000 000000 000000
0000627776 000000 000000 000000 000000
0000627777 000000 000000 000000 000000
0000630000 000000 000000 000000 000000
0000630001 000000 000000 000000 000000
0000630002 000000 000000 000000 020001
0000630003 000000 000000 000000 042260
0000630004 000000 000000 000000 000250
0000630005 000000 000000 000000 000010

```

Memory from A3 (A3 + DBA = 000000200)

```

-----
0000000200 000000 000000 000000 000000
0000000201 000105 160000 000000 005672
0000000202 051524 040503 045514 044515
0000000203 000000 000000 007772 154000
0000000204 043525 042523 052124 040502
0000000205 000105 165670 000000 000002
0000000206 000000 000000 000000 000000
0000000207 000000 000000 000000 000000
0000000210 000000 000000 000000 000000
0000000211 000000 000000 000000 000000
0000000212 000000 000000 000000 000000
0000000213 000000 000000 000000 000000
0000000214 000000 000000 000000 000000
0000000215 000000 000000 000000 000000
0000000216 000000 000000 000000 000000
0000000217 000000 000000 000000 000000
0000000220 000000 000000 000000 000000
0000000221 000000 000000 000000 000000
0000000222 000000 000000 000000 000000
0000000223 000000 000000 000000 000000
0000000224 000000 000000 000000 000000
0000000225 000000 000000 000000 000000
0000000226 000000 000000 000000 000000
0000000227 000000 000000 000000 000000
0000000230 000000 000000 000000 000000
0000000231 000000 000000 000000 000000
0000000232 000000 000000 000000 000000
0000000233 000000 000000 000000 000000
0000000234 000000 000000 000000 000000
0000000235 000000 000000 000000 000000
0000000236 000000 000000 000000 000000
0000000237 000000 000000 000000 000000
0000000240 000000 000000 000000 000000
0000000241 000000 000000 000000 000000
0000000242 000000 000000 000000 000000
0000000243 000000 000000 000000 000000
0000000244 000000 000000 000000 000000
0000000245 000000 000000 000000 000000
0000000246 000000 000000 000000 000000
0000000247 000000 000000 000000 000000
0000000250 000000 000000 000000 000000
0000000251 000000 000000 000000 000000
0000000252 000000 000000 000000 000000
0000000253 000000 000000 000000 000000
0000000254 000000 000000 000000 000000
0000000255 000000 000000 000000 000000
0000000256 000000 000000 000000 000000
0000000257 000000 000000 000000 000000
0000000260 000000 000000 000000 000000
0000000261 000000 000000 000000 000000
0000000262 000000 000000 000000 000000
0000000263 000000 000000 000000 000000
0000000264 000000 000000 000000 000000
0000000265 000000 000000 000000 000000
0000000266 000000 000000 000000 000000
0000000267 000000 000000 000000 000000
0000000270 000000 000000 000000 000000
0000000271 000000 000000 000000 000000
0000000272 000000 000000 000000 000000
0000000273 000000 000000 000000 000000
0000000274 000000 000000 000000 000000

```

```

000000275 000000 000000 000000 000000
000000276 000000 000000 000000 000000
000000277 000000 000000 000000 000000
000000300 000000 000000 000000 000000
000000301 000000 000000 000000 000000
000000302 000000 000000 000000 000000
000000303 000000 000000 000000 000000
000000304 000000 000000 000000 000000
000000305 000000 000000 000000 000000
000000306 000000 000000 000000 000000
000000307 000000 000000 000000 000000
000000310 000000 000000 000000 000000
000000311 000000 000000 000000 000000
000000312 000000 000000 000000 000000
000000313 000000 000000 000000 000000
000000314 000000 000000 000000 000000
000000315 000000 000000 000000 000000
000000316 000000 000000 000000 000000
000000317 000000 000000 000000 000000
000000320 000000 000000 000000 000000
000000321 000000 000000 000000 000000
000000322 000000 000000 000000 000000
000000323 000000 000000 000000 000000
000000324 000000 000000 000000 000000
000000325 000000 000000 000000 000000
000000326 000000 000000 000000 000000
000000327 000000 000000 000000 000000
000000330 000000 000000 000000 000000
000000331 000000 000000 000000 000000
000000332 000000 000000 000000 000000
000000333 000000 000000 000000 000000
000000334 000000 000000 000000 000000
000000335 000000 000000 000000 000000
000000336 000000 000000 000000 000000
000000337 000000 000000 000000 000000
000000340 000000 000000 000000 000000
000000341 000000 000000 000000 000000
000000342 025154 067567 066545 066452
000000343 000000 000000 000000 001360
000000344 061541 061550 062440 020040
000000345 000000 000000 000020 154115
000000346 061541 066154 067565 072040
000000347 000000 000000 000021 105374
000000350 061550 060556 067145 066040
000000351 000000 000000 000023 022303
000000352 061554 067543 065440 020040
000000353 000000 000000 000022 017510
000000354 062551 067546 071145 062440
000000355 000000 000000 000001 020015
000000356 062551 067560 060543 065440
000000357 000000 000000 000001 020021
000000360 062551 067560 065545 067144
000000361 000000 000000 000001 020023
000000362 066551 067560 020040 020040
000000363 000000 000000 000001 020013
000000364 062556 062157 063144 060564
000000365 000000 000000 000023 022245
000000366 062562 071056 064040 020040
000000367 000000 000000 000022 004144
000000370 063151 066145 027150 020040
000000371 000000 000000 000011 147575
000000372 063154 067543 065456 064040
000000373 000000 000000 000020 152123
000000374 063163 066157 063456 064040
000000375 000000 000000 000022 011242
000000376 063163 072141 061154 062563
000000377 000000 000000 000023 013415

```

```

Memory from A4 (A4 + DBA = 000002135)
-----
0000002135 000000 000000 000000 000000
0000002136 000000 000000 000000 000000
0000002137 000000 000000 000000 000000
0000002140 000001 106152 000012 120517
0000002141 000003 011460 000012 120501
0000002142 000003 012742 000012 120502
0000002143 000003 011460 000012 120517
0000002144 000003 012742 000012 124327
0000002145 176665 000004 000012 120523
0000002146 000000 000000 000012 120511
0000002147 006016 043100 000012 120517
0000002150 000000 000000 000000 000000
0000002151 000000 000000 000012 120512
0000002152 000000 000000 000000 000001
0000002153 000211 137646 123450 156735
0000002154 000000 000000 000001 171235
0000002155 020000 000041 000000 000002
0000002156 000000 000000 000000 000002
0000002157 020000 000041 000000 000000
0000002160 000000 000000 000000 000000
0000002161 000000 000000 000000 000000
0000002162 000000 000000 000000 000000

```

```

0000002163 000000 000000 000000 000000
0000002164 000000 000000 000000 000000
0000002165 000000 000000 000000 000000
0000002166 000000 000000 000000 000000
0000002167 000000 000000 000000 000000
0000002170 000000 000000 000000 000000
0000002171 000000 000000 000000 000000
0000002172 000000 000000 000000 000000
0000002173 000000 000000 000000 000000
0000002174 000000 000000 000000 000000
0000002175 000000 000000 000000 000000
0000002176 000000 000000 000000 000000
0000002177 000000 000000 000000 000000
0000002200 000135 052636 000000 000000
0000002201 000000 000000 000000 002200
0000002202 000003 177777 000000 000000
0000002203 000000 000000 000000 000000
0000002204 000003 177777 000000 000000
0000002205 000000 000011 000000 000000
0000002206 000000 000000 000001 013024
0000002207 006401 044000 000000 000000
0000002210 000000 000000 000000 000000
0000002211 000000 000000 000000 000000
0000002212 000000 000000 000000 000000
0000002213 000000 000000 000000 000000
0000002214 000000 000000 000000 000000
0000002215 000000 000000 000000 000000
0000002216 000000 000000 000000 000000
0000002217 000000 000000 000000 000000
0000002220 000000 000000 000000 000000
0000002221 000000 000000 000000 000000
0000002222 000000 000000 000000 000000
0000002223 000000 000000 000000 000000
0000002224 000000 000000 000000 000000
0000002225 000000 000000 000000 000000
0000002226 000000 000000 000000 000000
0000002227 000000 000000 000000 000000
0000002230 000000 000000 000000 000000
0000002231 000000 000000 000000 000000
0000002232 000000 000000 000000 000000
0000002233 000000 000000 000000 000000
0000002234 000000 000000 000000 000000
0000002235 000000 000000 000000 000000
0000002236 000000 000000 000000 000000
0000002237 000000 000000 000000 000000
0000002240 000135 070673 000000 004745
0000002241 000000 000000 000000 002240
0000002242 000003 177777 000003 053767
0000002243 000000 000000 004025 014000
0000002244 000003 177777 000022 063460
0000002245 176000 000017 004025 020000
0000002246 000000 000000 000001 013024
0000002247 006401 045200 000000 002200
0000002250 000000 000000 000000 000000
0000002251 000000 000000 000000 000133
0000002252 000000 000000 000000 000000
0000002253 000000 000000 007116 073564
0000002254 000211 137646 133362 125475
0000002255 000000 000001 065376 042321
0000002256 000211 137646 124244 031711
0000002257 000000 000000 000000 000001
0000002260 000000 000000 000000 000000
0000002261 000000 000000 000000 000000
0000002262 000000 000000 000000 000000
0000002263 000000 000000 000000 000000
0000002264 000000 000000 000000 000000
0000002265 000000 000000 000000 000000
0000002266 000000 000000 000000 000000
0000002267 000000 000000 000000 000000
0000002270 000000 000000 000000 000000
0000002271 000000 000000 000000 000000
0000002272 000000 000000 000000 000000
0000002273 000000 000000 000000 000000
0000002274 000000 000000 000000 000000
0000002275 000000 000000 000000 000000
0000002276 000000 000000 000000 000000
0000002277 000000 000000 000000 000000
0000002300 000135 052636 000000 000000
0000002301 000000 000000 000000 002300
0000002302 000003 177777 000000 000000
0000002303 000000 000000 000000 000000
0000002304 000003 177777 000000 000000
0000002305 000000 000011 000000 000000
0000002306 000000 000000 000001 013157
0000002307 007001 046000 000000 000000
0000002310 000000 000000 000000 000000
0000002311 000000 000000 000000 000000
0000002312 000000 000000 000000 000000
0000002313 000000 000000 000000 000000
0000002314 000000 000000 000000 000000
0000002315 000000 000000 000000 000000
0000002316 000000 000000 000000 000000

```

```

0000002317      000000 000000 000000 000000
0000002320      000000 000000 000000 000000
0000002321      000000 000000 000000 000000
0000002322      000000 000000 000000 000000
0000002323      000000 000000 000000 000000
0000002324      000000 000000 000000 000000
0000002325      000000 000000 000000 000000
0000002326      000000 000000 000000 000000
0000002327      000000 000000 000000 000000
0000002330      000000 000000 000000 000000
0000002331      000000 000000 000000 000000
0000002332      000000 000000 000000 000000
0000002333      000000 000000 000000 000000
0000002334      000000 000000 000000 000000

```

Memory from A5 (A5 + DBA = 000001750)

```

-----
0000001750      000000 000000 000000 000000
0000001751      000000 000000 000000 000156
0000001752      000000 000000 076070 140345
0000001753      000000 000000 005635 040000
0000001754      000211 137646 133362 123117
0000001755      000000 000000 017057 175631
0000001756      000211 137646 133362 122620
0000001757      000000 000000 000000 000277
0000001760      000000 000000 000000 000000
0000001761      000000 000000 000000 000000
0000001762      000000 000000 000000 000000
0000001763      000000 000000 000000 000000
0000001764      000000 000000 000000 000000
0000001765      000000 000000 000000 000000
0000001766      000000 000000 000000 000000
0000001767      000000 000000 000000 000000
0000001770      000000 000000 000000 000000
0000001771      000000 000000 000000 000000
0000001772      000000 000000 000000 000000
0000001773      000000 000000 000000 000000
0000001774      000000 000000 000000 000000
0000001775      000000 000000 000000 000000
0000001776      000000 000000 000000 000000
0000001777      000000 000000 000000 000000
0000002000      001135 052636 000000 000000
0000002001      000000 000000 000000 002000
0000002002      000003 177777 000000 000000
0000002003      000000 000000 000000 000000
0000002004      000003 177777 000000 000000
0000002005      000000 000011 000000 000000
0000002006      000000 000000 000001 012536
0000002007      005401 040000 000000 000000
0000002010      000000 000000 000000 000000
0000002011      000000 000000 000000 000000
0000002012      000000 000000 000000 000000
0000002013      000000 000000 000000 000000
0000002014      000000 000000 000000 000000
0000002015      000000 000000 000000 000000
0000002016      000000 000000 000000 000000
0000002017      000000 000000 000000 000000
0000002020      000000 000000 000000 000000
0000002021      000000 000000 000000 000000
0000002022      000000 000000 000000 000000
0000002023      000000 000000 000000 000000
0000002024      000000 000000 000000 000000
0000002025      000000 000000 000000 000000
0000002026      000000 000000 000000 000000
0000002027      000000 000000 000000 000000
0000002030      000000 000000 000000 000000
0000002031      000000 000000 000000 000000
0000002032      000000 000000 000000 000000
0000002033      000000 000000 000000 000000
0000002034      000000 000000 000000 000000
0000002035      000000 000000 000000 000000
0000002036      000000 000000 000000 000000
0000002037      000000 000000 000000 000000
0000002040      000135 070673 000001 035454
0000002041      000000 000000 000000 002040
0000002042      000003 177777 000003 045552
0000002043      000000 000000 000423 030000
0000002044      000003 177777 000022 063460
0000002045      176000 000017 000423 034000
0000002046      000000 000000 000001 012536
0000002047      005401 041200 000000 002000
0000002050      000000 000000 000000 000000
0000002051      000000 000000 000000 000132
0000002052      000000 000000 000000 000000
0000002053      000000 000000 000000 005750
0000002054      000211 137646 155772 072451
0000002055      000000 000001 100052 021313
0000002056      000211 137646 155772 064501
0000002057      000000 000000 000000 000001
0000002060      000000 000000 000000 000000
0000002061      000000 000000 000000 000000
0000002062      000000 000000 000000 000000

```

```

0000002063 000000 000000 000000 000000
0000002064 000000 000000 000000 000000
0000002065 000000 000000 000000 000000
0000002066 000000 000000 000000 000000
0000002067 000000 000000 000000 000000
0000002070 000000 000000 000000 000000
0000002071 000000 000000 000000 000000
0000002072 000000 000000 000000 000000
0000002073 000000 000000 000000 000000
0000002074 000000 000000 000000 000000
0000002075 000000 000000 000000 000000
0000002076 000000 000000 000000 000000
0000002077 000000 000000 000000 000000
0000002100 000135 052636 000000 000000
0000002101 000000 000000 000000 002100
0000002102 000003 177777 000000 000000
0000002103 000000 000000 000000 000000
0000002104 000003 177777 000000 000000
0000002105 000000 000011 000000 000000
0000002106 000000 000000 000001 012671
0000002107 006001 042000 000000 000000
0000002110 000000 000000 000000 000000
0000002111 000000 000000 000000 000000
0000002112 000000 000000 000000 000000
0000002113 000000 000000 000000 000000
0000002114 000000 000000 000000 000000
0000002115 000000 000000 000000 000000
0000002116 000000 000000 000000 000000
0000002117 000000 000000 000000 000000
0000002120 000000 000000 000000 000000
0000002121 000000 000000 000000 000000
0000002122 000000 000000 000000 000000
0000002123 000000 000000 000000 000000
0000002124 000000 000000 000000 000000
0000002125 000000 000000 000000 000000
0000002126 000000 000000 000000 000000
0000002127 000000 000000 000000 000000
0000002130 000000 000000 000000 000000
0000002131 000000 000000 000000 000000
0000002132 000000 000000 000000 000000
0000002133 000000 000000 000000 000000
0000002134 000000 000000 000000 000000
0000002135 000000 000000 000000 000000
0000002136 000000 000000 000000 000000
0000002137 000000 000000 000000 000000
0000002140 000001 106152 000012 120517
0000002141 000003 011460 000012 120501
0000002142 000003 012742 000012 120502
0000002143 000003 011460 000012 120517
0000002144 000003 012742 000012 124327
0000002145 176665 000004 000012 120523
0000002146 000000 000000 000012 120511
0000002147 006016 043100 000012 120517

```

Memory from A6 (A6 + DBA = 0000000165)

```

-----
0000000165 000000 000000 000000 000000
0000000166 000000 000000 000000 000000
0000000167 000000 000000 000000 000000
0000000170 000000 000000 000000 000000
0000000171 000000 000000 000000 000000
0000000172 000000 000000 000000 000000
0000000173 000000 000000 000000 000000
0000000174 000000 000000 000000 000000
0000000175 000000 000000 000000 000000
0000000176 000000 000000 000000 000000
0000000177 000000 000000 000000 000000
0000000200 000000 000000 000000 000000
0000000201 000105 160000 000000 005672
0000000202 051524 040503 045514 044515
0000000203 000000 000000 007772 154000
0000000204 043525 042523 052124 040502
0000000205 000105 165670 000000 000002
0000000206 000000 000000 000000 000000
0000000207 000000 000000 000000 000000
0000000210 000000 000000 000000 000000
0000000211 000000 000000 000000 000000
0000000212 000000 000000 000000 000000
0000000213 000000 000000 000000 000000
0000000214 000000 000000 000000 000000
0000000215 000000 000000 000000 000000
0000000216 000000 000000 000000 000000
0000000217 000000 000000 000000 000000
0000000220 000000 000000 000000 000000
0000000221 000000 000000 000000 000000
0000000222 000000 000000 000000 000000
0000000223 000000 000000 000000 000000
0000000224 000000 000000 000000 000000
0000000225 000000 000000 000000 000000
0000000226 000000 000000 000000 000000
0000000227 000000 000000 000000 000000
0000000230 000000 000000 000000 000000

```



```

000000231 000000 000000 000000 000000
000000232 000000 000000 000000 000000
000000233 000000 000000 000000 000000
000000234 000000 000000 000000 000000
000000235 000000 000000 000000 000000
000000236 000000 000000 000000 000000
000000237 000000 000000 000000 000000
000000240 000000 000000 000000 000000
000000241 000000 000000 000000 000000
000000242 000000 000000 000000 000000
000000243 000000 000000 000000 000000
000000244 000000 000000 000000 000000
000000245 000000 000000 000000 000000
000000246 000000 000000 000000 000000
000000247 000000 000000 000000 000000
000000250 000000 000000 000000 000000
000000251 000000 000000 000000 000000
000000252 000000 000000 000000 000000
000000253 000000 000000 000000 000000
000000254 000000 000000 000000 000000
000000255 000000 000000 000000 000000
000000256 000000 000000 000000 000000
000000257 000000 000000 000000 000000
000000260 000000 000000 000000 000000
000000261 000000 000000 000000 000000
000000262 000000 000000 000000 000000
000000263 000000 000000 000000 000000
000000264 000000 000000 000000 000000
000000265 000000 000000 000000 000000
000000266 000000 000000 000000 000000
000000267 000000 000000 000000 000000
000000270 000000 000000 000000 000000
000000271 000000 000000 000000 000000
000000272 000000 000000 000000 000000
000000273 000000 000000 000000 000000
000000274 000000 000000 000000 000000
000000275 000000 000000 000000 000000
000000276 000000 000000 000000 000000
000000277 000000 000000 000000 000000
000000300 000000 000000 000000 000000
000000301 000000 000000 000000 000000
000000302 000000 000000 000000 000000
000000303 000000 000000 000000 000000
000000304 000000 000000 000000 000000
000000305 000000 000000 000000 000000
000000306 000000 000000 000000 000000
000000307 000000 000000 000000 000000
000000310 000000 000000 000000 000000
000000311 000000 000000 000000 000000
000000312 000000 000000 000000 000000
000000313 000000 000000 000000 000000
000000314 000000 000000 000000 000000
000000315 000000 000000 000000 000000
000000316 000000 000000 000000 000000
000000317 000000 000000 000000 000000
000000320 000000 000000 000000 000000
000000321 000000 000000 000000 000000
000000322 000000 000000 000000 000000
000000323 000000 000000 000000 000000
000000324 000000 000000 000000 000000
000000325 000000 000000 000000 000000
000000326 000000 000000 000000 000000
000000327 000000 000000 000000 000000
000000330 000000 000000 000000 000000
000000331 000000 000000 000000 000000
000000332 000000 000000 000000 000000
000000333 000000 000000 000000 000000
000000334 000000 000000 000000 000000
000000335 000000 000000 000000 000000
000000336 000000 000000 000000 000000
000000337 000000 000000 000000 000000
000000340 000000 000000 000000 000000
000000341 000000 000000 000000 000000
000000342 025154 067567 066545 066452
000000343 000000 000000 000000 001360
000000344 061541 061550 062440 020040
000000345 000000 000000 000020 154115
000000346 061541 066154 067565 072040
000000347 000000 000000 000021 105374
000000350 061550 060556 067145 066040
000000351 000000 000000 000023 022303
000000352 061554 067543 065440 020040
000000353 000000 000000 000022 017510
000000354 062551 067546 071145 062440
000000355 000000 000000 000001 020015
000000356 062551 067560 060543 065440
000000357 000000 000000 000001 020021
000000360 062551 067560 065545 067144
000000361 000000 000000 000001 020023
000000362 066551 067560 020040 020040
000000363 000000 000000 000001 020013
000000364 062556 062157 063144 060564

```

```

Memory from A7 (A7 + DBA = 1777340016)
-----
1777340016 000000 000000 000037 155276
1777340017 000000 000000 000177 045665
1777340020 000000 000000 000027 055340
1777340021 000000 000000 007775 140010
1777340022 000000 000000 000000 000000
1777340023 000000 000000 000135 070000
1777340024 000000 000000 000001 010306
1777340025 000000 000000 000000 006063
1777340026 000000 000000 000000 000020
1777340027 000000 000000 000000 000001
1777340030 000000 000000 000000 000000
1777340031 000000 000000 000000 000000
1777340032 000000 000000 000000 006063
1777340033 000000 000000 000000 000000
1777340034 020000 000752 000000 000001
1777340035 000000 000000 000223 130223
1777340036 000000 000000 000000 000000
1777340037 000000 000000 000000 000020
1777340040 000000 000000 000000 000000
1777340041 000000 000000 000000 123620
1777340042 000000 000000 000000 000000
1777340043 000000 000000 000133 002140
1777340044 000000 000000 000000 000020
1777340045 000000 000000 000000 000000
1777340046 000000 000000 000003 001542
1777340047 000000 000000 000000 050365
1777340050 000000 000000 000000 130335
1777340051 000000 000000 000000 000000
1777340052 000000 000000 000003 001542
1777340053 000000 000000 000155 112641
1777340054 000000 000000 007775 140010
1777340055 000000 000000 000000 000000
1777340056 000000 000000 000000 000001
1777340057 020000 000727 000000 000001
1777340060 000000 000000 000020 154276
1777340061 000000 000000 000047 016550
1777340062 000000 000000 000000 000000
1777340063 000000 000000 000036 015743
1777340064 000000 000000 000153 171035
1777340065 000000 000000 007775 140030
1777340066 000000 000000 000000 000000
1777340067 000000 000000 000047 016550
1777340070 000000 000000 000047 017226
1777340071 004000 000000 000000 000000
1777340072 000010 000000 000000 000000
1777340073 000000 000000 000153 170523
1777340074 000000 000000 000000 000000
1777340075 000000 000000 000000 000000
1777340076 000000 000000 000000 000000
1777340077 000000 000000 000000 000047
1777340100 000000 000000 000400 000000
1777340101 177707 000000 000000 000000
1777340102 000000 000000 000000 000047
1777340103 000000 000000 000000 000001
1777340104 000000 000000 000000 000000
1777340105 000000 000000 000003 027606
1777340106 000000 000000 000003 041414
1777340107 000000 000000 000000 000001
1777340110 000000 000000 000000 000165
1777340111 000000 000000 000000 000000
1777340112 000000 000000 000026 147542
1777340113 000000 000000 000177 051542
1777340114 000000 000000 000037 155152
1777340115 000000 000000 007775 140016
1777340116 000000 000000 000000 001750
1777340117 000000 000000 000000 001750
1777340120 000000 000000 000000 000020
1777340121 000000 000000 000003 001542
1777340122 000000 000000 000003 001346
1777340123 000000 000000 000000 000000
1777340124 040060 140000 000000 000000
1777340125 000000 000000 000003 001346
1777340126 000000 000000 000000 000047
1777340127 000000 000000 000000 000000
1777340130 020000 002010 000000 000001
1777340131 000000 000000 003732 066225
1777340132 000000 000000 000000 000070
1777340133 000000 000000 000047 016550
1777340134 000000 000000 000000 000000
1777340135 000000 000000 000000 000000
1777340136 000000 000000 000005 140000
1777340137 000000 000000 000000 000000
1777340140 020000 003526 000000 000001
1777340141 000000 004000 000000 020005
1777340142 000000 000000 000000 127620
1777340143 000000 000000 000000 045145
1777340144 000000 000000 000000 037262
1777340145 000000 000000 000000 000004
1777340146 000000 000000 000000 002665
1777340147 000000 000000 000000 000044

```

```

1777340150 000000 000000 000000 000000
1777340151 000000 000000 000000 000000
1777340152 000000 000000 000000 017004
1777340153 000000 000000 000000 012671
1777340154 000000 000000 000000 000000
1777340155 000000 000000 000000 000314
1777340156 000000 000000 000000 000000
1777340157 000000 000000 000000 000000
1777340160 000000 000000 000000 000000
1777340161 000000 000000 000000 006614
1777340162 000000 000000 000000 000000
1777340163 000000 000000 000000 004711
1777340164 000000 000000 000000 012003
1777340165 000000 000000 000000 000000
1777340166 000000 000000 000000 005072
1777340167 000000 000000 000000 016252
1777340170 000000 000000 000000 000000
1777340171 000000 000000 000000 002665
1777340172 000000 000000 000000 000000
1777340173 000000 000000 000000 000000
1777340174 000000 000000 000000 000000
1777340175 000000 000000 000000 000000
1777340176 000000 000000 000000 000000
1777340177 000000 000000 000000 000000
1777340200 000000 000000 000000 000000
1777340201 000000 000000 000000 000000
1777340202 000000 000000 000150 011663
1777340203 000000 000000 007775 140116
1777340204 000000 000000 007775 140123
1777340205 000000 000000 000046 034507
1777340206 000000 000000 000026 125134
1777340207 000000 000000 000132 142173
1777340210 000000 000000 007775 140034
1777340211 000000 000000 007775 140112
1777340212 020000 002444 000000 000001
1777340213 000000 000000 000000 174306
1777340214 000000 000000 000000 000001
1777340215 000000 000000 000000 000000

```

```

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
Test Point Dump
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

```

```

TP-> 0      1      2      3      4      5      6      7
OPT  01234567 01234567 01234567 01234567 01234567 01234567 01234567 01234567

YE0  00110111 11100011 11100111 00100111 00110111 10010111 00000001 00000001
YF0  11101111 11111000 00010111 11100101 11010000 11111000 00010111 11000001
YF1  11101111 11111000 00010111 11100101 11010000 11111000 00010111 11000001
YF2  11101111 11111000 00010111 11100101 11010000 11111000 00010111 11000001
YG0  00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000001
YH0  11111111 11111111 11111111 11110000 11000000 11111111 11111111 11111111
YJ0  11111111 11111111 11111111 11111111 11111111 11111111 11111111 11111111
YJ1  11111111 11111111 11111111 11111111 11111111 11111111 11111111 11111111
YM0  11111111 11111111 11111111 11111111 00000001 00000011 11111111 11111111
YM1  11111111 11111111 11111111 11111111 00000000 00000011 11111111 11111111
YK0  00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000001
YK1  00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000001
HA0  00001000 00110000 00001000 01111111 00001000 00110000 00001000 01111111
HC0  00000000 00000000 00000000 00000000 00011000 00000001 00100100 10010001
JA0  11111111 11111111 00000000 00000000 11110010 00000110 00000000 00000001
JB0  00000000 00000000 00000000 00000000 00000000 00000000 00000010 00000001
JC0  00000000 00000000 00100000 00010000 00000000 01000000 00000000 00000001
JQ0  11111111 11111111 11100100 00101011 11111101 11111111 11111111 11111111
VQ0  00000000 00000000 00000000 00000000 11111111 11111111 11110000 00000001
VQ1  00000000 00000000 00000000 00000000 11111111 11111111 11110000 00000001
DA0  10000000 11010111 10000000 00000001 00000001 10101111 00000000 00000011
DC0  00000000 00010011 00000000 00100011 01100000 01000010 00000000 00000001
DD0  00001010 00000000 00001000 00001010 00000000 00001000 11111111 11111111
DD1  10000000 00000000 00000010 00000000 00000000 00001111 11111111 11111111
JQ1  00000000 00000000 11111111 11111111 11111111 11111111 11111111 11111111
SIE  11111111 11111111 11111111 11111111 11111111 11111111 11111111 11111111

```

```

****
**** the 'bin/.smon_trigger_cmd' script could not be executed
****

*****
System Monitor Dump completed at: Mon Feb 13 13:55:42 1995
*****

```

# Software Trap in the User Code

```

*****
LME-C90 4.1.8 System Monitor Dump Summary
S/N 4027 CPU 11 (logical 11) triggered at: Mon Feb 13 13:52:28 1995
*****

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
CPU/System Status
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

CPU Status: 100040 000117 177777 000000

CPU-related Status
-----
Memory: 256K mode = 0
Half memory mode = 0
Half upper select = 0
CPU: Idle = 0
I/O ECC Enabled = 0
Test Mode = 0
Maint Restricted = 0
Diag Monitor: Active = 0
Parity Error = 0
Shared Register Select = 0
Master CPU Select = 00

System-related Status
-----
System I/O Master Clear = 0
System CPU Master Clear = 0
Function Error = 0
System Error = 0
Control Cable Enables 0 = 1
1 = 0
Memory Priority = 17
CPUs Present Mask = 177777
CPU Parity Errors Mask = 000000

Memory Priority Check
-----
Memory priority is NOT LOCKED

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
DM Params set by the System Monitor (SMON)
and the resulting DM data buffer
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

DM Params:
-----
MODES 02 DISA *HICU HICD TPD IBD ECHO 00: 005000
DATA 20 [LWR P] [CIP] [UPPR P] [COIN/HOLD] 01: 101001
TP SEL 00 OPT=YE0 02: 000200
03: 002740
EVENT 01 *VCIP CIPM PLPM TP ~TP LEAD 04: 177747
TRIGGER 02 VCIP *CIPM PLPM TP ~TP LEAD 05: 000000
TR MODE 1 *CPCT TRCT 06: 000000
TR RESET 0 CIPM FLPM 07: 000000
10: 000000
DTC 000200 11: 000000
12: 000000
CIP VAL 002740 P VAL 0000000000a 13: 000000
CIP MASK 177747 P MASK 00000a

TP0 (YE /xx /HA /DA ) 00 TP4 (YG /YK0/JC /xx ) 00
TP1 (YF0/xx /HC /DB ) 00 TP5 (YH /YK1/JQ /JQ ) 00
TP2 (YF1/YM0/JA /DC ) 00 TP6 (YJ0/xx /VQ0/xx ) 00
TP3 (YF2/YM1/JB /DD ) 00 TP7 (YJ1/xx /VQ1/SIE) 00

DM Data:
-----

Buffer Contains 64 valid words of DM data

Event P Register CIP Coincidence Hold Issue Conditions IB Data Fetch Wait CIP
Valid Quiet Exch Valid
JB JA MEM SR FU BT V S A
000 0000001000a 176500 4 0 0 0 0 0 0 0 0 0 0 0 1 1 0 1
001 0000001000b 025003 4 0 1 0 0 0 0 0 0 0 0 0 1 1 0 1
002 0000001000c 150450 4 0 0 0 0 0 0 0 0 0 0 0 1 1 0 1
003 0000001000d 020600 4 0 0 0 0 0 0 0 0 0 0 0 1 1 0 1
004 0000001001c 030056 4 0 0 0 0 0 0 0 0 0 0 0 1 1 0 1
005 0000001001d 176300 4 0 0 0 0 0 0 0 0 0 0 1 1 1 0 1
006 0000001002a 042760 4 0 1 0 0 0 0 0 0 0 0 0 1 1 0 1
007 0000001002b 151230 4 0 0 0 0 0 1 0 0 0 0 0 1 1 0 1
010 0000001002c 155142 4 0 0 0 0 0 0 0 0 0 0 0 1 1 0 1
011 0000001002d 177010 4 0 0 0 0 0 0 0 0 0 0 0 1 1 0 1
012 0000001003a 024003 4 0 1 0 0 0 0 0 0 0 0 0 1 1 0 1

```

013	0000001003b	151050	4	0	0	0	0	1	0	0	0	0	1	1	0	1
014	0000001003c	150710	4	0	0	0	0	1	0	1	0	0	0	1	0	1
015	0000001003d	155607	4	0	0	0	0	0	0	0	0	0	0	1	0	1
016	0000001004a	177060	4	0	0	0	0	0	0	0	0	0	0	1	0	1
017	0000001004b	125600	4	0	1	1	0	0	0	0	0	0	0	1	0	1
020	0000001005a	044576	4	0	0	0	0	0	0	0	0	1	0	1	0	1
021	0000001005b	040700	4	0	0	0	0	0	0	0	0	0	0	1	0	1
022	0000001006a	046057	4	0	0	0	0	0	0	0	0	0	0	1	0	1
023	0000001006b	015000	4	0	0	0	0	0	0	0	0	1	0	1	0	1
024	0000000776b	020700	3	0	0	0	0	0	0	0	0	0	0	1	0	1
025	0000000777a	024502	3	0	0	0	0	0	0	0	0	0	0	1	0	1
026	0000000777b	022605	3	0	0	0	0	0	0	0	0	0	0	0	0	1
027	0000000777c	030056	4	0	0	0	0	0	0	0	0	0	0	0	1	0
030	0000000777d	002007	4	0	0	0	0	0	0	0	0	0	0	0	1	0
031	0000001000a	176500	4	0	0	0	0	0	0	0	0	0	0	1	0	1
032	0000001000b	025003	4	0	1	0	0	0	0	0	0	0	0	1	0	1
033	0000001000c	150450	4	0	0	0	0	0	0	0	0	0	0	1	0	1
034	0000001000d	020600	4	0	0	0	0	0	0	0	0	0	0	1	0	1
035	0000001001c	030056	4	0	0	0	0	0	0	0	0	0	0	1	0	1
036	0000001001d	176300	4	0	0	0	0	0	0	0	0	1	1	1	0	1
037	0000001002a	042760	4	0	1	0	0	0	0	0	0	0	0	1	0	1
040	0000001002b	151230	4	0	0	0	0	1	0	0	0	0	0	1	0	1
041	0000001002c	155142	4	0	0	0	0	0	0	0	0	0	0	1	0	1
042	0000001002d	177010	4	0	0	0	0	0	0	0	0	0	0	1	0	1
043	0000001003a	024003	4	0	1	0	0	0	0	0	0	0	0	1	0	1
044	0000001003b	151050	4	0	0	0	0	1	0	0	0	0	0	1	0	1
045	0000001003c	150710	4	0	0	0	0	1	0	1	0	0	0	1	0	1
046	0000001003d	155607	4	0	0	0	0	0	0	0	0	0	0	1	0	1
047	0000001004a	177060	4	0	0	0	0	0	0	0	0	0	0	1	0	1
050	0000001004b	125600	4	0	1	1	0	0	0	0	0	0	0	1	0	1
051	0000001005a	044576	4	0	0	0	0	0	0	0	1	0	1	1	0	1
052	0000001005b	040700	4	0	0	0	0	0	0	0	0	0	0	1	0	1
053	0000001006a	046057	4	0	0	0	0	0	0	0	0	0	0	1	0	1
054	0000001006b	015000	4	0	0	0	0	0	0	0	1	0	1	1	0	1
055	0000001007a	020600	4	0	0	0	0	0	0	0	0	0	0	1	0	1
056	0000001007d	007000	4	0	0	0	0	0	0	0	0	0	0	0	1	0
057	0000000744a	020000	3	0	0	0	0	0	0	0	0	0	0	1	0	1
060	0000000744d	025077	3	0	0	0	0	0	0	0	0	0	0	1	0	1
061	0000000745a	022504	3	0	0	0	0	0	0	0	0	0	0	1	0	1
062	0000000745b	024066	3	0	0	0	0	0	0	0	0	0	0	1	0	1
063	0000000745c	035577	3	0	0	0	0	0	0	0	0	0	0	1	0	1
064	0000000745d	024766	3	0	0	0	0	1	0	0	0	0	0	1	0	1
065	0000000746a	022504	3	0	0	0	0	0	0	0	0	0	0	1	0	1
066	0000000746b	030557	3	0	0	0	0	0	0	0	0	0	0	1	0	1
067	0000000746c	025601	3	0	0	0	0	0	0	0	0	0	0	1	0	1
070	0000000746d	025702	3	0	0	0	0	0	0	0	0	0	0	1	0	1
071	0000000747a	024767	3	0	0	0	0	1	0	0	0	0	0	1	0	1
072	0000000747b	031075	3	0	0	0	0	0	0	0	0	0	0	1	0	1
073	0000000747c	025566	3	0	0	0	0	0	0	0	0	0	0	1	0	1
074	0000000747d	024702	3	0	0	0	0	1	0	0	0	0	0	1	0	1
075	0000000750a	013000	3	0	0	0	0	0	0	0	1	1	1	1	0	1
076	0000000750d	002740	3	0	0	0	0	0	0	0	0	0	0	1	0	1
077	0000000751a	001000	3	1	0	0	0	0	0	0	0	0	0	1	0	1

\*\*\*\*\*  
 Failure Analysis  
 \*\*\*\*\*

\*\* Software failure occurred within a user job running under UNICOS  
 (UNICOS revision: 8.0.2.4 or beyond)

Failing Exchange Package:

(For a SOFTWARE failure, the failing exchange package is the  
 'EXCHANGE PACKAGE' in the CPU before IB dump', which is shown  
 in the 'Instruction Buffer Dump' section of this file.)

\*\*\*\*\*  
 Instruction Buffer Dump  
 (with buffer-to-memory data compare)  
 \*\*\*\*\*

INSTRUCTION BUFFER 0 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
00	061072	016000	003246 000000
01	042777	024602	040740 000456
02	020000	022710	136700 000010
03	000000	136100	000011 000000
04	030676	075102	007000 020060
05	000000	042777	024602 040740
06	000460	020000	022710 136700
07	000010	000000	074702 136700
10	000011	000000	030676 007000
11	017344	000000	020700 163110
12	000000	074701	071117 061017
13	051201	051102	017000 003266
14	000000	074700	061072 016000
15	003306	000000	042777 024602

16	040740	000466	020000	022710				
17	136700	000010	000000	136100				
20	000011	000000	030676	007000				
21	020060	000000	120700	040201				
22	000000	024602	042677	040640				
23	000513	020000	022710	136600				
24	000010	000000	030676	024702				
25	137700	000011	000000	007000				
26	143010	000000	020700	163120				
27	000000	074701	071117	061017				
30	017000	003350	000000	074700				
31	061071	016000	003371	000000				
32	042777	024602	040740	000517				
33	020000	022710	043600	136700				
34	000010	000000	136600	000011				
35	000000	030676	007000	163120				
36	000000	020700	163130	000000				
37	074701	071117	061017	017000				

INSTRUCTION BUFFER 1 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
00	003407	000000 074700 061071	
01	016000	003415 000000 020600	
02	040246	000000 007000 163130	
03	000000	020600 040247 000000	
04	007000	045001 000000 020700	
05	163140	000000 074701 071117	
06	061017	017000 003441 000000	
07	074700	061071 016000 003455	
10	000000	120000 055521 000000	
11	014000	003455 000000 020600	
12	040250	000000 007000 163140	
13	000000	020700 163150 000000	
14	074701	071117 061017 017000	
15	003473	000000 074700 061071	
16	016000	003501 000000 020600	
17	040251	000000 007000 163150	
20	000000	020700 163160 000000	
21	074701	071117 061017 017000	
22	003517	000000 074700 061071	
23	016000	003525 000000 020600	
24	040252	000000 007000 163160	
25	000000	120700 037451 000000	
26	100700	037452 000000 100600	
27	037453	000000 024402 042676	
30	040640	000550 020000 022510	
31	134600	000010 000000 025603	
32	030654	134700 000011 000000	
33	114700	000012 000000 024703	
34	114700	000013 000000 007000	
35	003705	000000 042777 024602	
36	040740	000550 020000 022710	
37	136700	000010 000000 136100	

INSTRUCTION BUFFER 2 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
00	000000	020000 043741 000000	
01	025077	024066 024266 024467	
02	022104	035177 030321 031043	
03	013000	044764 000000 025202	
04	025366	025601 043100 024002	
05	022104	025066 034177 005000	
06	030012	022501 025202 025366	
07	037500	025601 020700 045075	
10	000000	025700 020600 043747	
11	000000	006000 146300 000000	
12	020000	043757 000000 025077	
13	024066	024266 024467 022104	
14	035177	022711 030327 031043	
15	013000	045030 000000 030012	
16	022501	025202 037500 025366	
17	025601	024701 127700 000001	
20	000000	024502 042677 040640	
21	000030	020000 022605 135600	
22	000005	000000 030665 135700	
23	000006	000000 075700 007000	
24	154400	000000 024701 127700	
25	000002	000000 024502 074600	
26	022603	056666 040500 000002	
27	000000	060461 040540 000030	
30	020000	057446 022605 135500	
31	000005	000000 135400 000006	
32	000000	030665 135700 000007	
33	000000	007000 154000 000000	
34	074100	024002 024202 022104	
35	025066	034177 030021 022101	
36	036100	005000 000000 000000	
37	030012	022514 025202 025366	

INSTRUCTION BUFFER 3 (Memory IBA 0050740000)

ADDRESS	EXPECTED (memory)				00	ACTUAL (IB data)				DIFFERENCE
0000000740	000011	000000	030676	007000	00	000011	000000	030676	007000	-----
0000000741	040730	000000	043100	024002	01	040730	000000	043100	024002	-----
0000000742	024202	022105	025066	034177	02	024202	022105	025066	034177	-----
0000000743	030021	022103	036100	005000	03	030021	022103	036100	005000	-----
0000000744	020000	040264	000000	025077	04	020000	040264	000000	025077	-----
0000000745	022504	024066	035577	024766	05	022504	024066	035577	024766	-----
0000000746	022504	030557	025601	025702	06	022504	030557	025601	025702	-----
0000000747	024767	031075	025566	024702	07	024767	031075	025566	024702	-----
0000000750	013000	003654	000000	002740	10	013000	003654	000000	002740	-----
0000000751	001000	001000	024702	030070	11	001000	001000	024702	030070	-----
0000000752	025766	022703	034700	005000	12	025766	022703	034700	005000	-----
0000000753	020600	040262	000000	007000	13	020600	040262	000000	007000	-----
0000000754	146300	000000	024601	024702	14	146300	000000	024601	024702	-----
0000000755	006000	003643	000000	000000	15	006000	003643	000000	000000	-----
0000000756	025202	025366	025601	020700	16	025202	025366	025601	020700	-----
0000000757	003731	000000	025700	020600	17	003731	000000	025700	020600	-----
0000000760	040272	000000	006000	146300	20	040272	000000	006000	146300	-----
0000000761	000000	020000	040275	000000	21	000000	020000	040275	000000	-----
0000000762	025077	022105	024066	024266	22	025077	022105	024066	024266	-----
0000000763	024467	035177	020700	000337	23	024467	035177	020700	000337	-----
0000000764	000000	030327	031043	013000	24	000000	030327	031043	013000	-----
0000000765	003670	000000	025202	025366	25	003670	000000	025202	025366	-----
0000000766	025601	020700	000155	000000	26	025601	020700	000155	000000	-----
0000000767	020000	053602	000000	002007	27	020000	053602	000000	002007	-----
0000000770	176700	024502	042777	154677	30	176700	024502	042777	154677	-----
0000000771	022605	030056	177060	125700	31	022605	030056	177060	125700	-----
0000000772	000022	000000	020600	000162	32	000022	000000	020600	000162	-----
0000000773	000000	030056	042660	044567	33	000000	030056	042660	044567	-----
0000000774	040700	123456	000000	046057	34	040700	123456	000000	046057	-----
0000000775	177060	014000	004034	000000	35	177060	014000	004034	000000	-----
0000000776	002704	020700	000155	000000	36	002704	020700	000155	000000	-----
0000000777	024502	022605	030056	002007	37	024502	022605	030056	002007	-----

INSTRUCTION BUFFER 4 (Memory IBA 0050740000)

ADDRESS	EXPECTED (memory)				00	ACTUAL (IB data)				DIFFERENCE
0000001000	176500	025003	150450	020600	00	176500	025003	150450	020600	-----
0000001001	000162	000000	030056	176300	01	000162	000000	030056	176300	-----
0000001002	042760	151230	155142	177010	02	042760	151230	155142	177010	-----
0000001003	024003	151050	150710	155607	03	024003	151050	150710	155607	-----
0000001004	177060	125600	000022	000000	04	177060	125600	000022	000000	-----
0000001005	044576	040700	123456	000000	05	044576	040700	123456	000000	-----
0000001006	046057	015000	003771	000000	06	046057	015000	003771	000000	-----
0000001007	020600	040273	000000	007000	07	020600	040273	000000	007000	-----
0000001010	003620	000000	043100	024002	10	003620	000000	043100	024002	-----
0000001011	022105	025066	034177	005000	11	022105	025066	034177	005000	-----
0000001012	000000	000000	000000	000000	12	000000	000000	000000	000000	-----
0000001013	000000	000000	000000	000000	13	000000	000000	000000	000000	-----
0000001014	000000	000000	000000	000000	14	000000	000000	000000	000000	-----
0000001015	000000	000000	000000	000000	15	000000	000000	000000	000000	-----
0000001016	000000	000000	000000	000000	16	000000	000000	000000	000000	-----
0000001017	000000	000000	000000	000000	17	000000	000000	000000	000000	-----
0000001020	000000	000000	000000	000000	20	000000	000000	000000	000000	-----
0000001021	000000	000000	000000	000000	21	000000	000000	000000	000000	-----
0000001022	000000	000000	000000	000000	22	000000	000000	000000	000000	-----
0000001023	000000	000000	000000	000000	23	000000	000000	000000	000000	-----
0000001024	000000	000000	000000	000000	24	000000	000000	000000	000000	-----
0000001025	000000	000000	000000	000000	25	000000	000000	000000	000000	-----
0000001026	000000	000000	000000	000000	26	000000	000000	000000	000000	-----
0000001027	000000	000000	000000	000000	27	000000	000000	000000	000000	-----
0000001030	000000	000000	000000	000000	30	000000	000000	000000	000000	-----
0000001031	000000	000000	000000	000000	31	000000	000000	000000	000000	-----
0000001032	000000	000000	000000	000000	32	000000	000000	000000	000000	-----
0000001033	000000	000000	000000	000000	33	000000	000000	000000	000000	-----
0000001034	000000	000000	000000	000000	34	000000	000000	000000	000000	-----
0000001035	000000	000000	000000	000000	35	000000	000000	000000	000000	-----
0000001036	000000	000000	000000	000000	36	000000	000000	000000	000000	-----
0000001037	000000	000000	000000	000000	37	000000	000000	000000	000000	-----

INSTRUCTION BUFFER 5 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)				00	ACTUAL (IB data)				DIFFERENCE
-----	-----	-----	-----	-----	00	000000	025700	020600	047652	-----
-----	-----	-----	-----	-----	01	000000	006000	146300	000000	-----
-----	-----	-----	-----	-----	02	020000	047723	000000	025077	-----
-----	-----	-----	-----	-----	03	024066	024266	024467	022104	-----
-----	-----	-----	-----	-----	04	035177	022775	030327	031043	-----
-----	-----	-----	-----	-----	05	013000	142770	000000	030012	-----
-----	-----	-----	-----	-----	06	022501	025202	037500	025366	-----
-----	-----	-----	-----	-----	07	025601	024701	127100	000001	-----
-----	-----	-----	-----	-----	10	000000	042700	046017	014000	-----
-----	-----	-----	-----	-----	11	143130	000000	040700	000002	-----
-----	-----	-----	-----	-----	12	000000	024602	022710	040740	-----
-----	-----	-----	-----	-----	13	000042	020000	030567	022705	-----
-----	-----	-----	-----	-----	14	136700	000005	000000	136100	-----
-----	-----	-----	-----	-----	15	000006	000000	116500	000007	-----
-----	-----	-----	-----	-----	16	000000	030676	007000	164330	-----
-----	-----	-----	-----	-----	17	000000	024702	075100	127100	-----

20	000012	000000	074000	015000				
21	143130	000000	040700	170000				
22	000000	024701	044617	040700				
23	010000	000000	046067	127100				
24	000001	000000	015000	143130				
25	000000	130100	075316	000000				
26	120700	075316	000000	042600				
27	046076	015000	143167	000000				
30	042777	024502	040740	000044				
31	020000	020700	163260	000000				
32	022605	135700	000005	000000				
33	115700	000006	000000	030665				
34	007000	044415	000000	051001				
35	014000	143231	000000	042777				
36	024502	040740	000045	020000				
37	020700	143264	000000	022605				

INSTRUCTION BUFFER 6 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
00	025202	025366 025601 020700	
01	044436	000000 025700 020600	
02	043663	000000 006000 146300	
03	000000	020000 043673 000000	
04	025077	024066 024266 024467	
05	022104	035177 030321 031043	
06	013000	044400 000000 025202	
07	025366	025601 024701 127200	
10	000001	000000 120100 076051	
11	000000	051002 014000 044500	
12	000000	055140 055276 023710	
13	071707	061027 120100 076051	
14	000000	017000 044474 000000	
15	054140	055140 023710 071707	
16	061072	016000 044500 000000	
17	042177	006000 044501 000000	
20	043100	024002 022104 025066	
21	034177	005000 000000 000000	
22	025202	025366 025601 020700	
23	044546	000000 025700 020600	
24	043701	000000 006000 146300	
25	000000	020000 043704 000000	
26	025077	024066 024266 024467	
27	022104	035177 030321 031043	
30	013000	044510 000000 025202	
31	025366	025601 024701 127000	
32	000001	000000 014000 044561	
33	000000	042177 006000 044562	
34	000000	043100 024002 022104	
35	025066	034177 005000 000000	
36	030012	022501 025202 025366	
37	037500	025601 020700 044635	

INSTRUCTION BUFFER 7 (Memory IBA unknown)

ADDRESS	EXPECTED (memory)	ACTUAL (IB data)	DIFFERENCE
00	135700	000005 000000 115700	
01	000006	000000 030665 007000	
02	020060	000000 042700 046017	
03	015000	143231 000000 042100	
04	024002	024202 022104 025066	
05	034177	030021 022101 036100	
06	005000	043100 024002 024202	
07	022104	025066 034177 030021	
10	022101	036100 005000 000000	
11	030012	022502 025202 025366	
12	037500	025601 020700 143311	
13	000000	025700 020600 047731	
14	000000	006000 146300 000000	
15	020000	047733 000000 025077	
16	024066	024266 024467 022104	
17	035177	022761 030327 031043	
20	013000	143244 000000 030012	
21	022502	025202 037500 025366	
22	025601	120700 075316 000000	
23	042600	043500 046076 075500	
24	014000	144021 000000 040700	
25	000002	000000 024602 040740	
26	000077	020000 040600 047662	
27	000000	022706 043500 136700	
30	000006	000000 136600 000007	
31	000000	136500 000010 000000	
32	030676	007000 165160 000000	
33	024702	137100 000012 000000	
34	051001	016000 143435 000000	
35	042776	024602 040740 000100	
36	020000	040600 000002 000000	
37	040500	047664 000000 040400	



```

EXCHANGE PACKAGE in the CPU before IB dump
-----
P      00000000751c  A0 000000 003421  IMODES 176665
IBA    00050740000  A1 000000 000005  IFLAGS 000000
ILA    00051130000  A2 000000 107333
DBA    00050740000  A3 000000 107672  *IRP  RPE
DLA    00051130000  A4 000000 113317  *IUM  MEU
      A5 000000 107676  *IFP  FPE
PN 11  XA 0000      A6 000000 040273  *IOR  ORE
CN 15  VL 155      A7 000000 107672  *IPR  PRE
      *FEX  EEX
MODES 16 - *C90  *ESL  *BDM  MM      IBP  BPI
STATS 00 - VNU   FPS   WS    PS      *ICM  MEC
      *IMC  MCU
S0 000000 000000 000000 000000      IRT  RTI
S1 000000 000000 000000 163160      *IIP  ICP
S2 000000 000000 000000 034654      *IIO  IOI
S3 000000 000000 000000 163060      IPC  PCI
S4 000000 000000 000000 163514      *IDL  DL
S5 000000 000000 000000 123456      IMI  MII
S6 036264 032047 115133 123456      *FNX  NEX
S7 000000 000000 000000 123456

```

```

RESTART EXCHANGE PACKAGE after IB dump
-----
P      00000000751c  A0 000000 003421  IMODES 176665
IBA    00050740000  A1 000000 000005  IFLAGS 000000
ILA    00051130000  A2 000000 107333
DBA    00050740000  A3 000000 107672  *IRP  RPE
DLA    00051130000  A4 000000 113317  *IUM  MEU
      A5 000000 107676  *IFP  FPE
PN 11  XA 1640      A6 000000 040273  *IOR  ORE
CN 15  VL 155      A7 000000 107672  *IPR  PRE
      *FEX  EEX
MODES 16 - *C90  *ESL  *BDM  MM      IBP  BPI
STATS 00 - VNU   FPS   WS    PS      *ICM  MEC
      *IMC  MCU
S0 000000 000000 000000 000000      IRT  RTI
S1 000000 000000 000000 163160      *IIP  ICP
S2 000000 000000 000000 034654      *IIO  IOI
S3 000000 000000 000000 163060      IPC  PCI
S4 000000 000000 000000 163514      *IDL  DL
S5 000000 000000 000000 123456      IMI  MII
S6 036264 032047 115133 123456      *FNX  NEX
S7 000000 000000 000000 123456

```

```

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
Register Dump
(V, B, T, SB, ST, SM, VM, and VM1 registers)
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

```

Vector Register 0				Vector Register 1					
V00-000	005254	003064	105465	041503	V01-000	112357	125207	000271	074007
V00-001	011264	007717	131315	047304	V01-001	144250	024423	040275	072621
V00-002	037015	176055	046376	162046	V01-002	051566	160077	055046	101160
V00-003	005070	075333	066233	063203	V01-003	111355	014624	026641	127730
V00-004	072045	155703	007562	044470	V01-004	165472	062227	122030	102661
V00-005	024673	070037	126423	040470	V01-005	162373	136254	000513	164406
V00-006	010730	102465	004645	130460	V01-006	107017	172413	064525	100126
V00-007	054707	042744	046326	110522	V01-007	100542	142462	074656	057362
V00-010	035172	062637	107466	173077	V01-010	027570	066063	034422	006071
V00-011	062555	034536	127724	157740	V01-011	167277	147167	007031	123027
V00-012	003601	056240	145662	052216	V01-012	041567	075672	101463	153736
V00-013	017470	163170	140234	007103	V01-013	143333	034373	172475	145654
V00-014	067573	021425	033243	065600	V01-014	172530	157177	147237	170526
V00-015	026737	163500	034756	036214	V01-015	003552	023163	130066	132521
V00-016	075266	144222	143203	147175	V01-016	047265	023706	103517	104604
V00-017	062665	032057	171613	001735	V01-017	176662	125005	005147	142607
V00-020	035574	054162	154105	054734	V01-020	005144	132533	066010	010540
V00-021	075402	073722	077651	005665	V01-021	061042	057647	105573	003433
V00-022	011077	150500	041457	161213	V01-022	133554	175551	161510	160030
V00-023	070124	155415	171325	061701	V01-023	173330	101707	153705	137501
V00-024	032275	110061	055017	164424	V01-024	156226	013557	051626	063624
V00-025	055031	017322	155073	124325	V01-025	136316	130146	011616	017130
V00-026	046050	156666	153047	130300	V01-026	042312	004637	070632	053450
V00-027	020637	163233	157771	126743	V01-027	105743	027635	114454	123724
V00-030	077620	146427	072611	105223	V01-030	062134	064626	150712	142505
V00-031	013211	074575	145305	001262	V01-031	146635	060537	022241	014243
V00-032	061555	116175	175236	162726	V01-032	126357	054160	125427	122633
V00-033	014135	152256	115224	056205	V01-033	152637	106414	170600	177553
V00-034	041642	016064	127210	005117	V01-034	066154	175346	152013	131140
V00-035	045242	074056	025677	145324	V01-035	105356	055334	035370	124034
V00-036	041627	016604	164645	044235	V01-036	056127	031137	156767	013016
V00-037	075753	040413	061640	151452	V01-037	063263	177774	137100	144454
V00-040	056611	127731	177271	155336	V01-040	104215	022660	073065	057134
V00-041	060375	062652	027654	126543	V01-041	166046	022116	167566	025573
V00-042	046122	165754	156642	164442	V01-042	044736	141054	112202	047524
V00-043	020765	165561	111365	073224	V01-043	141204	017113	013606	063214
V00-044	065114	015265	054220	103405	V01-044	155260	033130	036662	013671
V00-045	007647	013040	156320	071340	V01-045	134056	121272	135054	033730



Vector Register 2				Vector Register 3			
V02-000	065077	110664	151744	065371	V03-000	152177	021551
V02-001	076727	164724	072610	035201	V03-001	175657	151650
V02-002	055476	167612	023052	170726	V03-002	133175	157424
V02-003	065013	027046	075464	012714	V03-003	152026	056114
V02-004	015242	172613	063117	060321	V03-004	032505	165426
V02-005	037015	176055	046376	162046	V03-005	076033	174132
V02-006	043255	160067	041276	135624	V03-006	106533	140156
V02-007	015105	126641	043124	014650	V03-007	032213	055502
V02-010	042616	152664	176066	031475	V03-010	105435	125551
V02-011	054412	164374	047306	023225	V03-011	131025	150770
V02-012	022562	004467	052152	102646	V03-012	045344	011156
V02-013	044767	117430	171315	111240	V03-013	111757	037061
V02-014	033554	051052	172022	041526	V03-014	067330	122125
V02-015	047752	104562	144175	141437	V03-015	117725	011345
V02-016	061732	002573	066500	047620	V03-016	143664	005366
V02-017	063335	154505	036073	133023	V03-017	146673	131212
V02-020	016163	051620	005362	124760	V03-020	034346	123440
V02-021	073030	100135	106326	154105	V03-021	166061	000273
V02-022	067155	133150	153211	052752	V03-022	156333	066321
V02-023	032605	013620	006160	030073	V03-023	065412	027440
V02-024	004637	153251	165526	141502	V03-024	011477	126523
V02-025	052152	032432	125237	075404	V03-025	124324	065065
V02-026	012046	111304	014373	112050	V03-026	024115	022610
V02-027	002543	112456	014505	170110	V03-027	005307	025134
V02-030	063031	032470	175644	115371	V03-030	146062	065161
V02-031	071567	075547	174615	006733	V03-031	163356	173317
V02-032	017470	163170	140234	007103	V03-032	037161	146361
V02-033	072050	035122	103457	106525	V03-033	164120	072245
V02-034	056744	105024	014753	104444	V03-034	135711	012050
V02-035	060144	075043	105771	076314	V03-035	140310	172107
V02-036	046772	136114	033541	171632	V03-036	115765	074230
V02-037	073406	175717	027675	076202	V03-037	167015	173636
V02-040	011146	063110	075515	171344	V03-040	022314	146220
V02-041	064060	106646	030303	072757	V03-041	150141	015514
V02-042	014223	011171	016766	125314	V03-042	030446	022362
V02-043	035254	070204	145660	106074	V03-043	072530	160411
V02-044	030577	145602	155557	175645	V03-044	061377	113405
V02-045	074622	045067	043352	066130	V03-045	171444	112156
V02-046	047402	123671	066640	013522	V03-046	117005	047562
V02-047	002251	150204	171322	035104	V03-047	004523	120411
V02-050	044001	026567	145145	017734	V03-050	110002	055357
V02-051	052776	002122	106167	172723	V03-051	125774	004245
V02-052	077304	052475	060433	126237	V03-052	176610	125172
V02-053	046141	161241	010665	102531	V03-053	114303	142502
V02-054	035527	017147	151432	050040	V03-054	073256	036317
V02-055	045731	131722	002312	171454	V03-055	113663	063644
V02-056	060155	172115	113744	062543	V03-056	140333	164233
V02-057	031743	015576	011466	123160	V03-057	063706	033374
V02-060	014753	135610	136672	172061	V03-060	031727	073421
V02-061	004620	136544	067413	053462	V03-061	011441	075310
V02-062	065231	113135	151516	152416	V03-062	152463	026273
V02-063	007641	045226	061600	141574	V03-063	017502	112454
V02-064	017250	104030	065244	105207	V03-064	036521	010060
V02-065	034015	116515	154520	055123	V03-065	070033	035233
V02-066	006250	073021	036747	014215	V03-066	014520	166042
V02-067	075520	120677	014620	071505	V03-067	173241	041576
V02-070	025313	032463	142540	161631	V03-070	052626	065147
V02-071	050621	022443	133203	140326	V03-071	121442	045107
V02-072	057154	145707	075021	013450	V03-072	136331	113616
V02-073	065067	124611	057475	145457	V03-073	152157	051422
V02-074	072327	013227	137346	124434	V03-074	164656	026457
V02-075	002415	130235	010410	024413	V03-075	005033	060472
V02-076	005552	132446	150110	143517	V03-076	013325	065115
V02-077	063306	076372	043332	056624	V03-077	146614	174764
V02-100	000077	117673	023665	034126	V03-100	000177	037566
V02-101	036576	164354	145734	013423	V03-101	075375	150731
V02-102	076171	005542	057321	114611	V03-102	174362	013304
V02-103	057712	000542	105425	121230	V03-103	137624	001305
V02-104	035460	045712	123331	110002	V03-104	073140	113625
V02-105	071231	050475	030775	046067	V03-105	162462	121172
V02-106	054525	111016	013517	003162	V03-106	131253	022034
V02-107	031153	033172	140512	015636	V03-107	062326	066365
V02-110	054607	122544	027424	122441	V03-110	131417	045310
V02-111	014676	077007	022172	025161	V03-111	031574	176016
V02-112	020175	150242	067324	171431	V03-112	040373	120504
V02-113	017635	034242	136655	060046	V03-113	037472	070505
V02-114	073500	132122	057427	157106	V03-114	167201	064244
V02-115	043271	151542	176424	102220	V03-115	106563	123305
V02-116	021032	014116	037157	057662	V03-116	042064	030234
V02-117	056240	001167	045401	071557	V03-117	134500	002356
V02-120	000436	013515	004306	026725	V03-120	001074	027232
V02-121	062452	147412	055622	132243	V03-121	145125	117024
V02-122	072104	021704	036710	053154	V03-122	164210	043610
V02-123	022421	051637	002256	015526	V03-123	045042	123476
V02-124	056205	107567	160206	024143	V03-124	134413	017357
V02-125	063055	127513	165541	004276	V03-125	146133	057227
V02-126	073103	167253	035776	144525	V03-126	166207	156526
V02-127	004567	047417	054250	047672	V03-127	011356	117036
V02-130	000562	057557	044445	106142	V03-130	001344	137336
V02-131	052347	126521	155732	000534	V03-131	124717	055243
V02-132	002601	131612	023370	173220	V03-132	005403	063424

V02-133 040406 141351 025244 166732  
 V02-134 043111 170017 142543 124245  
 V02-135 022636 146517 034755 172450  
 V02-136 027673 044776 104017 013717  
 V02-137 017604 064062 055412 137566  
 V02-140 015647 164476 171004 106003  
 V02-141 045003 060331 143231 070053  
 V02-142 000050 021715 046134 030447  
 V02-143 054325 166705 045224 057656  
 V02-144 012631 153572 132061 062776  
 V02-145 004400 003554 076246 107716  
 V02-146 016675 000424 121773 117657  
 V02-147 051603 173652 102746 036012  
 V02-150 001254 175576 075331 055506  
 V02-151 045540 155507 076601 074377  
 V02-152 040334 167365 147644 115770  
 V02-153 035416 176060 073305 075476  
 V02-154 002061 134324 045142 014254  
 V02-155 000000 000000 000000 000000  
 V02-156 000000 000000 000000 000000  
 V02-157 000000 000000 000000 000000  
 V02-160 000000 000000 000000 000000  
 V02-161 000000 000000 000000 000000  
 V02-162 000000 000000 000000 000000  
 V02-163 000000 000000 000000 000000  
 V02-164 000000 000000 000000 000000  
 V02-165 000000 000000 000000 000000  
 V02-166 000000 000000 000000 000000  
 V02-167 000000 000000 000000 000000  
 V02-170 000000 000000 000000 000000  
 V02-171 000000 000000 000000 000000  
 V02-172 000000 000000 000000 000000  
 V02-173 000000 000000 000000 000000  
 V02-174 000000 000000 000000 000000  
 V02-175 000000 000000 000000 000000  
 V02-176 000000 000000 000000 000000  
 V02-177 000000 000000 000000 000000

## Vector Register 4

V04-000 025260 014322 026325 006416  
 V04-001 045320 037476 145465 035420  
 V04-002 174067 170265 031773 110232  
 V04-003 024341 165555 131155 115014  
 V04-004 150227 067414 036711 022340  
 V04-005 123355 140176 132115 002340  
 V04-006 043542 012324 023226 142302  
 V04-007 063435 013621 031532 042512  
 V04-010 164751 113176 036333 154374  
 V04-011 112664 162572 137523 077602  
 V04-012 017005 071203 027311 051070  
 V04-013 076343 114743 001160 034414  
 V04-014 136754 106124 155215 127000  
 V04-015 133577 116400 163670 171062  
 V04-016 165333 021113 015017 034764  
 V04-017 113324 150277 147054 007564  
 V04-020 166761 060713 060425 063560  
 V04-021 166011 157511 177244 027326  
 V04-022 044377 042401 006277 105056  
 V04-023 140523 066067 145525 107406  
 V04-024 151366 040305 064077 122122  
 V04-025 064144 075513 064356 121524  
 V04-026 030243 073333 054236 141400  
 V04-027 103177 115157 077746 133614  
 V04-030 177103 032135 153046 025114  
 V04-031 055045 162767 025424 005310  
 V04-032 106666 070767 165173 113530  
 V04-033 060567 051272 065121 071026  
 V04-034 007210 070322 135040 024474  
 V04-035 025211 160270 127377 025520  
 V04-036 007134 073023 123225 021164  
 V04-037 167655 020255 107203 046252  
 V04-040 073046 137547 175347 065570  
 V04-041 101765 113250 137262 132614  
 V04-042 030513 127663 073213 122210  
 V04-043 103727 126706 045725 155120  
 V04-044 124460 065325 061102 016024  
 V04-045 037234 054203 071501 145600  
 V04-046 127743 114176 165637 170354  
 V04-047 100125 111176 011217 067664  
 V04-050 171166 006324 026247 066456  
 V04-051 145036 113157 016330 071362  
 V04-052 051253 044313 124733 044772  
 V04-053 014556 176674 035671 027412  
 V04-054 161301 002636 116705 165400  
 V04-055 015722 123777 105610 167774  
 V04-056 026314 106601 134540 077612  
 V04-057 137201 175425 074563 061424  
 V04-060 062746 064627 112140 156414  
 V04-061 040633 005211 070237 147064  
 V04-062 023753 172216 021111 150466  
 V04-063 067220 052514 003263 003152  
 V04-064 161601 053065 165171 162426

V03-133 101015 102722 052511 155664  
 V03-134 106223 160037 105307 050512  
 V03-135 045475 115236 071733 165120  
 V03-136 057566 111775 010036 027637  
 V03-137 037410 150144 133025 077354  
 V03-140 033517 151175 162011 014007  
 V03-141 112006 140663 106462 160126  
 V03-142 000120 043632 114270 061116  
 V03-143 130653 155612 112450 137535  
 V03-144 025463 127365 064142 145774  
 V03-145 011000 007330 174515 017634  
 V03-146 035572 001051 043767 037537  
 V03-147 123407 167525 005714 074024  
 V03-150 002531 173374 172662 133215  
 V03-151 113301 133216 175402 170777  
 V03-152 100671 156753 117511 033760  
 V03-153 073035 174140 166612 173175  
 V03-154 004143 070650 112304 030531  
 V03-155 000000 000000 000000 000000  
 V03-156 000000 000000 000000 000000  
 V03-157 000000 000000 000000 000000  
 V03-160 000000 000000 000000 000000  
 V03-161 000000 000000 000000 000000  
 V03-162 000000 000000 000000 000000  
 V03-163 000000 000000 000000 000000  
 V03-164 000000 000000 000000 000000  
 V03-165 000000 000000 000000 000000  
 V03-166 000000 000000 000000 000000  
 V03-167 000000 000000 000000 000000  
 V03-170 000000 000000 000000 000000  
 V03-171 000000 000000 000000 000000  
 V03-172 000000 000000 000000 000000  
 V03-173 000000 000000 000000 000000  
 V03-174 000000 000000 000000 000000  
 V03-175 000000 000000 000000 000000  
 V03-176 000000 000000 000000 000000  
 V03-177 000000 000000 000000 000000

## Vector Register 5

V05-000 012530 006151 013152 103207  
 V05-001 022550 017637 062632 116610  
 V05-002 076033 174132 114775 144115  
 V05-003 012160 172666 154466 146406  
 V05-004 164113 133606 017344 111160  
 V05-005 051566 160077 055046 101160  
 V05-006 021661 005152 011513 061141  
 V05-007 131616 105710 114655 021245  
 V05-010 072364 145477 017155 166176  
 V05-011 145332 071275 057651 137701  
 V05-012 007402 134501 113544 124434  
 V05-013 037161 146361 100470 016206  
 V05-014 157366 043052 066506 153400  
 V05-015 055677 147200 071734 074431  
 V05-016 172555 110445 106407 116372  
 V05-017 145552 064137 163426 003672  
 V05-020 073370 130345 130212 131670  
 V05-021 173004 167644 177522 013553  
 V05-022 022177 121200 103137 142427  
 V05-023 160251 133033 162652 143603  
 V05-024 064573 020142 132037 151051  
 V05-025 132062 036645 132167 050652  
 V05-026 114121 135555 126117 060600  
 V05-027 041477 146467 137763 055706  
 V05-030 177441 115056 165423 012446  
 V05-031 026422 171373 112612 002544  
 V05-032 143333 034373 172475 145654  
 V05-033 030273 124535 032450 134413  
 V05-034 103504 034151 056420 012236  
 V05-035 112504 170134 053577 112650  
 V05-036 103456 035411 151512 110472  
 V05-037 173726 101026 143501 123125  
 V05-040 135423 057663 176563 132674  
 V05-041 140772 145524 057531 055306  
 V05-042 114245 153731 135505 151104  
 V05-043 041753 153343 022752 166450  
 V05-044 152230 032552 130441 007012  
 V05-045 017516 026101 134640 162700  
 V05-046 053761 146077 072717 174166  
 V05-047 040052 144477 004507 133732  
 V05-050 074473 003152 013123 133227  
 V05-051 062417 045467 107154 034571  
 V05-052 024525 122145 152355 122375  
 V05-053 106267 077336 016734 113605  
 V05-054 170540 101317 047342 172600  
 V05-055 106751 051777 142704 073776  
 V05-056 113146 043300 156260 037705  
 V05-057 157500 176612 136271 130612  
 V05-060 131363 032313 145060 067206  
 V05-061 120315 102504 134117 163432  
 V05-062 011765 175107 010444 164233  
 V05-063 133510 025246 001531 101465  
 V05-064 170700 125432 172474 171213





V06-153	056402	156343	046647	141371	V07-153	023351	067052	023210	017010
V06-154	172625	157265	056640	044464	V07-154	155472	167411	043516	170470
V06-155	000000	000000	000000	000000	V07-155	060551	066057	067563	072145
V06-156	000000	000000	000000	000000	V07-156	071000	050123	030475	071556
V06-157	000000	000000	000000	000000	V07-157	032060	031067	022040	000120
V06-160	000000	000000	000000	000000	V07-160	051111	047124	042522	036555
V06-161	000000	000000	000000	000000	V07-161	073563	061564	030400	050123
V06-162	000000	000000	000000	000000	V07-162	031075	037056	027150	072550
V06-163	000000	000000	000000	000000	V07-163	037440	020000	046120	050075
V06-164	000000	000000	000000	000000	V07-164	034460	000104	044523	050114
V06-165	000000	000000	000000	000000	V07-165	040531	036472	030056	030000
V06-166	000000	000000	000000	000000	V07-166	047520	042516	053511	047110
V06-167	000000	000000	000000	000000	V07-167	047515	042475	027565	071562
V06-170	000000	000000	000000	000000	V07-170	027557	070145	067167	064556
V06-171	000000	000000	000000	000000	V07-171	000124	042522	046475	074164
V06-172	000000	000000	000000	000000	V07-172	062562	066563	000120	053504
V06-173	000000	000000	000000	000000	V07-173	036457	064157	066545	027556
V06-174	000000	000000	000000	000000	V07-174	067461	071565	067057	067563
V06-175	000000	000000	000000	000000	V07-175	072145	071057	071555	067556
V06-176	000000	000000	000000	000000	V07-176	027542	064556	000124	055075
V06-177	000000	000000	000000	000000	V07-177	041523	052066	041504	052000

B Registers					T Registers				
B00	000000	004042			T00	000000	000000	000000	163514
B01	000000	040273			T01	000000	000000	000000	163060
B02	000000	107672			T02	000000	000000	000000	134754
B03	000000	107340			T03	000000	000000	000000	000000
B04	000000	000000			T04	000000	000000	000000	000000
B05	000000	000000			T05	000000	000000	000000	000000
B06	000000	000000			T06	000000	000000	000000	000000
B07	000000	000000			T07	000000	000000	000000	000000
B10	000000	000000			T10	000000	000000	000000	000000
B11	000000	000000			T11	000000	000000	000000	000000
B12	000000	000000			T12	000000	000000	000000	000000
B13	000000	000000			T13	000000	000000	000000	000000
B14	000000	000000			T14	000000	000000	000000	000000
B15	000000	000000			T15	000000	000000	000000	000000
B16	000000	000000			T16	000000	000000	000000	000000
B17	000000	000000			T17	000000	000000	000000	000000
B20	000000	000000			T20	000000	000000	000000	000000
B21	000000	000000			T21	000000	000000	000000	000000
B22	000000	000000			T22	000000	000000	000000	000000
B23	000000	000000			T23	000000	000000	000000	000000
B24	000000	000000			T24	000000	000000	000000	000000
B25	000000	000000			T25	000000	000000	000000	000000
B26	000000	000000			T26	000000	000000	000000	000000
B27	000000	000000			T27	000000	000000	000000	000000
B30	000000	000000			T30	000000	000000	000000	000000
B31	000000	000000			T31	000000	000000	000000	000000
B32	000000	000000			T32	000000	000000	000000	000000
B33	000000	000000			T33	000000	000000	000000	000000
B34	000000	000000			T34	000000	000000	000000	000000
B35	000000	000000			T35	000000	000000	000000	000000
B36	000000	000000			T36	000000	000000	000000	000000
B37	000000	000000			T37	000000	000000	000000	000000
B40	000000	000000			T40	000000	000000	000000	000000
B41	000000	000000			T41	000000	000000	000000	000000
B42	000000	000000			T42	000000	000000	000000	000000
B43	000000	000000			T43	000000	000000	000000	000000
B44	000000	000000			T44	000000	000000	000000	000000
B45	000000	000000			T45	000000	000000	000000	000000
B46	000000	000000			T46	000000	000000	000000	000000
B47	000000	000000			T47	000000	000000	000000	000000
B50	000000	000000			T50	000000	000000	000000	000000
B51	000000	000000			T51	000000	000000	000000	000000
B52	000000	000000			T52	000000	000000	000000	000000
B53	000000	000000			T53	000000	000000	000000	000000
B54	000000	000000			T54	000000	000000	000000	000000
B55	000000	000000			T55	000000	000000	000000	000000
B56	000000	000000			T56	000000	000000	000000	000000
B57	000000	000000			T57	000000	000000	000000	000000
B60	000000	000000			T60	000000	000000	000000	000000
B61	000000	000000			T61	000000	000000	000000	000000
B62	000000	000000			T62	000000	000000	000000	000000
B63	000000	000000			T63	000000	000000	000000	000000
B64	000000	000000			T64	000000	000000	000000	000000
B65	000000	000000			T65	000000	000000	000000	000000
B66	000000	107676			T66	000000	000000	000000	000000
B67	000000	113317			T67	000000	000000	000000	000000
B70	000000	000000			T70	000211	137634	071171	012215
B71	000000	165335			T71	000000	000000	000000	165553
B72	000000	000001			T72	000000	000000	000000	000000
B73	000000	076065			T73	000000	000000	000000	000000
B74	000000	000000			T74	000000	000000	000000	000000
B75	000000	076065			T75	000000	000000	000000	000000
B76	000000	002777			T76	000000	000000	000000	000000
B77	000000	040264			T77	000000	000000	000000	000000

SHARED REGISTERS for Cluster 15

Shared B Registers				Shared T Registers			
SB00	000000	000000		ST00	000000	000000	000000 000000
SB01	000000	000000		ST01	000000	000000	000000 000000
SB02	000000	000000		ST02	000000	000000	000000 000000
SB03	000000	000000		ST03	000000	000000	000000 000000
SB04	000000	000000		ST04	000000	000000	000000 000000
SB05	000000	000000		ST05	000000	000000	000000 000000
SB06	000000	000000		ST06	000000	000000	000000 000000
SB07	000000	000000		ST07	000000	000000	000000 000000
SM	000002	000000					
VM	000000	000000	000000 000000				
VM1	000000	000000	000000 000000				

#####  
A-register Memory Dump  
#####

Memory from A0 (A0 + DBA = 0050743421)

```

-----
0050743421  013320 000000 024601 024702
0050743422  024002 022504 034577 024101
0050743423  043101 043201 025101 005000
0050743424  030012 022502 037500 025202
0050743425  025366 025601 020700 016167
0050743426  000000 025700 020600 042656
0050743427  000000 006000 146300 000000
0050743430  020000 042712 000000 025077
0050743431  022104 024066 024266 024467
0050743432  035177 020700 000100 000000
0050743433  030327 031043 013000 016120
0050743434  000000 030012 022502 037500
0050743435  025202 025366 025601 024701
0050743436  127700 000000 000000 127100
0050743437  000002 000000 042640 042577
0050743440  044467 061754 046654 050756
0050743441  051007 016000 016245 000000
0050743442  051001 016000 016241 000000
0050743443  042777 024602 040740 000047
0050743444  020000 040600 000010 000000
0050743445  022706 136700 000006 000000
0050743446  136600 000007 000000 030676
0050743447  075100 007000 165425 000000
0050743450  074100 061001 017000 016600
0050743451  000000 040700 000002 000000
0050743452  024602 040740 000052 020000
0050743453  040600 042667 000000 022706
0050743454  043500 136700 000006 000000
0050743455  136600 000007 000000 136500
0050743456  000010 000000 030676 007000
0050743457  027214 000000 024701 075100
0050743460  127100 000001 000000 074700
0050743461  061007 016000 016424 000000
0050743462  040700 000016 000000 024102
0050743463  040600 000005 000000 071501
0050743464  060457 040640 000055 020000
0050743465  040700 000310 000000 022706
0050743466  042377 131600 000006 000000
0050743467  074600 131600 000007 000000
0050743470  131300 000010 000000 131100
0050743471  000011 000000 131400 000012
0050743472  000000 131700 000013 000000
0050743473  030671 007000 022140 000000
0050743474  040700 000002 000000 024602
0050743475  040740 000056 020000 040600
0050743476  042670 000000 022706 136700
0050743477  000006 000000 136100 000007
0050743500  000000 136600 000010 000000
0050743501  030676 075101 007000 153400
0050743502  000000 051001 074101 015000
0050743503  016427 000000 040100 042671
0050743504  000000 006000 016427 000000
0050743505  040100 042677 000000 024701
0050743506  127700 000001 000000 024102
0050743507  040600 000047 000000 042575
0050743510  071401 060346 040540 000064
0050743511  020000 040600 042704 000000
0050743512  040200 042705 000000 040000
0050743513  042706 000000 040400 000310
0050743514  000000 022606 131500 000006
0050743515  000000 131600 000007 000000
0050743516  131200 000010 000000 131700
0050743517  000011 000000 131000 000012
0050743520  000000 131100 000013 000000
0050743521  131300 000014 000000 131400
0050743522  000015 000000 030661 007000

```



```

0050743523    024260 000000 040700 000002
0050743524    000000 020700 061310 000000
0050743525    022614 040740 000064 020000
0050743526    030576 022706 024602 136700
0050743527    000006 000000 116500 000007
0050743530    000000 136100 000010 000000
0050743531    030676 007000 133131 000000
0050743532    042777 024602 040740 000066
0050743533    020000 022706 136700 000006
0050743534    000000 074700 136700 000007
0050743535    000000 030676 007000 021210
0050743536    000000 020600 042657 000000
0050743537    002706 007000 016634 000000
0050743540    043100 024002 024202 022104
0050743541    025066 034177 030021 022102
0050743542    036100 005000 000000 000000
0050743543    030012 022501 025202 025366
0050743544    037500 025601 020700 016661
0050743545    000000 025700 020600 042726
0050743546    000000 006000 146300 000000
0050743547    020000 042740 000000 025077
0050743550    024066 024266 024467 022105
0050743551    035177 022716 030327 031043
0050743552    013000 016614 000000 030012
0050743553    022501 025202 037500 025366
0050743554    025601 022712 024602 042777
0050743555    040740 000034 020000 030567
0050743556    022706 136700 000006 000000
0050743557    116500 000007 000000 030676
0050743560    025503 007000 141455 000000
0050743561    040700 000002 000000 024503
0050743562    024602 040740 000035 020000
0050743563    040600 000006 000000 022706
0050743564    136700 000006 000000 116500
0050743565    000007 000000 136600 000010
0050743566    000000 030676 007000 141665
0050743567    000000 022713 024602 042776
0050743570    040740 000036 020000 040600
0050743571    000006 000000 030567 022706
0050743572    022400 136700 000006 000000
0050743573    136600 000007 000000 116400
0050743574    000010 000000 116500 000011
0050743575    000000 030676 007000 166310
0050743576    000000 042776 024403 024502
0050743577    040740 000037 020000 040600
0050743600    000002 000000 022706 022600
0050743601    135700 000006 000000 135600
0050743602    000007 000000 115400 000010
0050743603    000000 115600 000011 000000
0050743604    030675 007000 167340 000000
0050743605    024702 127200 000013 000000
0050743606    043100 046021 014000 017067
0050743607    000000 042777 046027 014000
0050743610    017067 000000 042777 024602
0050743611    040740 000044 020000 040600
0050743612    000006 000000 022706 136700
0050743613    000006 000000 136600 000007
0050743614    000000 030676 075100 007000
0050743615    165425 000000 074100 120700
0050743616    042736 000000 042677 060576
0050743617    046056 130500 042736 000000
0050743620    015000 017113 000000 020600
    
```

Memory from A1 (A1 + DBA = 0050740005)

```

-----
0050740005    000000 000000 000000 000000
0050740006    000000 000000 000000 000000
0050740007    000000 000000 000000 000000
0050740010    072100 075170 075771 025171
0050740011    043200 073202 020200 000000
0050740012    000000 030002 010000 000060
0050740013    000000 007000 163070 000000
0050740014    120000 037451 000000 020100
0050740015    076030 000000 121200 000022
0050740016    000000 121300 000011 000000
0050740017    023220 015000 000124 000000
0050740020    030002 010000 000115 000000
0050740021    006000 000711 000000 020100
0050740022    076030 000000 121300 000011
0050740023    000000 051003 014000 000124
0050740024    000000 006000 001410 000000
0050740025    024671 126400 000000 000000
0050740026    126500 000001 000000 126600
0050740027    000002 000000 074771 020100
0050740030    055127 000000 131700 000000
0050740031    000000 020100 055130 000000
0050740032    131700 000000 000000 020100
0050740033    055131 000000 131700 000000
0050740034    000000 074170 130100 037455
0050740035    000000 071306 061373 130400
0050740036    037451 000000 130500 037452
0050740037    000000 130600 037453 000000
    
```

0050740040	020100	076030	000000	121400
0050740041	000017	000000	121500	000016
0050740042	000000	121700	000020	000000
0050740043	020200	037463	000000	020300
0050740044	101274	000000	112300	000210
0050740045	000000	020400	103730	000000
0050740046	113400	002432	000000	121600
0050740047	000023	000000	023770	055740
0050740050	071207	020300	037457	000000
0050740051	133700	000000	000000	133200
0050740052	000001	000000	023440	023550
0050740053	031365	055440	071505	071204
0050740054	030004	010000	000265	000000
0050740055	071403	020300	104030	000000
0050740056	133500	000000	000000	133400
0050740057	000001	000000	133200	000002
0050740060	000000	023760	055640	071707
0050740061	051007	014000	000327	000000
0050740062	061063	016000	000322	000000
0050740063	006000	000650	000000	006000
0050740064	000327	000000	051406	051507
0050740065	006000	000427	000000	061043
0050740066	016000	000402	000000	030004
0050740067	011000	000377	000000	040100
0050740070	000002	000000	130100	040147
0050740071	000000	040100	040123	000000
0050740072	130100	040150	000000	040100
0050740073	000155	000000	130100	040151
0050740074	000000	040000	000004	000000
0050740075	040100	040147	000000	003616
0050740076	003614	004000	003716	000000
0050740077	006000	000402	000000	006000
0050740100	000650	000000	030004	011000
0050740101	000407	000000	061443	051603
0050740102	054640	051665	131600	000023
0050740103	000000	020300	104030	000000
0050740104	060753	133700	000000	000000
0050740105	133400	000001	000000	071606
0050740106	030306	023450	023530	022601
0050740107	042271	071705	044227	051002
0050740110	020200	000200	000000	014000
0050740111	000447	000000	023220	030003
0050740112	002002	176606	031552	030004
0050740113	030332	177066	030005	030442
0050740114	020200	000200	000000	010000
0050740115	000505	000000	030003	002002
0050740116	176706	031552	030004	030332
0050740117	177076	030005	030442	020200
0050740120	000200	000000	011000	000447
0050740121	000000	023260	023330	022401
0050740122	043200	022600	020500	000200
0050740123	000000	002005	042700	003070
0050740124	003071	146121	031363	042771
0050740125	071203	045272	023620	030760
0050740126	002007	030002	177014	032674
0050740127	030337	020700	000200	000000
0050740130	030226	002007	030003	013000
0050740131	000531	000000	051105	023350
0050740132	123300	000000	000000	123200
0050740133	000001	000000	123500	000002
0050740134	000000	061461	130300	037451
0050740135	000000	061224	133200	000001
0050740136	000000	130200	037452	000000
0050740137	023120	121700	000000	000000
0050740140	061554	133500	000002	000000
0050740141	130500	037453	000000	023250
0050740142	122600	000000	000000	051007
0050740143	043240	014000	000641	000000
0050740144	121100	000001	000000	023670
0050740145	071506	044772	061554	051775
0050740146	131700	000000	000000	030110
0050740147	051001	051701	015000	000620
0050740150	000000	051006	051706	030102
0050740151	043600	015000	000620	000000
0050740152	043000	130000	052645	000000
0050740153	073101	055120	042277	044312
0050740154	130300	055507	000000	055177
0050740155	044312	130300	055511	000000
0050740156	055177	044312	130300	055510
0050740157	000000	020200	037463	000000
0050740160	025202	025266	025267	022000
0050740161	025000	006000	002434	000000
0050740162	000000	121200	000015	000000
0050740163	121300	000012	000000	121500
0050740164	000013	000000	055240	023420
0050740165	031440	023330	023550	030434
0050740166	030553	031550	122100	000000
0050740167	000000	023110	025170	122600
0050740170	000001	000000	023660	025672
0050740171	030660	030662	031746	030007
0050740172	010000	001057	000000	031557
0050740173	030550	030660	071307	025273

```

0050740174    030306 030405 030507 030235
0050740175    071604 071702 031034 061067
0050740176    022601 042671 071705 044667
0050740177    012000 001006 000000 016000
0050740200    001006 000000 031320 030445
0050740201    031440 031600 051006 020200
0050740202    000200 000000 014000 001016
0050740203    000000 023260 030003 002002
0050740204    176606 031552 032226 030004
    
```

Memory from A2 (A2 + DBA = 0051047333)

```

-----
0051047333    000000 000000 000000 040275
0051047334    000000 000000 000000 003566
0051047335    000000 000000 000000 165551
0051047336    000000 000000 000000 107317
0051047337    000000 000000 000000 104417
0051047340    032213 055502 106250 031521
0051047341    122004 060766 032110 034746
0051047342    162373 136254 000513 164406
0051047343    030022 127003 143736 143063
0051047344    045232 122362 053643 052232
0051047345    171662 164567 127653 011504
0051047346    026770 067513 156120 130734
0051047347    056214 150111 040063 047466
0051047350    114553 037006 000533 007261
0051047351    041354 153114 146010 026016
0051047352    107160 052025 151032 002112
0051047353    126357 054160 125427 122633
0051047354    055054 160024 151743 047054
0051047355    036264 032047 115133 123456
0051047356    014041 014037 152442 160605
0051047357    060432 104072 004132 107353
0051047360    050105 141451 130125 076234
0051047361    037507 033441 113237 014753
0051047362    100431 144024 004701 121273
0051047363    057005 161235 121140 161103
0051047364    166751 137420 000474 134074
0051047365    151666 077637 000527 162605
0051047366    152674 170365 134534 057420
0051047367    034546 042727 011122 176613
0051047370    044111 120105 014437 012435
0051047371    130704 036074 012007 031770
0051047372    136514 046537 050316 030414
0051047373    141634 167310 076626 055533
0051047374    016174 011002 053237 067417
0051047375    060177 026746 120661 015414
0051047376    176105 101104 122623 072271
0051047377    044523 040404 160042 062602
0051047400    067243 175472 165444 113626
0051047401    034511 127110 007231 002131
0051047402    160020 070106 003247 103712
0051047403    123376 024007 141002 041654
0051047404    017654 103545 151764 133167
0051047405    100004 055626 050450 161220
0051047406    024705 063220 066550 006267
0051047407    025024 025245 107547 027735
0051047410    130613 173714 174502 172537
0051047411    071300 155437 014726 167106
0051047412    133612 007105 000144 137660
0051047413    010775 100051 124713 132210
0051047414    132340 104564 104461 170400
0051047415    013115 060643 001612 001117
0051047416    062650 023377 010341 164674
0051047417    052152 125754 073660 165715
0051047420    054435 162246 104517 154675
0051047421    163276 051176 015576 037131
0051047422    027406 011373 071703 140325
0051047423    054007 052427 153024 052546
0051047424    076464 011052 022333 054177
0051047425    112472 002544 034306 020170
0051047426    032172 103713 017230 045577
0051047427    030604 174336 122110 152622
0051047430    175162 142700 044541 121237
0051047431    042304 001511 067500 133171
0051047432    034274 020135 117031 174577
0051047433    061447 075670 076544 035677
0051047434    015007 043227 010635 055705
0051047435    101350 056127 144670 105151
0051047436    004170 025203 007030 035422
0051047437    052455 161005 010455 117416
0051047440    021211 034210 021576 111132
0051047441    104767 010077 030514 076620
0051047442    045404 051740 105301 077336
0051047443    041707 111251 076254 040327
0051047444    121123 170276 172427 161163
0051047445    154532 133732 024052 124330
0051047446    172751 160444 123474 104666
0051047447    071251 052375 014112 052072
0051047450    176362 011641 025301 032726
0051047451    051755 104712 147726 177600
0051047452    164164 053541 061707 140524
    
```

```

0051047453 100363 057413 012175 071412
0051047454 056436 050367 117533 120712
0051047455 017111 033775 066141 136507
0051047456 141113 141466 076774 144073
0051047457 035220 130174 051033 052117
0051047460 024752 135236 001125 037067
0051047461 173340 061206 077502 001432
0051047462 152012 046230 171652 167435
0051047463 172731 062722 002734 033657
0051047464 077621 171022 137736 166675
0051047465 161530 177236 016242 004242
0051047466 177753 043415 134661 166457
0051047467 165507 031426 140225 121777
0051047470 174432 053424 105303 020625
0051047471 163776 073611 117154 175617
0051047472 051263 115227 053231 113672
0051047473 130234 132256 135410 142055
0051047474 051165 007210 165747 024315
0051047475 176437 072757 076001 175662
0051047476 022735 130436 024122 171436
0051047477 104642 171727 004433 054616
0051047500 176451 076230 162203 070360
0051047501 164305 160111 071623 055114
0051047502 013671 015742 051264 174122
0051047503 162022 054402 010056 004103
0051047504 172621 116777 034327 122452
0051047505 153577 064777 106460 073074
0051047506 064131 104566 173026 112051
0051047507 072607 117110 014505 012753
0051047510 151573 126731 106303 036325
0051047511 006523 071665 137215 100116
0051047512 031054 020525 074675 114463
0051047513 056402 156343 046647 141371
0051047514 172625 157265 056640 044464
0051047515 112357 125207 000271 074007
0051047516 144250 024423 040275 072621
0051047517 051566 160077 055046 101160
0051047520 111355 014624 026641 127730
0051047521 165472 062227 122030 102661
0051047522 162373 136254 000513 164406
0051047523 107017 172413 064525 100126
0051047524 100542 142462 074656 057362
0051047525 027570 066063 034422 006071
0051047526 167277 147167 007031 123027
0051047527 041567 075672 101463 153736
0051047530 143333 034373 172475 145654
0051047531 172530 157177 147237 170526
0051047532 003552 023163 130066 132521

```

Memory from A3 (A3 + DBA = 0051047672)

```

-----
0051047672 000000 000000 000000 040264
0051047673 000000 000000 000000 004042
0051047674 000000 000000 000000 107327
0051047675 000000 000000 000000 107333
0051047676 000000 000000 000000 000000
0051047677 000000 000000 000000 000000
0051047700 000000 000000 000000 000000
0051047701 000000 000000 000000 000000
0051047702 000000 000000 000000 000000
0051047703 000000 000000 000000 000000
0051047704 000000 000000 000000 000000
0051047705 000000 000000 000000 000000
0051047706 000000 000000 000000 000000
0051047707 000000 000000 000000 000000
0051047710 000000 000000 000000 000000
0051047711 000000 000000 000000 000000
0051047712 000000 000000 000000 000000
0051047713 000000 000000 000000 000000
0051047714 000000 000000 000000 000000
0051047715 000000 000000 000000 000000
0051047716 000000 000000 000000 000000
0051047717 000000 000000 000000 000000
0051047720 000000 000000 000000 000000
0051047721 000000 000000 000000 000000
0051047722 000000 000000 000000 000000
0051047723 000000 000000 000000 000000
0051047724 000000 000000 000000 000000
0051047725 000000 000000 000000 000000
0051047726 000000 000000 000000 000000
0051047727 000000 000000 000000 000000
0051047730 000000 000000 000000 000000
0051047731 000000 000000 000000 000000
0051047732 000000 000000 000000 000000
0051047733 000000 000000 000000 000000
0051047734 000000 000000 000000 000000
0051047735 000000 000000 000000 000000
0051047736 000000 000000 000000 000000
0051047737 000000 000000 000000 000000
0051047740 000000 000000 000000 000000
0051047741 000000 000000 000000 000000
0051047742 000000 000000 000000 000000

```







```

0051047761 000000 000000 000000 000000
0051047762 000000 000000 000000 000000
0051047763 000000 000000 000000 000000
0051047764 000000 000000 000000 000000
0051047765 000000 000000 000000 000000
0051047766 000000 000000 000000 000000
0051047767 000000 000000 000000 000000
0051047770 000000 000000 000000 000000
0051047771 000000 000000 000000 000000
0051047772 000000 000000 000000 000000
0051047773 000000 000000 000000 000000
0051047774 000000 000000 000000 000000
0051047775 000000 000000 000000 000000
0051047776 000000 000000 000000 000000
0051047777 000000 000000 000000 000000
0051050000 000000 000000 000000 000000
0051050001 000000 000000 000000 000000
0051050002 000000 000000 000000 000000
0051050003 000000 000000 000000 000000
0051050004 000000 000000 000000 000000
0051050005 000000 000000 000000 000000
0051050006 000000 000000 000000 000000
0051050007 000000 000000 000000 000000
0051050010 000000 000000 000000 000000
0051050011 000000 000000 000000 000000
0051050012 000000 000000 000000 000000
0051050013 000000 000000 000000 000000
0051050014 000000 000000 000000 000000
0051050015 000000 000000 000000 000000
0051050016 000000 000000 000000 000000
0051050017 000000 000000 000000 000000
0051050020 000000 000000 000000 000000
0051050021 000000 000000 000000 000000
0051050022 000000 000000 000000 000000
0051050023 000000 000000 000000 000000
0051050024 000000 000000 000000 000000
0051050025 000000 000000 000000 000000
0051050026 000000 000000 000000 000000
0051050027 000000 000000 000000 000000
0051050030 000000 000000 000000 000000
0051050031 000000 000000 000000 000000
0051050032 000000 000000 000000 000000
0051050033 000000 000000 000000 000000
0051050034 000000 000000 000000 000000
0051050035 000000 000000 000000 000000
0051050036 000000 000000 000000 000000
0051050037 000000 000000 000000 000000
0051050040 000000 000000 000000 000000
0051050041 000000 000000 000000 000000
0051050042 000000 000000 000000 000000
0051050043 000000 000000 000000 000000
0051050044 000000 000000 000000 000000
0051050045 000000 000000 000000 000000
0051050046 000000 000000 000000 000000
0051050047 000000 000000 000000 000000
0051050050 000000 000000 000000 000000
0051050051 000000 000000 000000 000000
0051050052 000000 000000 000000 000000
0051050053 000000 000000 000000 000000
0051050054 000000 000000 000000 000000
0051050055 000000 000000 000000 000000
0051050056 000000 000000 000000 000000
0051050057 000000 000000 000000 000000
0051050060 000000 000000 000000 000000
0051050061 000000 000000 000000 000000
0051050062 000000 000000 000000 000000
0051050063 000000 000000 000000 000000
0051050064 000000 000000 000000 000000
0051050065 000000 000000 000000 000000
0051050066 000000 000000 000000 000000
0051050067 000000 000000 000000 000000
0051050070 000000 000000 000000 000000
0051050071 000000 000000 000000 000000
0051050072 000000 000000 000000 000000
0051050073 000000 000000 000000 000000
0051050074 000000 000000 000000 000000
0051050075 000000 000000 000000 000000

```

Memory from A6 (A6 + DBA = 0051000273)

```

-----
0051000273 020000 000030 000000 000000
0051000274 066541 064556 000000 000000
0051000275 000000 000000 000005 000004
0051000276 000000 000000 000000 003705
0051000277 004000 000000 000000 000337
0051000300 000000 000000 000000 000011
0051000301 000000 000000 000000 000005
0051000302 000000 000000 000000 000000
0051000303 020100 024043 024554 064542
0051000304 072457 067165 066543 067556
0051000305 073057 061461 027563 062062
0051000306 072546 027163 004470 030056

```



```

0051000307 030011 030064 027461 033057
0051000310 034463 020061 031472 031470
0051000311 035060 032000 000000 000000
0051000312 020003 100000 000000 000001
0051000313 057776 177777 177777 177776
0051000314 040000 105075 070243 153412
0051000315 037777 115040 115204 175720
0051000316 037775 146314 146314 146314
0051000317 146314 146314 146314 146315
0051000320 040000 000000 000000 000000
0051000321 040000 000000 000000 000000
0051000322 040000 000000 000000 000000
0051000323 040000 000000 000000 000000
0051000324 040000 000000 000000 000000
0051000325 040000 000000 000000 000000
0051000326 040000 000000 000000 000000
0051000327 040000 000000 000000 000000
0051000330 057563 062062 072546 020040
0051000331 000000 000000 000005 000006
0051000332 000000 000000 000000 004200
0051000333 000400 000000 000000 000000
0051000334 000000 000000 000000 000000
0051000335 000000 000000 000000 000411
0051000336 000000 000000 000000 000000
0051000337 020100 024043 024554 064542
0051000340 072457 067165 066543 067556
0051000341 073057 061461 027563 062062
0051000342 072544 062545 027163 004470
0051000343 030056 030411 030070 027460
0051000344 031057 034463 020060 034072
0051000345 031466 035061 031000 000000
0051000346 020003 100000 000000 000001
0051000347 057776 177777 177777 177776
0051000350 040000 105075 070243 153412
0051000351 037777 115040 115204 175720
0051000352 037775 146314 146314 146314
0051000353 146314 146314 146314 146315
0051000354 040000 000000 000000 000000
0051000355 040000 000000 000000 000000
0051000356 040000 000000 000000 000000
0051000357 040000 000000 000000 000000
0051000360 040000 000000 000000 000000
0051000361 040000 000000 000000 000000
0051000362 040000 000000 000000 000000
0051000363 040000 000000 000000 000000
0051000364 040000 000000 000000 000000
0051000365 057563 062062 072545 071545
0051000366 000000 000000 000005 000010
0051000367 000000 000000 000000 006000
0051000370 000400 000000 000000 000000
0051000371 000000 000000 000000 000000
0051000372 000000 000000 000000 000411
0051000373 000000 000000 000000 000000
0051000374 057563 062062 072544 062545
0051000375 000000 000000 000005 000010
0051000376 000000 000000 000000 006043
0051000377 000400 000000 000000 000000
0051000400 000000 000000 000000 000000
0051000401 000000 000000 000000 000411
0051000402 000000 000000 000000 000000
0051000403 000000 000000 000000 000031
0051000404 000000 000000 000000 000000
0051000405 000000 000000 000000 000000
0051000406 000000 000000 000000 000000
0051000407 000000 000000 000000 000000
0051000410 000000 000000 000000 000000
0051000411 000000 000000 000000 000000
0051000412 000000 000000 000000 000000
0051000413 000000 000000 000000 000000
0051000414 000000 000000 000000 000000
0051000415 000000 000000 000000 000000
0051000416 000000 000000 000000 000000
0051000417 000000 000000 000000 000000
0051000420 000000 000000 000000 000000
0051000421 000000 000000 000000 000000
0051000422 000000 000000 000000 000000
0051000423 000000 000000 000000 000000
0051000424 000000 000000 000000 000000
0051000425 000000 000000 000000 000000
0051000426 000000 000000 000000 000000
0051000427 000000 000000 000000 000000
0051000430 000000 000000 000000 000000
0051000431 020100 024043 024554 064542
0051000432 072457 072162 061153 027543
0051000433 030457 072162 061153 027163
0051000434 004470 030056 030411 030462
0051000435 027460 030457 034464 020061
0051000436 032072 032065 035061 031000
0051000437 005040 041145 063551 067156
0051000440 064556 063440 067546 020124
0051000441 071141 061545 061141 061553
0051000442 035012 000000 000000 000000
    
```

```

0051000443 020040 041541 066154 062544
0051000444 020146 071157 066440 005000
0051000445 020040 051564 060562 072145
0051000446 062040 063162 067555 020012
0051000447 020105 067144 020157 063040
0051000450 052162 060543 062542 060543
0051000451 065456 005000 000000 000000
0051000452 020124 071141 061545 061141
0051000453 061553 020141 061157 071164
0051000454 062544 035440 070157 071563
0051000455 064542 066145 020163 072141
0051000456 061553 020143 067562 071165
0051000457 070164 064557 067056 005000
0051000460 020124 071141 061545 061141
0051000461 061553 020164 062562 066551
0051000462 067141 072145 062073 020155
0051000463 060570 064555 072555 020144
0051000464 062560 072150 020145 074143
0051000465 062545 062145 062056 005000
0051000466 060544 062162 062563 071440
0051000467 066151 067145 020000 000000
0051000470 020151 067040 071157 072564
0051000471 064556 062440 023400 000000
0051000472 000000 014631 114631 114632

```

Memory from A7 (A7 + DBA = 0051047672)

```

-----
0051047672 000000 000000 000000 040264
0051047673 000000 000000 000000 004042
0051047674 000000 000000 000000 107327
0051047675 000000 000000 000000 107333
0051047676 000000 000000 000000 000000
0051047677 000000 000000 000000 000000
0051047700 000000 000000 000000 000000
0051047701 000000 000000 000000 000000
0051047702 000000 000000 000000 000000
0051047703 000000 000000 000000 000000
0051047704 000000 000000 000000 000000
0051047705 000000 000000 000000 000000
0051047706 000000 000000 000000 000000
0051047707 000000 000000 000000 000000
0051047710 000000 000000 000000 000000
0051047711 000000 000000 000000 000000
0051047712 000000 000000 000000 000000
0051047713 000000 000000 000000 000000
0051047714 000000 000000 000000 000000
0051047715 000000 000000 000000 000000
0051047716 000000 000000 000000 000000
0051047717 000000 000000 000000 000000
0051047720 000000 000000 000000 000000
0051047721 000000 000000 000000 000000
0051047722 000000 000000 000000 000000
0051047723 000000 000000 000000 000000
0051047724 000000 000000 000000 000000
0051047725 000000 000000 000000 000000
0051047726 000000 000000 000000 000000
0051047727 000000 000000 000000 000000
0051047730 000000 000000 000000 000000
0051047731 000000 000000 000000 000000
0051047732 000000 000000 000000 000000
0051047733 000000 000000 000000 000000
0051047734 000000 000000 000000 000000
0051047735 000000 000000 000000 000000
0051047736 000000 000000 000000 000000
0051047737 000000 000000 000000 000000
0051047740 000000 000000 000000 000000
0051047741 000000 000000 000000 000000
0051047742 000000 000000 000000 000000
0051047743 000000 000000 000000 000000
0051047744 000000 000000 000000 000000
0051047745 000000 000000 000000 000000
0051047746 000000 000000 000000 000000
0051047747 000000 000000 000000 000000
0051047750 000000 000000 000000 000000
0051047751 000000 000000 000000 000000
0051047752 000000 000000 000000 000000
0051047753 000000 000000 000000 000000
0051047754 000000 000000 000000 000000
0051047755 000000 000000 000000 000000
0051047756 000000 000000 000000 000000
0051047757 000000 000000 000000 000000
0051047760 000000 000000 000000 000000
0051047761 000000 000000 000000 000000
0051047762 000000 000000 000000 000000
0051047763 000000 000000 000000 000000
0051047764 000000 000000 000000 000000
0051047765 000000 000000 000000 000000
0051047766 000000 000000 000000 000000
0051047767 000000 000000 000000 000000
0051047770 000000 000000 000000 000000
0051047771 000000 000000 000000 000000
0051047772 000000 000000 000000 000000

```

```

0051047773 000000 000000 000000 000000
0051047774 000000 000000 000000 000000
0051047775 000000 000000 000000 000000
0051047776 000000 000000 000000 000000
0051047777 000000 000000 000000 000000
0051050000 000000 000000 000000 000000
0051050001 000000 000000 000000 000000
0051050002 000000 000000 000000 000000
0051050003 000000 000000 000000 000000
0051050004 000000 000000 000000 000000
0051050005 000000 000000 000000 000000
0051050006 000000 000000 000000 000000
0051050007 000000 000000 000000 000000
0051050010 000000 000000 000000 000000
0051050011 000000 000000 000000 000000
0051050012 000000 000000 000000 000000
0051050013 000000 000000 000000 000000
0051050014 000000 000000 000000 000000
0051050015 000000 000000 000000 000000
0051050016 000000 000000 000000 000000
0051050017 000000 000000 000000 000000
0051050020 000000 000000 000000 000000
0051050021 000000 000000 000000 000000
0051050022 000000 000000 000000 000000
0051050023 000000 000000 000000 000000
0051050024 000000 000000 000000 000000
0051050025 000000 000000 000000 000000
0051050026 000000 000000 000000 000000
0051050027 000000 000000 000000 000000
0051050030 000000 000000 000000 000000
0051050031 000000 000000 000000 000000
0051050032 000000 000000 000000 000000
0051050033 000000 000000 000000 000000
0051050034 000000 000000 000000 000000
0051050035 000000 000000 000000 000000
0051050036 000000 000000 000000 000000
0051050037 000000 000000 000000 000000
0051050040 000000 000000 000000 000000
0051050041 000000 000000 000000 000000
0051050042 000000 000000 000000 000000
0051050043 000000 000000 000000 000000
0051050044 000000 000000 000000 000000
0051050045 000000 000000 000000 000000
0051050046 000000 000000 000000 000000
0051050047 000000 000000 000000 000000
0051050050 000000 000000 000000 000000
0051050051 000000 000000 000000 000000
0051050052 000000 000000 000000 000000
0051050053 000000 000000 000000 000000
0051050054 000000 000000 000000 000000
0051050055 000000 000000 000000 000000
0051050056 000000 000000 000000 000000
0051050057 000000 000000 000000 000000
0051050060 000000 000000 000000 000000
0051050061 000000 000000 000000 000000
0051050062 000000 000000 000000 000000
0051050063 000000 000000 000000 000000
0051050064 000000 000000 000000 000000
0051050065 000000 000000 000000 000000
0051050066 000000 000000 000000 000000
0051050067 000000 000000 000000 000000
0051050070 000000 000000 000000 000000
0051050071 000000 000000 000000 000000

```

```

@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@
Test Point Dump
@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@@

```

TP->	0	1	2	3	4	5	6	7
OPT	01234567	01234567	01234567	01234567	01234567	01234567	01234567	01234567
YE0	01001011	11000111	11010111	01010111	11011111	01011111	00000001	00000001
YF0	11101111	00010110	01101010	11100101	11010000	00010110	01101010	11000011
YF1	11101111	00010110	01101010	11100101	11010000	00010110	01101010	11000011
YF2	11101111	00010110	01101010	11100101	11010000	00010110	01101010	11000011
YG0	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000001
YH0	11111111	11111111	11111111	11110000	11000000	11111111	11111111	11111111
YJ0	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
YJ1	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
YM0	11111111	11111111	11111111	11111111	00000001	00000001	11111111	11111111
YM1	11111111	11111111	11111111	11111111	00000001	00000001	11111111	11111111
YK0	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000001
YK1	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000001
HA0	00001000	00110000	00001010	00000000	00001000	00110000	00001010	00000001
HCO	00000000	00000000	10101101	10111111	00001011	10110110	01011100	01110001
JA0	11111111	11111111	00000000	00000000	11110000	00000110	00000000	00000001
JB0	00000000	00000000	00000000	00000000	00000000	00000000	00000010	00000001
JCO	00000000	00000000	00100000	00010010	00100000	01101000	00000000	00000001
JQ0	11110011	11111111	11100101	10101100	11111111	11111111	10111111	11111111
VQ0	00000000	00000000	00000000	00000000	11111111	11111111	11110000	00000001

```
VQ1 00000000 00000000 00000000 00000000 11111111 11111111 11110000 00000001
DA0 10000000 11010111 10000000 00000001 00000001 10101111 00000000 00000011
DC0 00000000 10010011 00000000 00100011 01100000 01000010 00000000 00000001
DD0 00001010 00000011 11001000 00001010 00000011 11001000 11111111 11111111
DD1 10000000 00000000 00000010 00000000 00000000 00001111 11111111 11111111
JQ1 10100101 10110001 10001111 11001100 10111100 00001101 00011110 10011011
SIE 11111111 11111111 11111111 11111111 11111111 11111111 11111111 11111111
```

```
****
**** the 'bin/.smon_trigger_cmd' script could not be executed
****
```

```
*****
System Monitor Dump completed at: Mon Feb 13 13:52:29 1995
*****
```